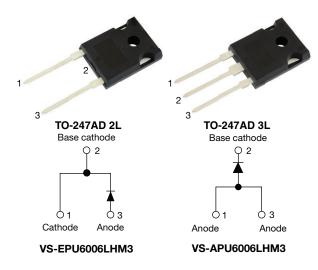
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# VS-EPU6006LHN3, VS-APU6006LHN3

Vishay Semiconductors

# Ultrafast Soft Recovery Diode, 60 A FRED Pt®



PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	60 A							
V <sub>R</sub>	600 V							
V <sub>F</sub> at I <sub>F</sub>	1.05 V							
t <sub>rr</sub> typ.	32 ns							
T <sub>J</sub> max.	175 °C							
Package	TO-247AD 2L, TO-247AD 3L							
Circuit configuration	Single							

### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- AEC-Q101 qualified meets JESD 201 class 1 whisker test



HALOGEN

FREE

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

VS-EPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, welding, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V						
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 116 °C	60	А						
Single pulse forward current	I <sub>FSM</sub>	$T_C$ = 25 °C, $t_p$ = 8.3 ms; half sine wave	600	~						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER SYMBOL TEST CONDITIONS MIN. TYP. MAX.										
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-					
Forward voltage		I <sub>F</sub> = 60 A	-	1.2	1.5	V				
	V <sub>F</sub>	I <sub>F</sub> = 60 A, T <sub>J</sub> = 125 °C	-	1.1	1.3					
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 175 °C	-	1.05	1.2					
Reverse leakage current		$V_{R} = V_{R}$ rated	-	0.2	30					
	I <sub>R</sub>	$T_J = 150 \ ^{\circ}C, V_R = V_R \text{ rated}$	150 °C, $V_{\rm R} = V_{\rm R}$ rated 2		200	μA				
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	38	-	pF				

 Revision: 10-Oct-2018
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 Document Number: 95957

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
		$I_F = 1 \text{ A}, \text{ di}_F/\text{dt} = 20$	$I_F = 1 \text{ A}, \text{ di}_F/\text{dt} = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		32	-			
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	110	-	ns A		
		T <sub>J</sub> = 125 °C		-	200	-			
Deal and a second	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_F = 60 A$	-	10	-			
Peak recovery current		T <sub>J</sub> = 125 °C	di <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 200 V	-	19	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	530	-			
		T <sub>J</sub> = 125 °C		-	1900	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C				
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.65					
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W				
Thermal resistance, case to heat sink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-					
Maight			-	6	-	g				
Weight			-	0.21	-	oz.				
Mounting torque			6 (5)	-	1.2 (10)	kgf. cm (lbf ⋅ in)				
Marking davias		Case style: TO-247AD 2L		EPU6	006LH					
Marking device		Case style: TO-247AD 3L		APU6	006LH					



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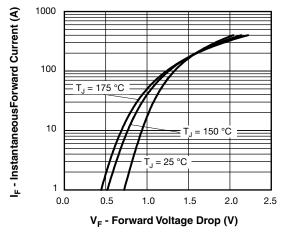


Fig. 1 - Typical Forward Voltage Drop Characteristics

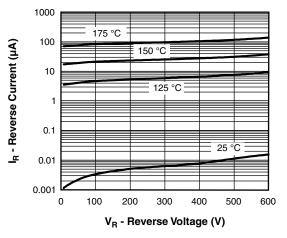


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

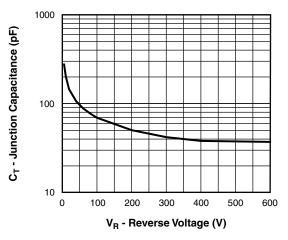
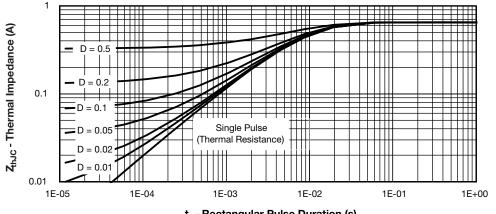


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



t<sub>1</sub> - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

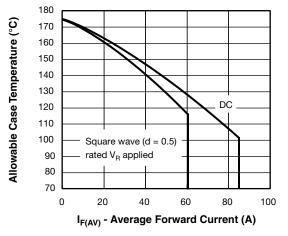
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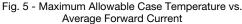
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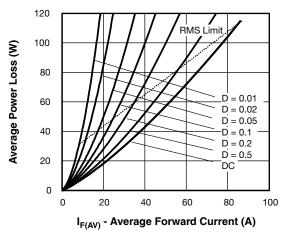


Fig. 6 - Forward Power Loss Characteristics

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ; Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV}$  = inverse power loss =  $V_{B1} \times I_{R} (1 - D)$ ;  $I_{R}$  at  $V_{B1}$  = 80 % rated  $V_{R}$ 

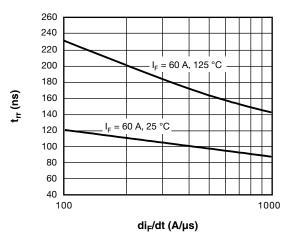


Fig. 7 - Typical Reverse Recovery Time vs. di<sub>F</sub>/dt

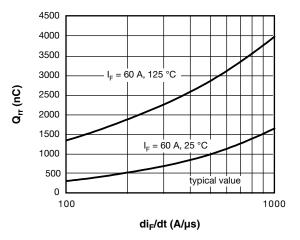
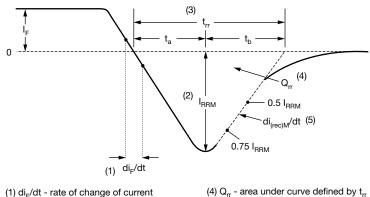


Fig. 8 - Typical Stored Charge vs. di<sub>F</sub>/dt



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- through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.

(4)  ${\rm Q}_{\rm rr}$  - area under curve defined by  ${\rm t}_{\rm rr}$  and  ${\rm I}_{\rm RRM}$ 

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_{b}$  portion of  $t_{rr}$ 

Fig. 9 - Reverse Recovery Waveform and Definitions

### **ORDERING INFORMATION TABLE**

Device code	VS-	Е	Р	U	60	06	L	Н	N3
	1	2	3	4	5	6	7	8	9
	1 -	Visł	nay Sem	niconduc	tors pro	oduct			
	2 - Circuit configuration:								
	<ul> <li>E = single diode 2 pins</li> <li>A = single diode 3 pins</li> </ul>								
	3 -	P =	TO-247						
	4 -	U =	ultrafas	t recove	ry time				
	5 -	Cur	rent cod	le (60 =	60 A)				
	6 -	Volt	tage coo	le (06 =	600 V)				
	7 -	L =	long lea	d					
	8 -	H =	AEC-Q	101 qua	lified				
	9 -	Env	ironmer	tal digit					
		N3 :	= haloge	en-free,	RoHS-c	ompliar	nt, and t	otally le	ad (Pb)

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-EPU6006LHN3	25	500	Antistatic plastic tube						
VS-APU6006LHN3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS							
Dimensions	TO-247AD 2L	www.vishay.com/doc?95536					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626					
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648					
	TO-247AD 3L	www.vishay.com/doc?95007					

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**TO-247AD 2L** 

### **DIMENSIONS** in millimeters and inches



Section C - C, D - D

(b. b2)

(4)

/	$\square$
	C C
Vie	<u>w B</u>

SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL		IETERS	INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NUTES
А	4.65	5.31	0.183	0.209		Е	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102		E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098		е	5.46	BSC	0.215	BSC	
b	0.99	1.40	0.039	0.055		ØК	0.2	254	0.0	)10	
b1	0.99	1.35	0.039	0.053		L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094		L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092		ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035		Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4	S	5.51	BSC	0.217	BSC	
D2	0.51	1.35	0.020	0.053							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

<sup>(2)</sup> Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

(7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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