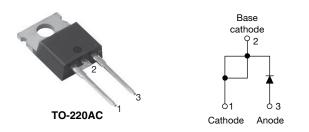
Vishay Semiconductors

High Performance Schottky Rectifier, 10 A



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SHA

PRIMARY CHARACTE	RISTICS
I _{F(AV)}	10 A
V _R	35 V, 40 V, 45 V
V _F at I _F	0.49 V
I _{RM}	15 mA at 125 °C
T _J max.	175 °C
E _{AS}	13 mJ
Package	TO-220AC
Circuit configuration	Single

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



COMPLIANT

HALOGEN

- strength and moisture resistanceGuard ring for enhanced ruggedness and long term reliability
- Meet JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	10	А					
V _{RRM}		35/45	V					
I _{FSM}	$t_p = 5 \ \mu s \ sine$	1050	А					
V _F	10 A _{pk} , T _J = 125 °C	0.49	V					
TJ	Range	-55 to +175	°C					

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-10TQ035HN3	VS-10TQ040HN3	VS-10TQ045HN3	UNITS				
Maximum DC reverse voltage	V _R	35	40	45	V				
Maximum working peak reverse voltage	V _{RWM}		40	45					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 151 °C,	10						
Maximum peak one cycle non-repetitive surge current	I =0.1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1050	А				
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	280					
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 6.5 mł	13	mJ					
Repetitive avalanche current	I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical						



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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
Maximum forward voltage drop See fig. 1		10 A	T ₁ = 25 °C	0.57					
	V _{FM} ⁽¹⁾	20 A	1j=25 0	0.67	V				
	V FM V	10 A	T, = 125 °C	0.49					
		20 A	$1_{\rm J} = 125$ C	0.61					
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	2	mA				
See fig. 2	IRM (")	T _J = 125 °C	V _R = naleu V _R	15					
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF				
Typical series inductance	L _S	Measured lead to lead 5	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

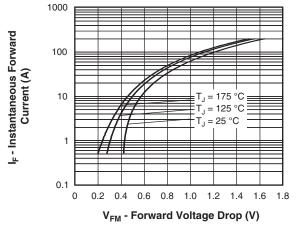
Note

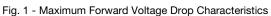
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - ME	THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	PARAMETER		TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C					
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	2.0	°C/W					
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50						
Approximate weight	A second s			2	g					
Approximate weight				0.07	oz.					
Mounting torque	minimum			6 (5)	kgf. cm					
Mounting torque maximum				12 (10)	(lbf. in)					
	Marking device					10TQ035H				
Marking device			Case style TO-220AC	10TQ	040H					
				10TQ045H						



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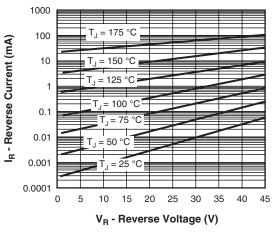


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

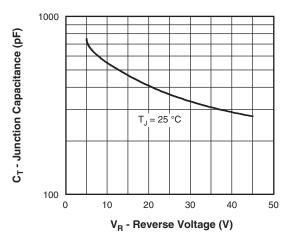


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

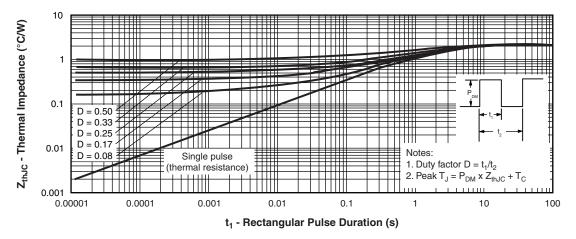
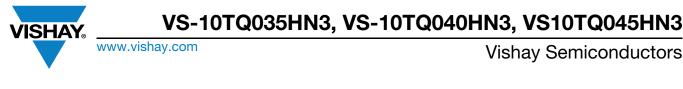


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

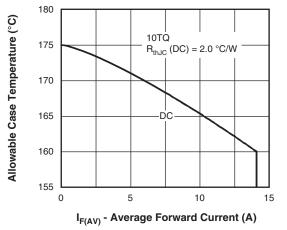
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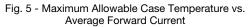
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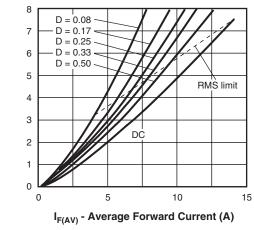
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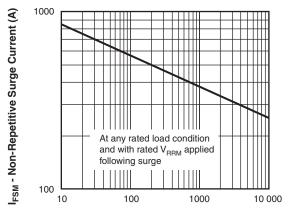
Average Power Loss (W)



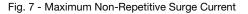








t_p - Square Wave Pulse Duration (μs)



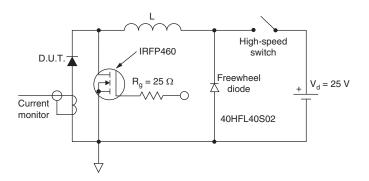


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE

Device code	VS-	10	Т	Q	045	Н	N3
		2	3	4	5	6	7
	1 - 2 - 3 - 4 - 5 - 6 - 7 -	Cur Pac T = Sch Volt H =	rent rati kage: TO-220 ottky "C age rati AEC-Q ironmer	≀" series ngs — 101 qua ntal digit	10 A)		035 = 35 040 = 40 045 = 45 liant, and

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-10TQ035HN3	50	1000	Antistatic plastic tube						
VS-10TQ040HN3	50	1000	Antistatic plastic tube						
VS-10TQ045HN3	50	1000	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95221
Part marking information	www.vishay.com/doc?95068



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TO-220AC

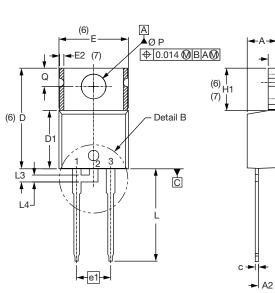
B Seating

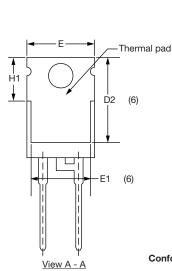
A-

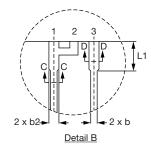
-A1

plane

DIMENSIONS in millimeters and inches









Conforms to JEDEC[®] outline TO-220AC

⊕ 0.015 **()** BA()

SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183		E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055		E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115		e1	4.88	5.28	0.192	0.208	
b	0.69	1.01	0.027	0.040		H1	5.84	6.86	0.230	0.270	6, 7
b1	0.38	0.97	0.015	0.038	4	L	13.52	14.02	0.532	0.552	
b2	1.20	1.73	0.047	0.068		L1	3.32	3.82	0.131	0.150	2
b3	1.14	1.73	0.045	0.068	4	L3	1.78	2.13	0.070	0.084	
с	0.36	0.61	0.014	0.024		L4	0.76	1.27	0.030	0.050	2
c1	0.36	0.56	0.014	0.022	4	ØР	3.54	3.73	0.139	0.147	
D	14.85	15.25	0.585	0.600	3	Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355							
D2	11.68	12.88	0.460	0.507	6						
E	10.11	10.51	0.398	0.414	3, 6						

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

- (4) Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1

⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed

⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline

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