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Vishay Semiconductors

Small Signal Schottky Diode

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DESIGN SUPPORT TOOLS



MECHANICAL DATA

Case: SOD-123 Weight: approx. 10.3 mg Packaging codes/options: 18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

FEATURES

- These diodes feature very low turn-on voltage and fast switching
- These devices are protected by a PN junction guard ring against excessive voltage, such as electrostatic discharges



RoHS

COMPLIANT

- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAT54W	BAT54W-E3-08 or BAT54W-E3-18	Single	1.4	Tape and reel	
	BAT54W-HE3-08 or BAT54W-HE3-18	Single	L4		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		V _{RRM}	30	V	
Forward continuous current ⁽¹⁾		IF	200	mA	
Repetitive peak forward current ⁽¹⁾	t _p < 1 s, δ < 0.5	I _{FRM}	300	mA	
Surge forward current ⁽¹⁾	t _p = 10 ms	I _{FSM}	600	mA	
Power dissipation ⁽¹⁾		P _{tot}	150	mW	

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R _{thJA}	650	K/W	
Maximum junction temperature		Tj	125	°C	
Storage temperature range		T _{stg}	-65 to +150	°C	
Operating temperature range		T _{op}	-55 to +125	°C	

Note

⁽¹⁾ Valid provided that electrodes are kept at ambient temperature

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reserve breakdown voltage	Tested with 100 µA pulses	V _(BR)	30			V
Leakage current ⁽¹⁾	V _R = 25 V	I _R			2	μA
	I _F = 0.1 mA	V _F			240	mV
	I _F = 1 mA	V _F			320	mV
Forward voltage ⁽¹⁾	I _F = 10 mA	V _F			400	mV
	I _F = 30 mA	V _F			500	mV
	I _F = 100 mA	V _F			800	mV
Diode capacitance	V _R = 1 V, f = 1 MHz	CD			10	pF
Reserve recovery time	I_F = 10 mA, I_R = 10 mA, i_R = 1 mA, R_L = 100 Ω	t _{rr}			5	ns

Note

 $^{(1)}\,$ Pulse test: t_p < 300 µs, θ < 2 %

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

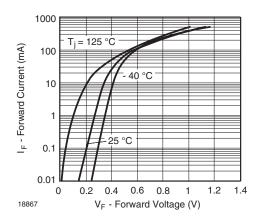


Fig. 1 - Typical Forward Current vs. Forward Voltage vs. Various Temperatures

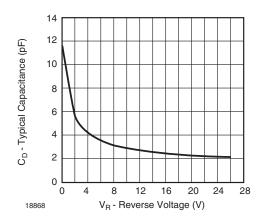


Fig. 2 - Typical Capacitance vs. Reverse Applied Voltage

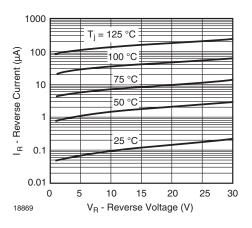
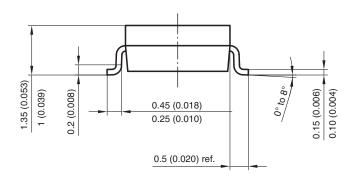


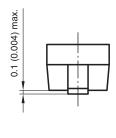
Fig. 3 - Typical Reverse Current vs. Reverse Voltage vs. Various Temperatures

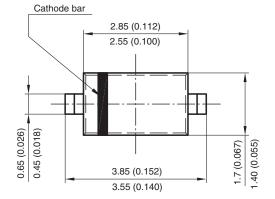


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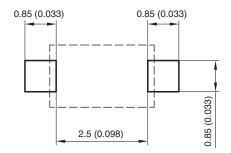
PACKAGE DIMENSIONS in millimeters (inches): SOD-123







Mounting Pad Layout



Rev. 4 - Date: 24. Sep. 2009 Document no.: S8-V-3910.01-001 (4) 17432



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