



P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
- 30	0.0087 at $V_{GS} = -10 \text{ V}$	- 45 ^d	60		
- 30	0.0150 at $V_{GS} = -4.5 \text{ V}$	- 32	00		

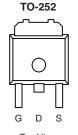
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_q and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

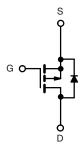
- · Power Switch
- Load Switch in High Current Applications
- DC/DC Converters



Drain Connected to Tab

Top View

Ordering Information: SUD45P03-09-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_C = 25 ^{\circ}C$, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V	
Gate-Source Voltage	V _{GS}	± 20	v	
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 25 °C	I-	- 45 ^d	
Continuous Diam Current (1 _J = 150 °C)	T _C = 70 °C	I _D	- 42.5	
Pulsed Drain Current		I _{DM}	- 100	A
Avalanche Current		I _{AS}	- 35	
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	61	mJ
M ·	T _C = 25 °C	В	41.7 ^b	14/
Maximum Power Dissipation ^a	T _A = 25 °C ^c	$ P_D$ $-$	2.1	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	60	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3	C/VV		

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).
- d. Package limited.



SPECIFICATIONS $T_J = 25 ^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 30			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 2.5	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	μΑ
		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 10 V, $V_{GS} =$ - 10 V	- 50			Α
Duein Course On Chata Basistanas	B	V _{GS} = - 10 V, I _D = - 20 A		0.0072	0.0087	0
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -15 \text{ A}$		0.0125	0.0150	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 20 A		45		S
Dynamic ^b						
Input Capacitance	C _{iss}			2700		pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = -15 \text{ V}, f = 1 \text{ MHz}$		515		
Reverse Transfer Capacitance	C _{rss}			445		
Total Gate Charge ^c	Q_g			60	90	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -20 \text{ A}$		9.3		nC
Gate-Drain Charge ^c	Q_{gd}			15		
Gate Resistance	R_g	f = 1 MHz	0.5	2.5	5	Ω
Turn-On Delay Time ^c	t _{d(on)}			12	20	
Rise Time ^c	t _r	V_{DD} = - 15 V, R_{L} = 1.5 Ω		11	20	ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 10 A, $V_{GEN} =$ - 10 V, $R_g = 1 \Omega$		40	60	115
Fall Time ^c	t _f			12	20	
Drain-Source Body Diode Ratings an	d Characteri	stics T _C = 25 °C ^b				
Continuous Current	I _S				- 45	^
Pulsed Current	I _{SM}				- 100	Α
Forward Voltage ^a	V_{SD}	I _F = - 10 A, V _{GS} = 0 V		- 0.8	- 1.5	V
Reverse Recovery Time	t _{rr}			27	40	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 10 A, dI/dt = 100 A/μs		1.3	2	Α
Reverse Recovery Charge	Q _{rr}			20	30	nC

Notes:

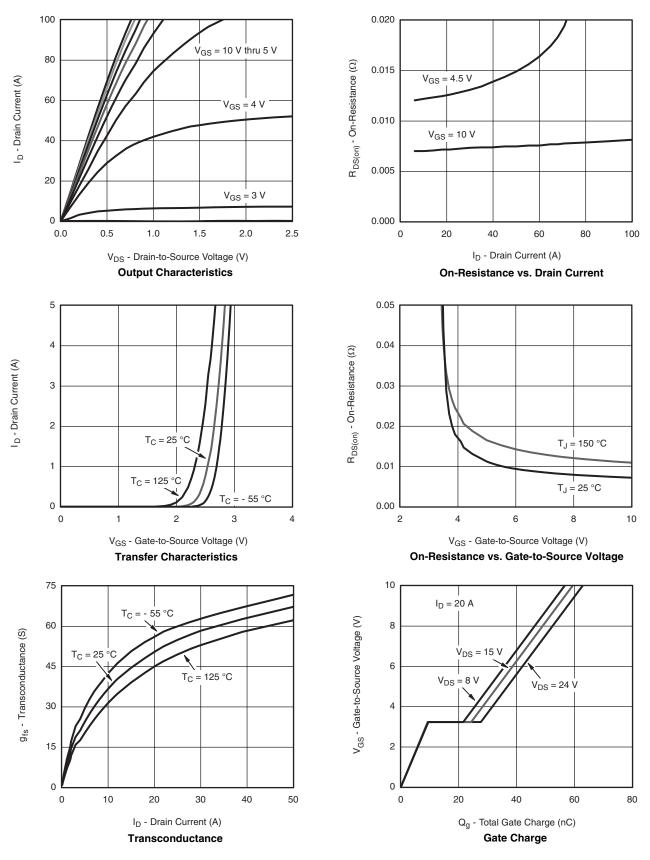
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



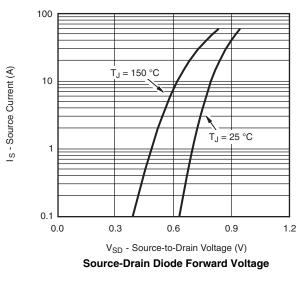


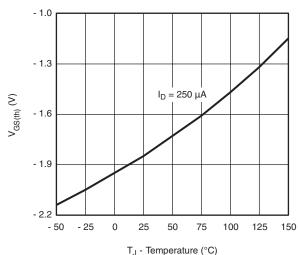
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

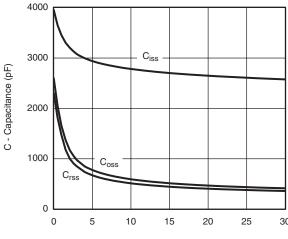


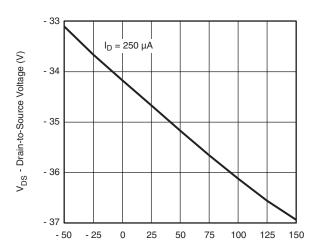
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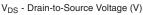


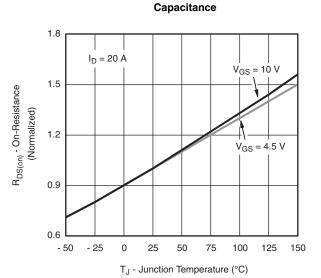




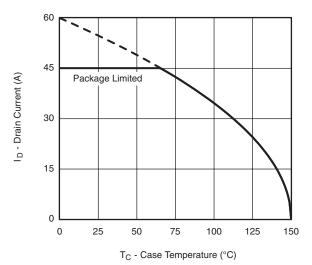


Threshold Voltage



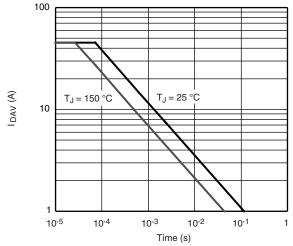


T_J - Junction Temperature (°C) **Drain Source Breakdown vs. Junction Temperature**

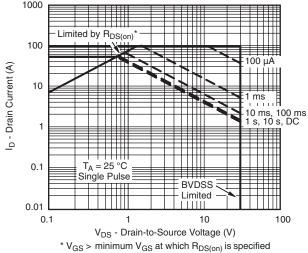


On-Resistance vs. Junction Temperature

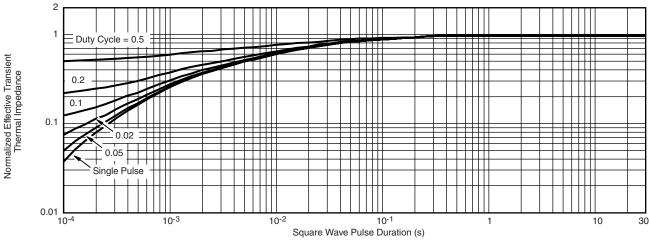
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Single Pulse Avalanche Current Capability vs. Time





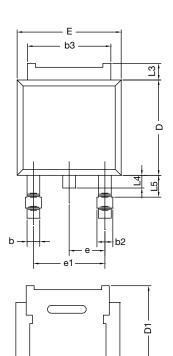


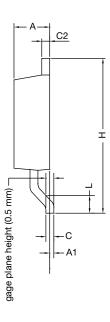
Normalized Thermal Transient Impedance, Junction-to-Case

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TO-252AA Case Outline





	MILLIMETERS		INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	2.28 BSC		BSC	
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
ECN: T16-0236-Rev. P, 16-May-16					

DWG: 5347

Notes

• Dimension L3 is for reference only.

Revision: 16-May-16 Document Number: 71197



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

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ATTLICATION NOT



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