End of Life "August 2021" - Alternative Device "S2D - S2M-E3"



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# CS2D, CS2G, CS2J, CS2K, CS2M

Vishay General Semiconductor

# **Surface Mount Glass Passivated Rectifier**



SMB (DO-214AA)

**DESIGN SUPPORT TOOLS** 



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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2.0 A				
V <sub>RRM</sub>	200 V, 400 V, 600 V, 800 V, 1000 V				
I <sub>FSM</sub>	50 A				
I <sub>R</sub>	5.0 µA				
$V_{\rm F}$ at $I_{\rm F}$ = 2.0 A ( $T_{\rm A}$ = 125 °C)	0.90 V				
T <sub>J</sub> max.	150 °C				
Package	SMB (DO-214AA)				
Circuit configuration	Single				

### **FEATURES**

- Low profile package
- Ideal for automated placement
- Glass passivated pellet chip junction
- Low forward voltage drop
- · Low leakage current
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes for consumer, and telecommunication.

### **MECHANICAL DATA**

**Case:** SMB (DO-214AA) Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	CS2D	CS2G	CS2J	CS2K	CS2M	UNIT
Device marking code		D	G	J	K	М	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	1000	V
Average forward rectified current	I <sub>F(AV)</sub> <sup>(1)</sup>	1.6					
	I <sub>F(AV)</sub> <sup>(2)</sup>	2.0					A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50				А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150				°C	

Notes

<sup>(1)</sup> Free air, mounted on recommended copper pad area

<sup>(2)</sup> Mounted on 8 mm x 8 mm copper pad areas

(Pb) (e3) RoHS



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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Maximum instantaneous forward voltage	I <sub>F</sub> = 1.0 A	$I_F = 1.0 A$ $I_F = 2.0 A$ $T_A = 25 ^{\circ}C$	V <sub>F</sub> <sup>(1)</sup>	0.92	-	V		
	I <sub>F</sub> = 2.0 A			0.99	1.2			
	I <sub>F</sub> = 1.0 A	T <sub>A</sub> = 125 °C		0.81	-			
	I <sub>F</sub> = 2.0 A			0.90	0.98			
Maximum DC reverse current at rated DC	$T_A = 25 \text{ °C}$	1 (2)	-	5.0				
blocking voltage	Rated V <sub>R</sub>	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	350	μA		
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1.0 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	2.1	-	μs		
Typical junction capacitance	4.0 V, 1 MHz	4.0 V, 1 MHz		12	-	pF		

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1  $\,\%$  duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	CS2D	CS2G	CS2J	CS2K	CS2M	UNIT
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	97					°C/W
	R <sub>0JM</sub> <sup>(2)</sup>	20					0/11

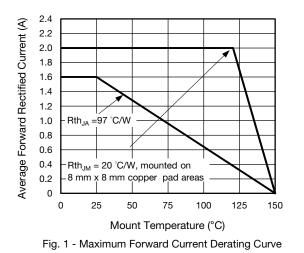
#### Notes

 $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction-to-ambient

 $^{(2)}$  Mounted on 8 mm x 8 mm copper pad areas,  $R_{\theta JM}$  - junction-to-mount at the terminal

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
CS2J-E3/I	0.096	I	3200	13" diameter plastic tape and reel					

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)



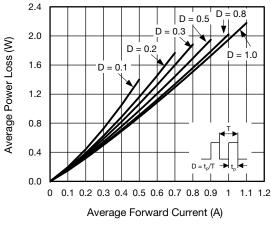


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

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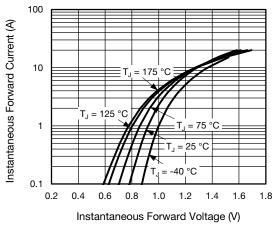
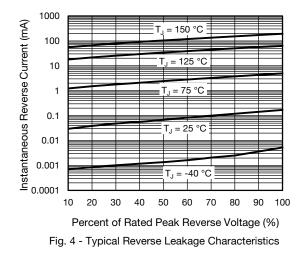
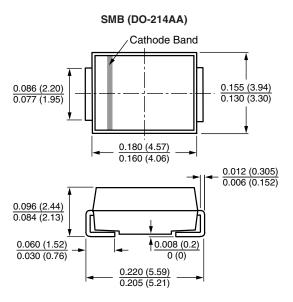


Fig. 3 - Typical Instantaneous Forward Characteristics







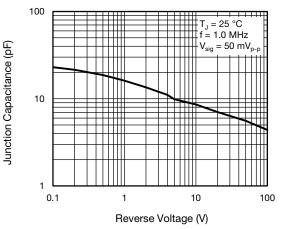


Fig. 5 - Typical Junction Capacitance

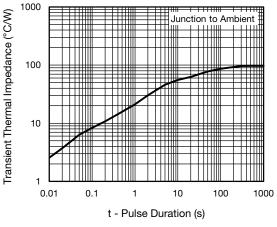
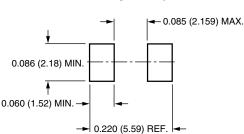


Fig. 6 - Typical Transient Thermal Impedance



#### Mounting Pad Layout

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