SQ2337ES

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Automotive P-Channel 80 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	- 80			
$R_{DS(on)}(\Omega)$ at V_{GS} = - 10 V	0.290			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -6 V$	0.314			
I _D (A)	- 2.2			
Configuration	Single			

FEATURES

- TrenchFET[®] Power MOSFET
- AEC-Q101 Qualified c
- 100 % $R_{\rm q}$ and UIS Tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

G

S

P-Channel MOSFET



RoHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2337ES (for detailed order number please see <u>www.vishay.com/doc?79771</u>)

ABSOLUTE MAXIMUM RATINGS (T	_C = 25 °C, unles	s otherwise noted	l)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V _{DS}	- 80	v
Gate-source voltage		V _{GS}	± 20	v
Continuous drain current	T _C = 25 °C	1	- 2.2	
	T _C = 125 °C	- I _D	- 1.3	
Continuous source current (diode conduction)		۱ _S	- 3.7	А
Pulsed drain current ^a		I _{DM}	- 9	-
Single pulse avalanche current		I _{AS}	- 11	
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	6	mJ
Maximum power dissipation ^a	T _C = 25 °C	D	3	w
	T _C = 125 °C	P _D	1	v
Operating junction and storage temperature rang	e	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount ^b	R _{thJA}	166	°C/W
Junction-to-foot (drain)		R _{thJF}	50	C/W

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

b. When mounted on 1" square PCB (FR-4 material)

c. Parametric verification ongoing

S21-1074-Rev. C, 15-Nov-2021

1

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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static					I	<u> </u>	
Drain-source breakdown voltage	V _{DS}	V _{GS}	= 0, I _D = - 250 μA	- 80	-	-	v
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = - 250 μA	- 1.5	-	- 2.5	v
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = - 80 V	-	-	- 1	μA
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = -80 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	- 50	
		$V_{GS} = 0 V$	$V_{DS} = -80 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	- 150	
On-state drain current ^a	I _{D(on)}	V _{GS} = - 10 V	$V_{DS} \ge 5 V$	- 8	-	-	Α
Drain-source on-state resistance ^a	P	V _{GS} = - 10 V	I _D = - 1.2 A	-	0.241	0.290	Ω
	R _{DS(on)}	V _{GS} = - 6 V	I _D = - 1.1 A	-	0.261	0.314	
Forward transconductance b	g fs	V _{DS} = - 15 V, I _D = - 1.2 A		-	3.5	-	S
Dynamic ^b	•				•		
Input capacitance	C _{iss}			-	495	620	
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = - 40 V, f = 1 MHz	-	40	55	pF
Reverse transfer capacitance	C _{rss}			-	30	38	
Total gate charge ^c	Qg			-	11.5	18	
Gate-source charge ^c	Q _{gs}	V _{GS} = - 10 V	$V_{DS} = -40 \text{ V}, \text{ I}_{D} = -1.2 \text{ A}$	-	1.9	-	nC
Gate-drain charge ^c	Q _{gd}			-	3.3	-	
Gate resistance	Rg	f = 1 MHz		2.2	4.43	7	Ω
Turn-on delay time ^c	t _{d(on)}	V_{DD} = - 40 V, R _L = 41.6 Ω I _D ≅ - 0.96 A, V _{GEN} = - 10 V, R _g = 1 Ω		-	5	8	
Rise time ^c	t _r			-	10	15	ns
Turn-off delay time ^c	t _{d(off)}			-	18	27	
Fall time ^c	t _f			-	8	12	
Source-Drain Diode Ratings and Char	acteristics ^b					- -	
Pulsed current ^a	I _{SM}			-	-	- 9	Α
Forward voltage	V _{SD}	I _F =	- 0.8 A, V _{GS} = 0	-	- 0.8	- 1.2	V

Notes

a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$

b. Guaranteed by design, not subject to production testing

c. Independent of operating temperature

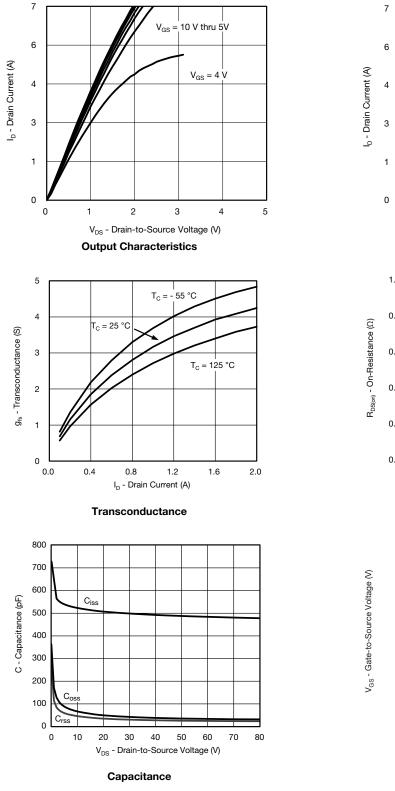
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

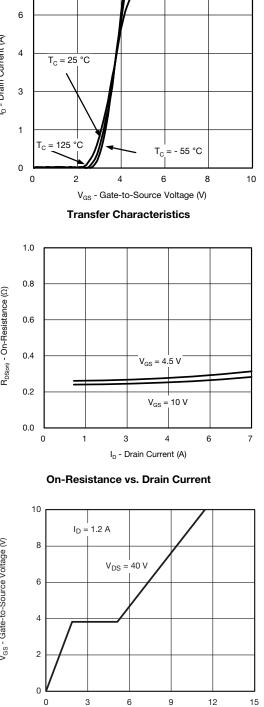
2



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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





Gate Charge

Q_q - Total Gate Charge (nC)

S21-1074-Rev. C, 15-Nov-2021

3

Document Number: 66717

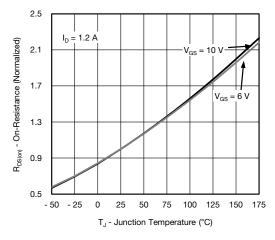
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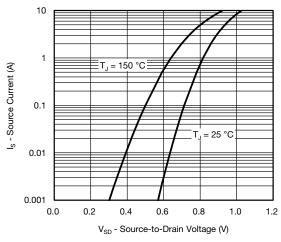
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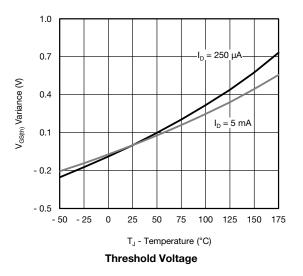
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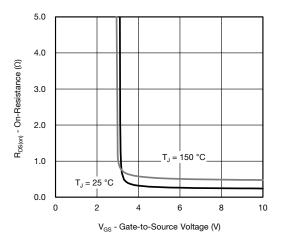


On-Resistance vs. Junction Temperature

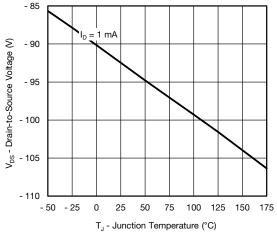


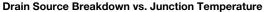


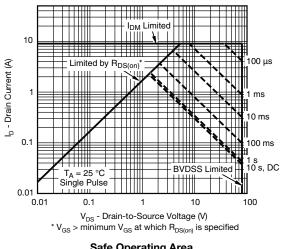




On-Resistance vs. Gate-to-Source Voltage







Safe Operating Area

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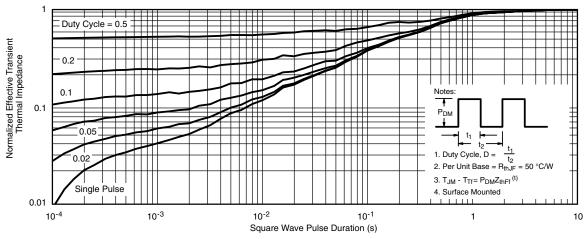
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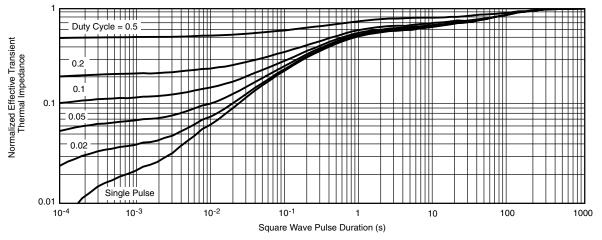


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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)









Note

• The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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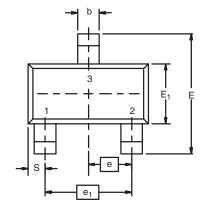
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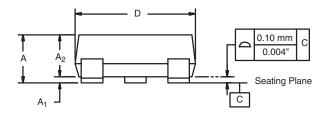


Package Information

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SOT-23 (TO-236): 3-LEAD







Dim -	MILLIN	METERS	INCHES			
	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95	0.95 BSC		0.0374 Ref		
e ₁	1.90	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025 Ref			
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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