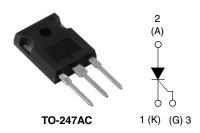


Vishay High Power Products

Phase Control SCR, 20 A



PRODUCT SUMMARY			
V _T at 20 A	< 1.3 V		
I _{TSM}	300 A		
V_{RRM}	800/1200 V		

DESCRIPTION/FEATURES

The 30TPS... High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 125 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
I _{T(AV)}	Sinusoidal waveform	20	٨	
I _{RMS}		30	A	
V _{RRM} /V _{DRM}		800/1200	V	
I _{TSM}		300	A	
V _T	20 A, T _J = 25 °C	1.3	V	
dV/dt		500	V/µs	
dl/dt		150	A/μs	
T _J		- 40 to 125	°C	

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
30TPS08	800	900	10			
30TPS12	1200	10				

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 95 °C, 180° conduc	ction half sine wave	20	
Maximum RMS on-state current	I _{RMS}			30	Α
Maximum peak, one-cycle		10 ms sine pulse, rated	V _{RRM} applied	250	A
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no vol	tage reapplied	300	
Maximum 12+ for fusing	l²t	10 ms sine pulse, rated	V _{RRM} applied	310	A ² s
Maximum I ² t for fusing	I²t	10 ms sine pulse, no voltage reapplied		442	A-S
Maximum I $^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		4420	A²√s
Maximum on-state voltage drop	V_{TM}	20 A, T _J = 25 °C		1.3	V
On-state slope resistance	r _t	T = 105 °C		12	mΩ
Threshold voltage	V _{T(TO)}	- T _J = 125 °C		1.0	V
Maximum various and divast lackage current	I _{RM} /I _{DM}	T _J = 25 °C	V _R = Rated V _{RRM} /V _{DRM}	0.5	- mA
Maximum reverse and direct leakage current		T _J = 125 °C		10	
Maximum holding current	l _Η	Anode supply = 6 V, resistive load, initial $I_T = 1$ A		100	mA
Maximum latching current	ΙL	Anode supply = 6 V, resistive load		200	
Maximum rate of rise of off-state voltage	dV/dt			500	V/µs
Maximum rate of rise of turned-on current	dl/dt			150	A/μs

TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}		8.0	W
Maximum average gate power	P _{G(AV)}		2.0	٧٧
Maximum peak positive gate current	+ I _{GM}		1.5	Α
Maximum peak negative gate voltage	- V _{GM}		10	V
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	60	mA
		Anode supply = 6 V, resistive load, T _J = 25 °C	45	
		Anode supply = 6 V, resistive load, T _J = 125 °C	20	
	V _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5	
Maximum required DC gate voltage to trigger		Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	v
		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0	V
Maximum DC gate voltage not to trigger	V_{GD}	T. – 125 °C V Potod volue	0.25	
Maximum DC gate current not to trigger	I_{GD}	$T_J = 125 ^{\circ}\text{C}, V_{\text{DRM}} = \text{Rated value}$ 2.0		mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T 105 °C	4	μs
Typical turn-off time	tq	T _J = 125 °C	110	

For technical questions, contact: diodes-tech@vishay.com

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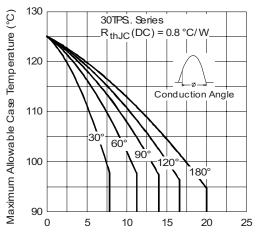
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PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and sto temperature range	orage	T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistar junction to case	nce,	R _{thJC}	DC operation	0.8	
Maximum thermal resistance, junction to ambient		R _{thJA}	40	°C/W	
Maximum thermal resistar case to heatsink	nce,	R _{thCS}	Mounting surface, smooth and greased	0.2	
Approximate weight			6	g	
			0.21	OZ.	
Mounting torque —	minimum			6 (5)	kgf · cm
	maximum			12 (10)	(lbf ⋅ in)
Marking device			0 TO 04740 (15050)	30TF	PS08
			Case style TO-247AC (JEDEC)	30TF	30TPS12

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Average On-state Current (A)
Fig. 1 - Current Rating Characteristics

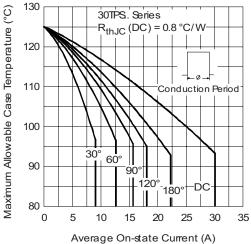


Fig. 2 - Current Rating Characteristics

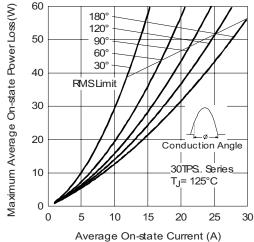


Fig. 3 - On-State Power Loss Characteristics

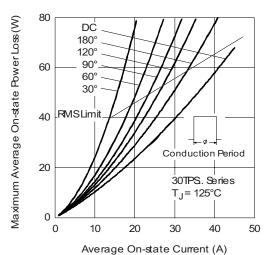


Fig. 4 - On-State Power Loss Characteristics

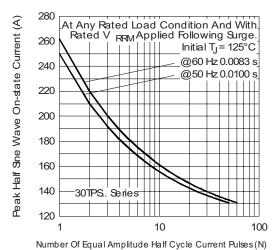


Fig. 5 - Maximum Non-Repetitive Surge Current

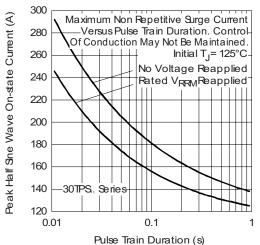


Fig. 6 - Maximum Non-Repetitive Surge Current



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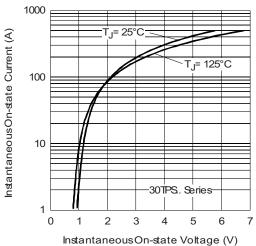


Fig. 7 - On-State Voltage Drop Characteristics

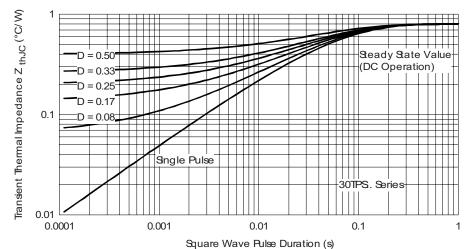


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

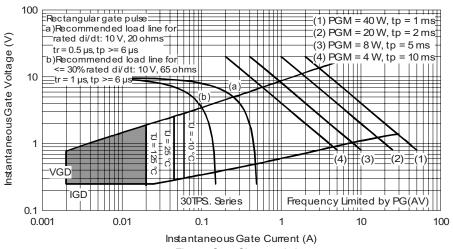


Fig. 9 - Gate Characteristics

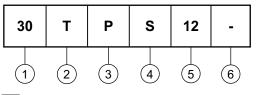
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (30 = 30 A)

2 - Circuit configuration:

T = Thyristor

3 - Package:

P = TO-247

Type of silicon:

S = Standard recovery rectifier

08 = 800 V 12 = 1200 V

5 -

Voltage code x 100 = V_{RRM} —
 None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95223			
Part marking information	http://www.vishay.com/doc?95226		



Vishay

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