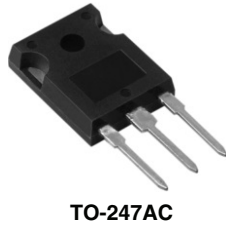
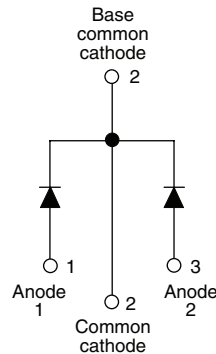


Schottky Rectifier, 2 x 20 A


TO-247AC


FEATURES

- 125 °C T_J operation ($V_R < 5$ V)
- Center tap module
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified for industrial level

DESCRIPTION

The MBR40L15CW center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

PRODUCT SUMMARY

$I_{F(AV)}$	2 x 20 A
V_R	15 V
I_{RM}	600 mA at 100 °C

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	40	A
V_{RRM}		15	V
I_{FSM}	$t_p = 5 \mu s$ sine	700	A
V_F	20 Apk, $T_J = 125$ °C (per leg, typical)	0.26	V
T_J	Range	- 55 to 125	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MBR40L15CW	UNITS
Maximum DC reverse voltage	V_R	$T_J = 100$ °C	15	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$	50 % duty cycle, at $T_C = 86$ °C, rectangular waveform	20	A
			40	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	700	A
		10 ms sine or 6 ms rect. pulse	330	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25$ °C, $I_{AS} = 2$ A, $L = 6$ mH	5	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	2	A

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	20 A	$T_J = 25\text{ }^\circ\text{C}$	-	0.42	V
		40 A		-	0.52	
		20 A	$T_J = 125\text{ }^\circ\text{C}$	0.26	0.34	
		40 A		0.37	0.50	
Reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	-	10	mA
		$T_J = 100\text{ }^\circ\text{C}$		-	600	
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.182		V
Forward slope resistance	r_t			7.6		m Ω
Maximum junction capacitance per leg	C_T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		8	-	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000		V/ μ s

Note

(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T_J		- 55 to 125	$^\circ\text{C}$
Maximum storage temperature range	T_{Stg}		- 55 to 150	
Maximum thermal resistance, junction to case per leg	R_{thJC}	DC operation See fig. 4	1.4	$^\circ\text{C/W}$
Maximum thermal resistance, junction to case per package		DC operation	0.7	
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.24	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum	Non-lubricated threads	6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-247AC (JEDEC)	MBR40L15CW	

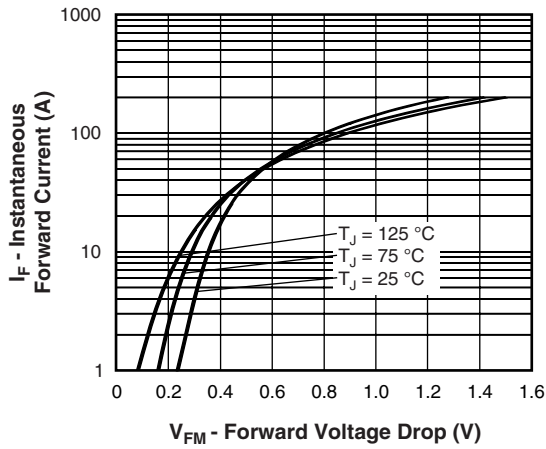


Fig. 1 - Maximum Forward Voltage Drop Characteristics

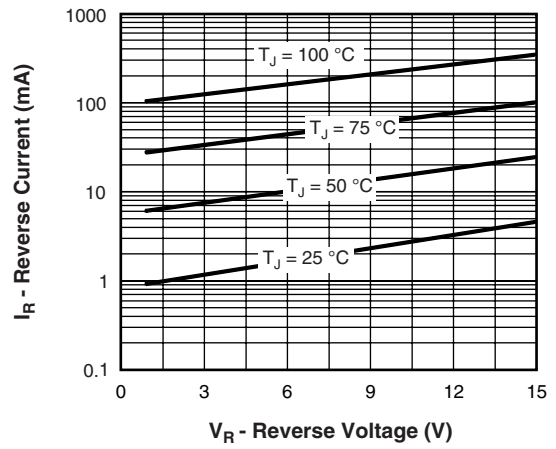


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

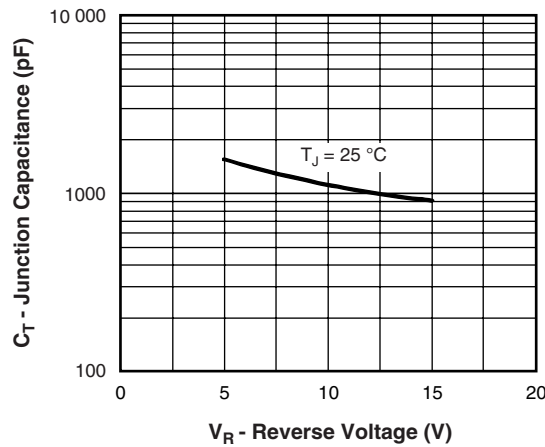
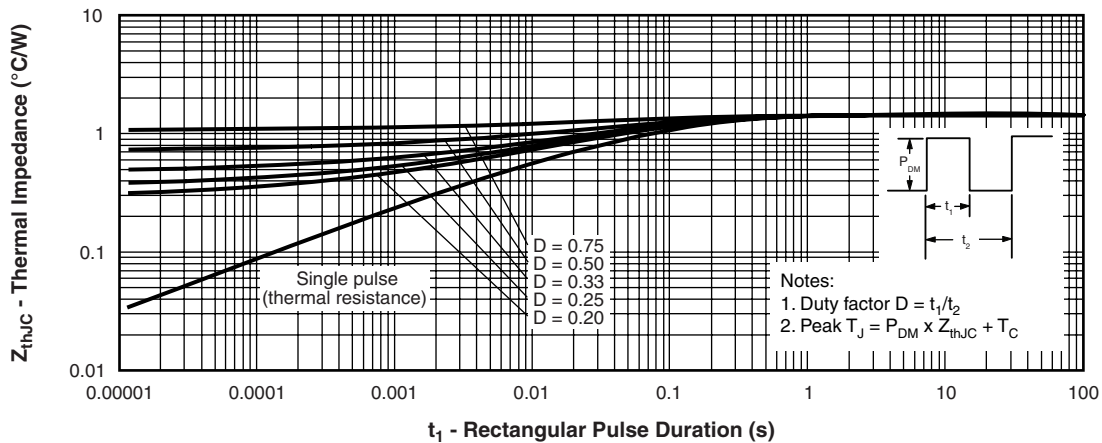
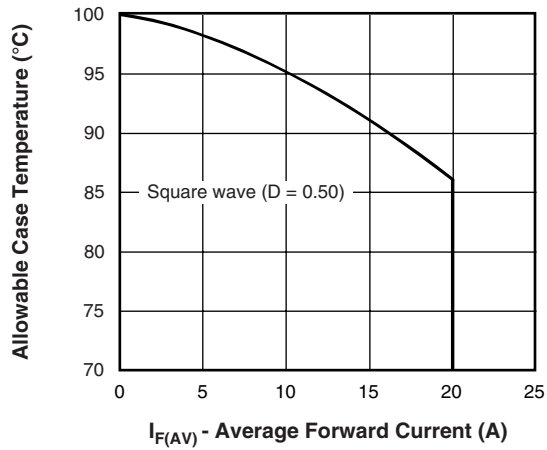
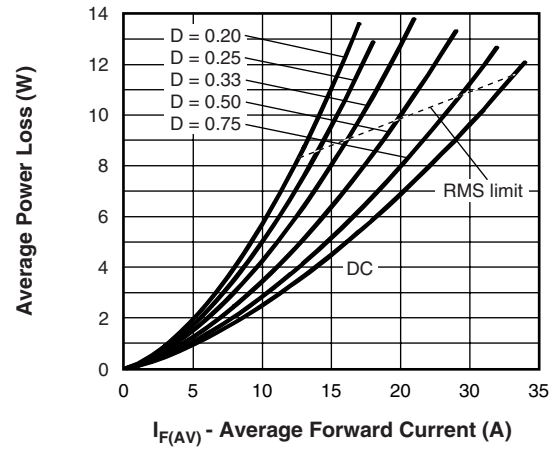


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

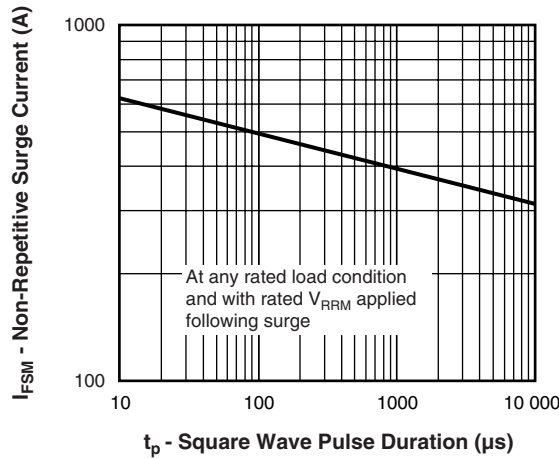

 Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



$I_{F(AV)}$ - Average Forward Current (A)
 Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



$I_{F(AV)}$ - Average Forward Current (A)
 Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)
 Fig. 7 - Maximum Non-Repetitive Surge Current

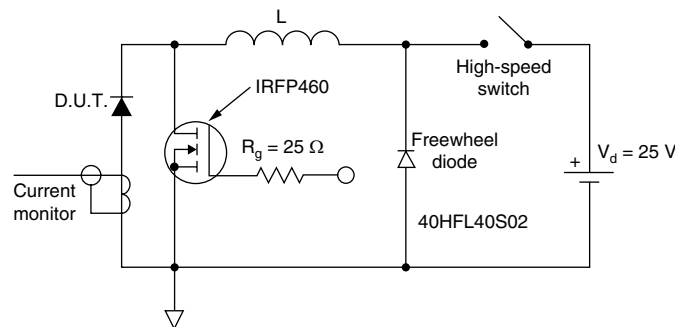
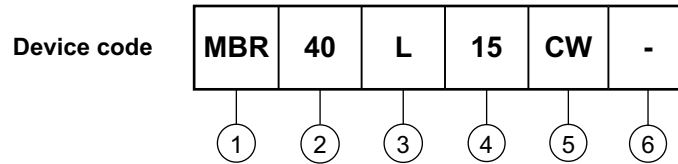


Fig. 8 - Unclamped Inductive Test Circuit



ORDERING INFORMATION TABLE



- 1** - Schottky MBR series
- 2** - Current rating (40 = 40 A)
- 3** - L = Low forward voltage
- 4** - Voltage rating (15 = 15 V)
- 5** - Circuit configuration:
Center tap TO-247
- 6** -
 - None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95223
Part marking information	http://www.vishay.com/doc?95226



Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

单击下面可查看定价，库存，交付和生命周期等信息

[>>Vishay\(威世\)](#)