Vishay Semiconductors

High Performance Schottky Rectifier, 175 A



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PowerTab[®]

PRODUCT SUMMARY					
Package	PowerTab [®]				
I _{F(AV)}	175 A				
V _R	30 V				
V _F at I _F	0.52 V				
I _{RM}	650 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Single die				
E _{AS}	80 mJ				

FEATURES

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
- Screw mounting only
- AEC-Q101 qualified
- PowerTab[®] package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-175BGQ030HF4 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES					
. I	Rectangular waveform	175	А				
IF(AV)	T _C	97	°C				
V _{RRM}		30	V				
I _{FSM}	$t_p = 5 \ \mu s \ sine$	7400	А				
N-	175 A _{pk} (typical)	0.47	V				
V _F	TJ	150	°C				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-175BGQ030HF4	UNITS				
Maximum DC reverse voltage	V _R	- 30	V				
Maximum working peak reverse voltage	V _{RWM}		v				

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 97 °C,	175	А			
Maximum peak one cycle	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	7400	А		
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	V_{RRM} applied	1400	~		
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 12 \ A, \ L = 1.12$	80	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	12	А			

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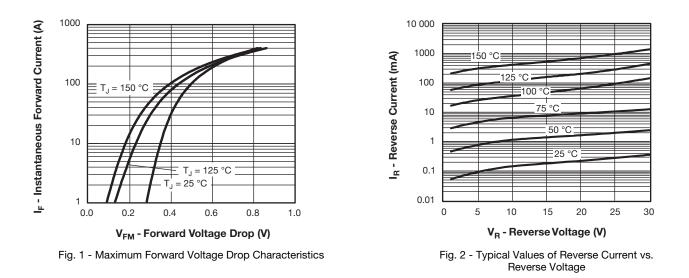
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PARAMETER SYMBOL TEST CONDITIONS TYP. MAX. UNIT						
FARAIVIETER	STIVIBOL	TEST CC	DNDITIONS	TTF.	IVIAA.	01113
		100 A	– T _{.1} = 25 °C	0.47	0.49	v
Forward voltage drop	V _{FM} ⁽¹⁾	175 A	1 - 25 0	0.55	0.59	
Forward voltage drop	VFM (')	100 A	T 150.00	0.36	0.39	v
		175 A	T _J = 150 °C	0.47	0.52	1
		T _J = 125 °C, V _R = 15 V		160	220	
	I _{BM} ⁽¹⁾	T _J = 150 °C, V _R = 30 V		1400	2000	mA
Reverse leakage current	IRM ("	T _J = 25 °C		1.3	4.5	
		T _J = 125 °C	V _R = Rated V _R	450	650	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C 8500				pF
Typical series inductance	L _S	Measured from tab to mounting plane 3.5			nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/			V/µs	

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and temperature range	storage	T _J , T _{Stg}		-55 to +150	°C	
Maximum thermal resis junction to case	tance,	R _{thJC}	DC operation	0.35	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.20		
Approximate weight				5	g	
Approximate weight				0.18	oz.	
Mounting torque minimum maximum				1.2 (10)	N·m	
				2.4 (20)	(lbf \cdot in)	
Marking device	Marking device Case style PowerTab [®] 175BGQ		Q030H			



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VS-175BGQ030HF4

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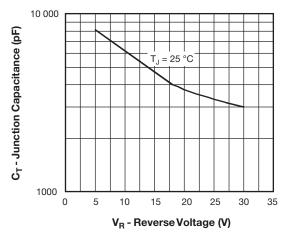


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

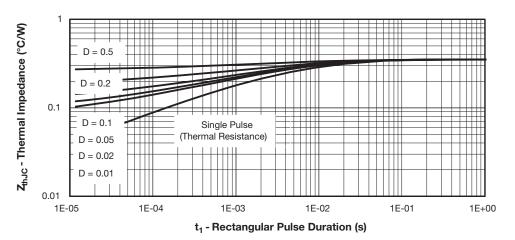
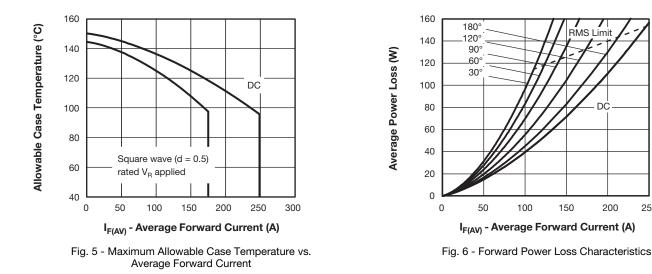


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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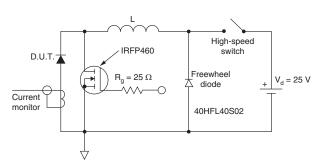
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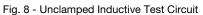
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10 000 I_{FSM} - Non Repetitive Surge Current (A) 1000 10 100 1000 10 000 t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current

ORDERING INFORMATION TABLE





Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; . Similar used: I_C = I_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

Device code	VS-	175	BGQ	030	н	F4
		2	3	4	5	6
	1 -		hay Serr rrent rati			
	3 -		sential pa	0.		·)
	4 -	Vol	tage rati	ng (030	= 30 V)	
	5 -	H =	AEC-Q	101 qua	lified	
	6 -	En	/ironmer	ntal digit	:	
	-	F4	= RoHS	complia	ant and t	otally le

ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-175BGQ030HF4	25	375	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95240					
Part marking information	www.vishay.com/doc?95467				
SPICE model	www.vishay.com/doc?95427				
Application note	www.vishay.com/doc?95179				

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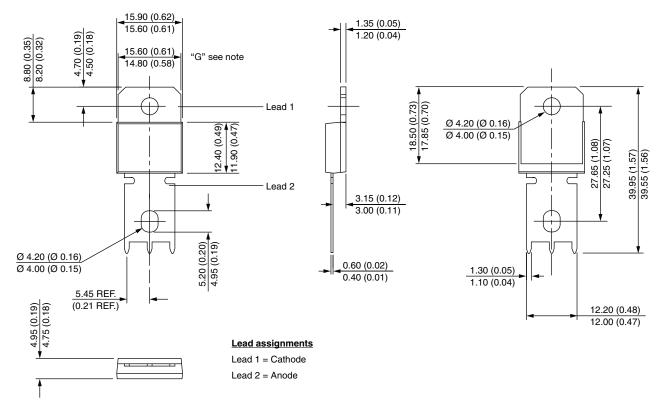
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DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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