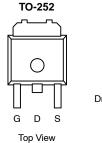




# N-Channel 100-V (D-S) 175°C MOSFET

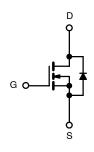
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$r_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)		
100	0.200 @ V <sub>GS</sub> = 10 V	6.5	2.7		
	0.225 @ V <sub>GS</sub> = 4.5 V	6.0	2.7		





Drain Connected to Tab

Order Number: SUD06N10-225L SUD06N10-225L—E3 (ILead (Pb)-Free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	.,	
Gate-Source Voltage		V <sub>GS</sub>	± 20	٧	
0 D . 0	T <sub>C</sub> = 25°C	- I <sub>D</sub>	6.5		
Continuous Drain Current (T <sub>J</sub> = 175°C) <sup>b</sup>	T <sub>C</sub> = 125°C		3.75		
Pulsed Drain Current		I <sub>DM</sub>	8.0	A	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	6.5		
Avalanche Current		I <sub>AR</sub>	5.0		
Repetitive Avalanche Energy (Duty Cycle ≤ 1%)	L = 0.1 mH	E <sub>AR</sub>	1.25	mJ	
	T <sub>C</sub> = 25°C		20 <sup>b</sup>		
Maximum Power Dissipation	T <sub>A</sub> = 25°C	P <sub>D</sub>	1.5 <sup>a</sup>	- W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 sec	R <sub>thJA</sub>	40	50	°C/W	
Junction-to-Ambient <sup>a</sup>	Steady State		80	100		
Junction-to-Case		R <sub>thJC</sub>	6.0	7.5		

#### Notes

Surface Mounted on 1" x1" FR4 Board.

b. See SOA curve for voltage derating.

## SUD06N10-225L

# Vishay Siliconix



Parameter	Symbol	Test Condition	Min	Typ <sup>a</sup>	Max	Unit	
Static	1 1		1	1		•	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, $I_D$ = 250 $\mu A$	100			v	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.0		3.0		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm 20$ V			±100	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	μΑ	
		$V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 175°C			250	7	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	8.0			Α	
		$V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		0.160	0.200	1	
		$V_{GS}$ = 10 V, $I_D$ = 3 A, $T_J$ = 125°C			0.350		
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3 A, T <sub>J</sub> = 175°C			0.450	Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 1.0 \text{ A}$		0.180	0.225		
Forward Transconductance <sup>b</sup>	9fs	$V_{DS} = 15 \text{ V}, I_{D} = 3 \text{ A}$		8.5		S	
Dynamic <sup>a</sup>						•	
Input Capacitance	C <sub>iss</sub>			240		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, F = 1 MHz		42			
Reverse Transfer Capacitance	C <sub>rss</sub>			17			
Total Gate Charge <sup>c</sup>	Qg			2.7	4.0	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, \ V_{GS} = 5 \text{ V}, \ I_{D} = 6.5 \text{ A}$		0.6			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			0.7			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			7	11	- ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_1 = 7.5 \Omega$		8	12		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\begin{aligned} &V_{DD} = 50 \text{ V, } R_L = 7.5 \Omega\\ I_D \cong 6.5 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 2.5 \Omega \end{aligned}$		8	12		
Fall Time <sup>c</sup>	t <sub>f</sub>			9	14		
Source-Drain Diode Ratings an	d Characteristi	c (T <sub>C</sub> = 25°C)					
Pulsed Current	I <sub>SM</sub>				8.0	Α	
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	$I_F = 6.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.9	1.3	٧	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 6.5 A, di/dt = 100 A/μs		35	60	ns	

#### Notes

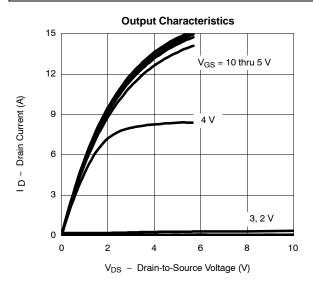
- Guaranteed by design, not subject to production testing. Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . Independent of operating temperature. a.

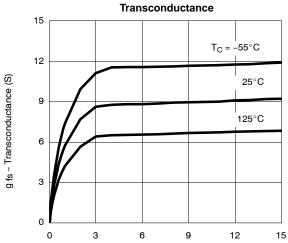
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

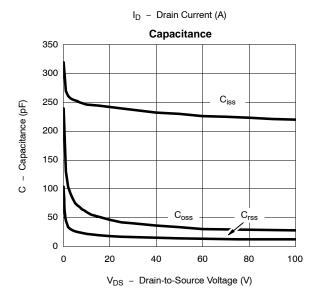


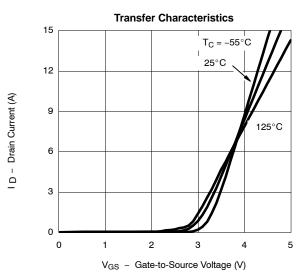
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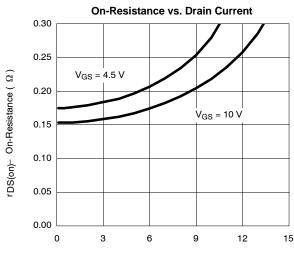
### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

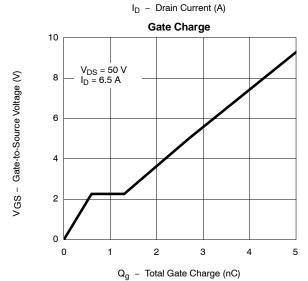








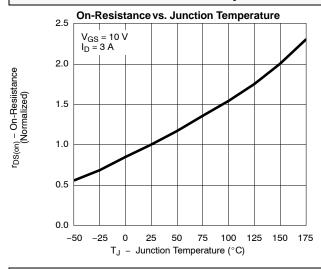


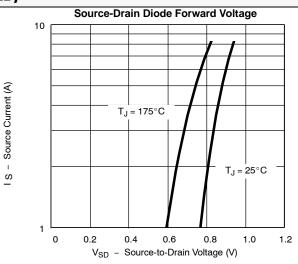


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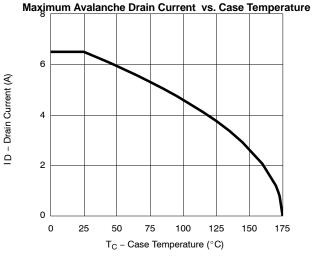


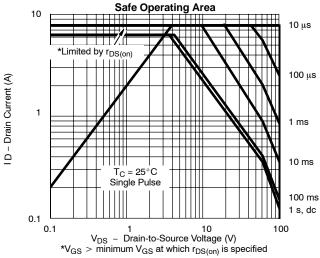
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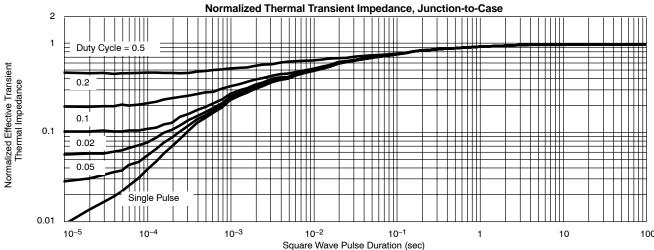




### THERMAL RATINGS







Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71253">http://www.vishay.com/ppg?71253</a>.



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