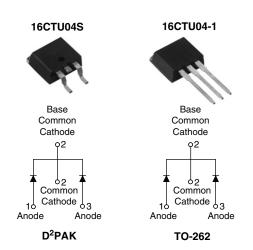


## Vishay High Power Products

## Ultrafast Rectifier, 2 x 8 A FRED Pt<sup>TM</sup>



PRODUCT SUMMARY				
t <sub>rr</sub>	60 ns			
I <sub>F(AV)</sub>	2 x 8 A			
V <sub>R</sub>	400 V			

#### **FEATURES**

- · Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

### **DESCRIPTION/APPLICATIONS**

FRED Pt<sup>TM</sup> series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		$V_{RRM}$		400	V
Average rectified forward current	per leg	I <sub>F(AV)</sub>		8	A
	total device		Rated V <sub>R</sub> , T <sub>C</sub> = 155 °C	16	
Non-repetitive peak surge current		I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	100	A
Peak repetitive forward current		I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 155 °C	16	
Operating junction and storage temp	eratures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	400	-	-		
Forward voltage V <sub>F</sub>	V	I <sub>F</sub> = 8 A	-	1.19	1.3	V	
	I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	0.94	1.0			
Payaraa laakaga aurrant	_	$V_R = V_R$ rated	-	0.2	10		
Reverse leakage current I <sub>R</sub>		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	20	500	μΑ	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 400 V	-	14	-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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# 16CTU04S/16CTU04-1

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<b>DYNAMIC RECOVERY CHARACTERISTICS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A}/\mu\text{A}, V_R = 30 \text{ V}$		-	35	60	
Reverse recovery time t <sub>rr</sub>	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	43	-	ns
		T <sub>J</sub> = 125 °C	$I_F = 8 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	67	-	
Peak recovery current I <sub>F</sub>		T <sub>J</sub> = 25 °C		-	2.8	-	Α
	IRRM	T <sub>J</sub> = 125 °C		-	6.3	-	
Reverse recovery charge Q <sub>rr</sub>		T <sub>J</sub> = 25 °C		-	60	-	nC
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.8	2.0	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	50	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-	
Mojest			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Mandana dasta		Case style D <sup>2</sup> PAK	16CTU04S			
Marking device		Case style TO-262	16CTU04-1			



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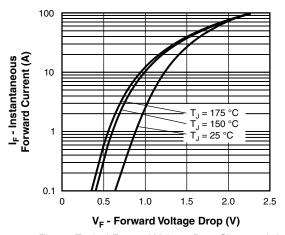


Fig. 1 - Typical Forward Voltage Drop Characteristics

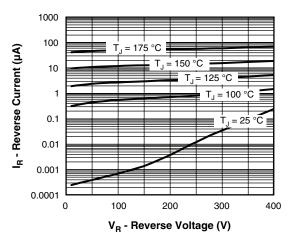


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

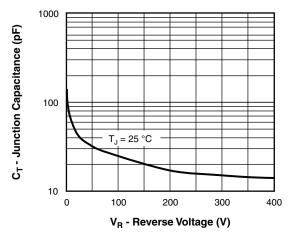


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

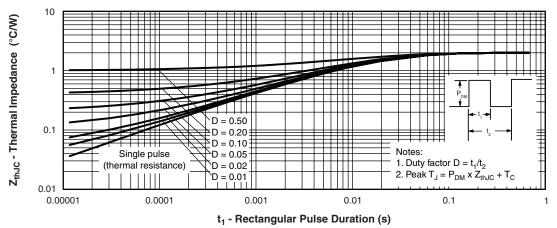


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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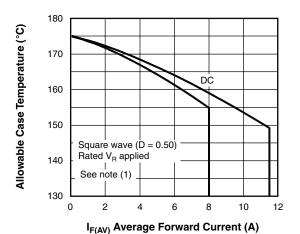


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

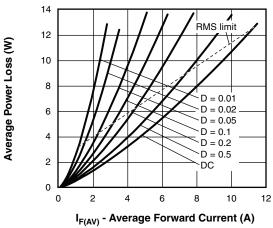


Fig. 6 - Forward Power Loss Characteristics

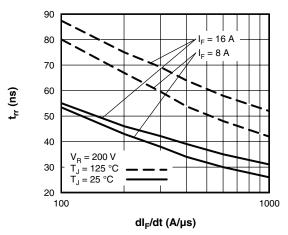


Fig. 7 - Typical Reverse Recovery Time vs.  $dI_F/dt$ 

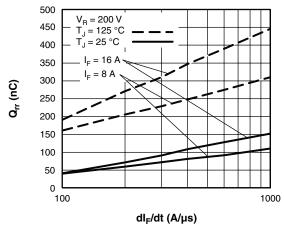


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \text{at} \; (I_{F(AV)}/D) \; (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \text{at} \; V_{R1} = \text{Rated} \; V_R \\ \end{array}$ 



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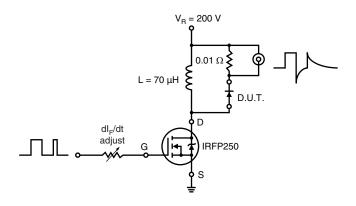
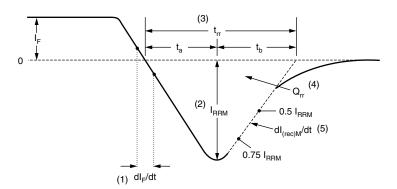


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 10 - Reverse Recovery Waveform and Definitions

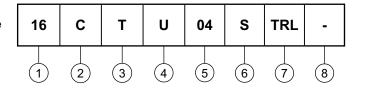
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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Current rating (16 A)

2 - C = Common cathode

3 - T = TO-220, D<sup>2</sup>PAK

4 - U = Ultrafast recovery

Voltage rating (04 = 400 V)

6 - • S = D<sup>2</sup>PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK package)

• TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95014			
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			



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