







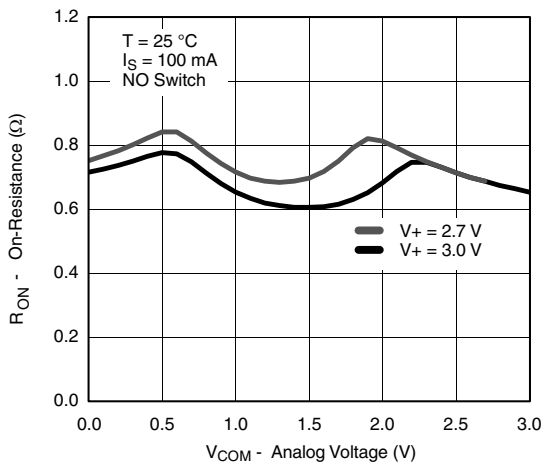
SPECIFICATIONS (V+ = 3 V)							
Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 3 V, ± 10 %, VIN = 0.4 V or 2 V <sup>e</sup>	Temp. <sup>a</sup>	Limits - 40 °C to 85 °C			Unit
				Min. <sup>b</sup>	Typ. <sup>c</sup>	Max. <sup>b</sup>	
<b>Power Supply</b>							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	VIN = 0 or V+	Full			1	µA

Notes:

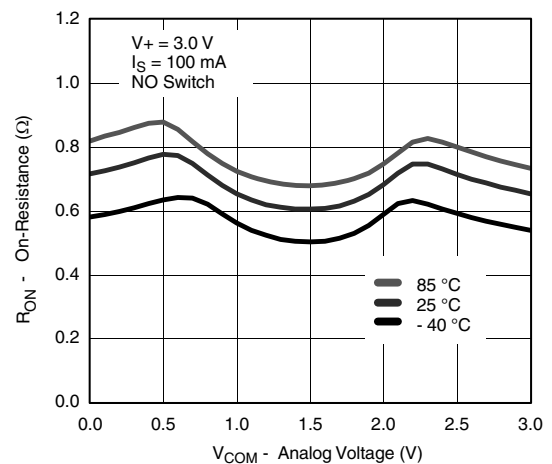
- a. Room = 25 °C, full = as determined by the operating suffix.
- b. Typical values are for design aid only, not guaranteed nor subject to production testing.
- c. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- d. Guarantee by design, nor subjected to production test.
- e. VIN = input voltage to perform proper function.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

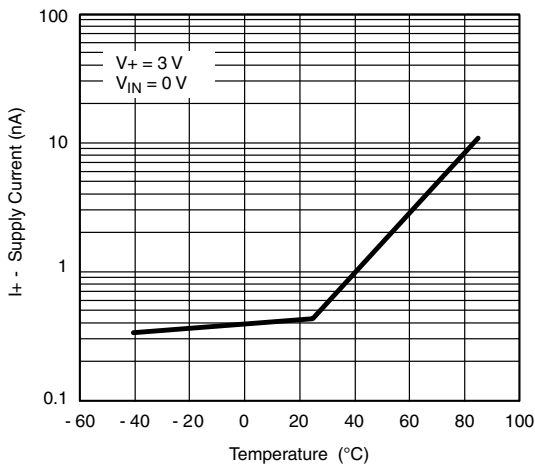
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



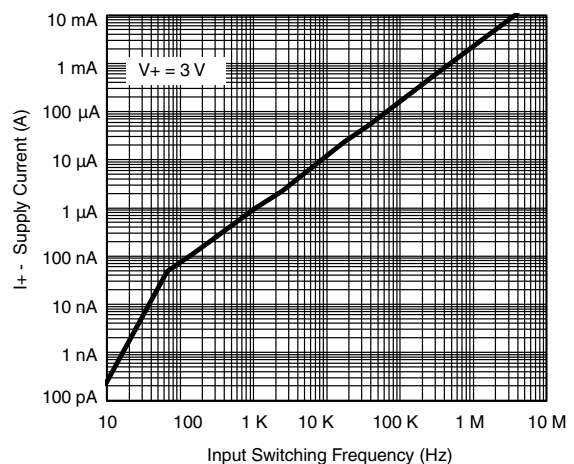
**RON vs. VCOM and Single Supply Voltage**



**RON vs. Analog Voltage and Temperature**



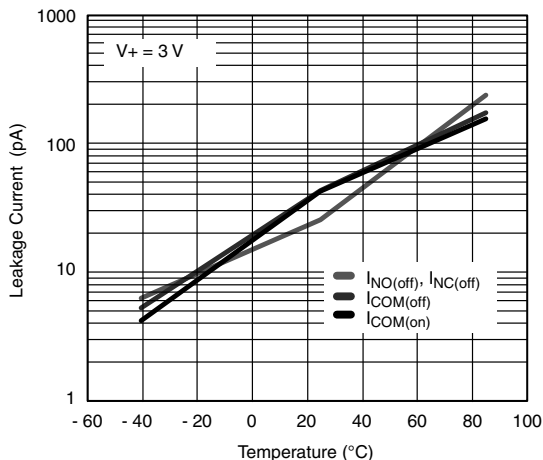
**Supply Current vs. Temperature**



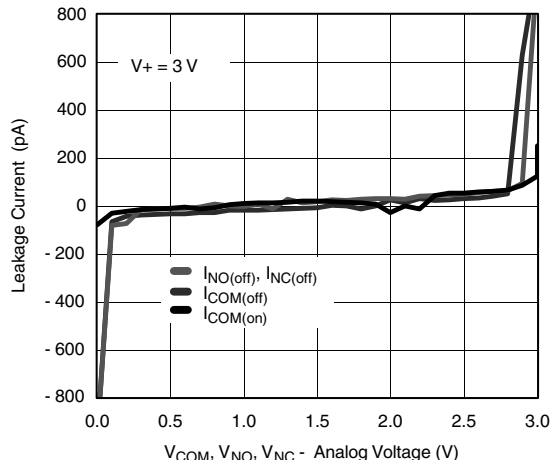
**Supply Current vs. Input Switching Frequency**



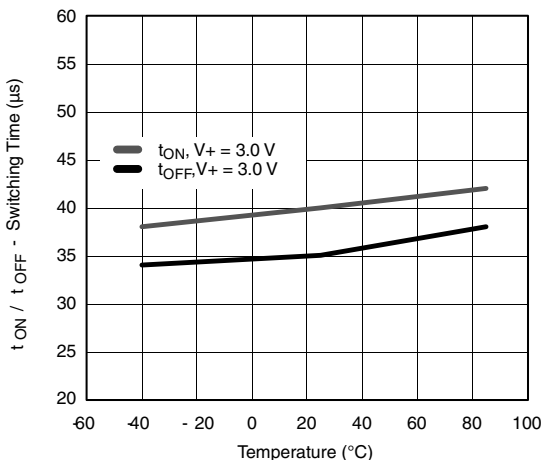
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



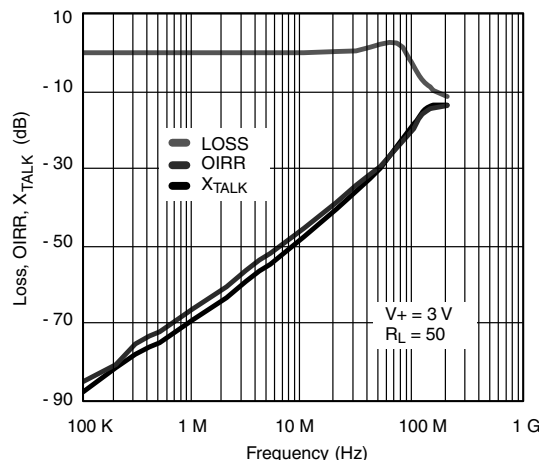
**Leakage Current vs. Temperature**



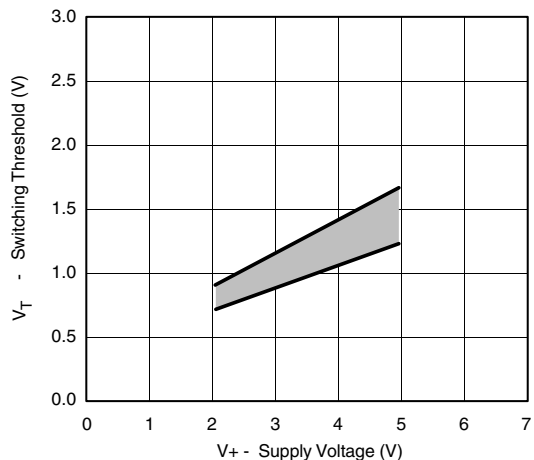
**Leakage vs. Analog Voltage**



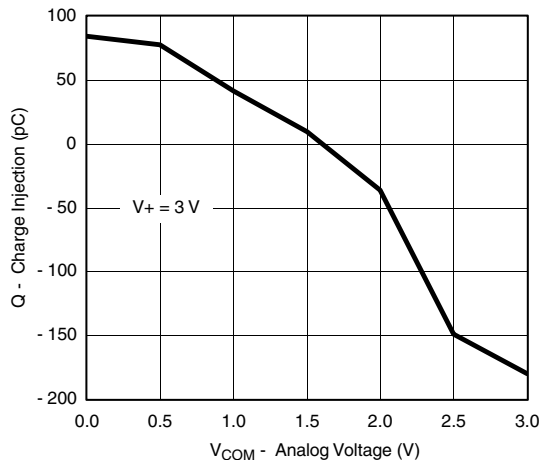
**Switching Time vs. Temperature**



**Insertion Loss, Off-Isolation, Crosstalk vs. Frequency**

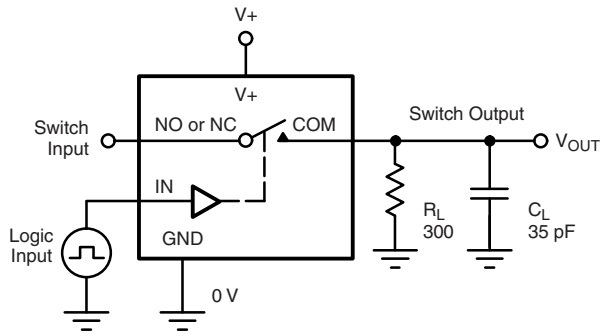


**Switching Threshold vs. Supply Voltage**



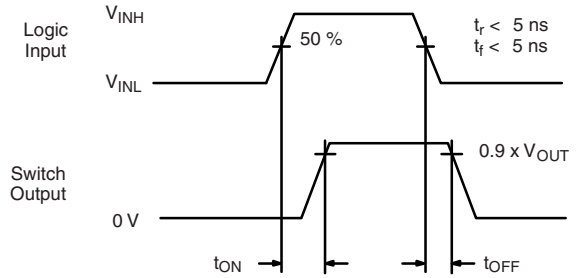
**Charge Injection vs. Analog Voltage**

TEST CIRCUITS



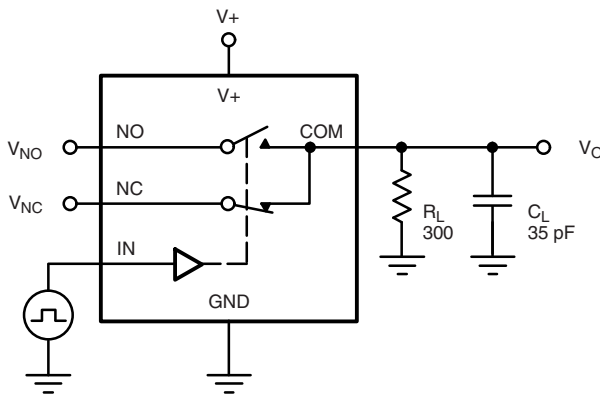
$C_L$  (includes fixture and stray capacitance)

$$V_{OUT} = V_{COM} \left( \frac{R_L}{R_L + R_{ON}} \right)$$



Logic "1" = Switch On  
Logic input waveforms inverted for switches that have the opposite logic sense.

Figure 1. Switching Time



$C_L$  (includes fixture and stray capacitance)

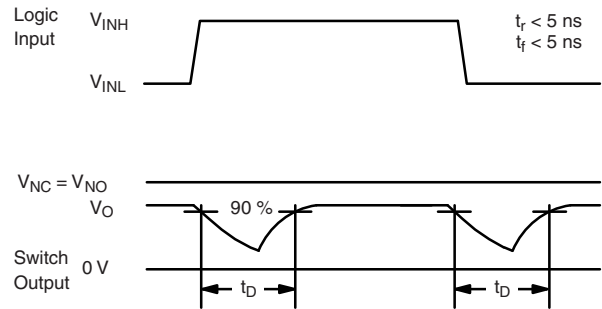
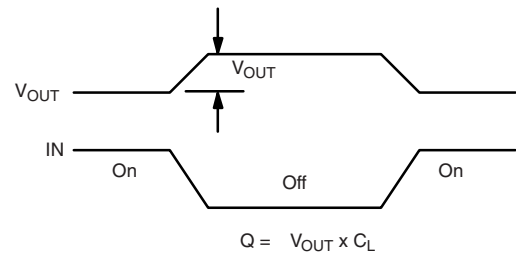
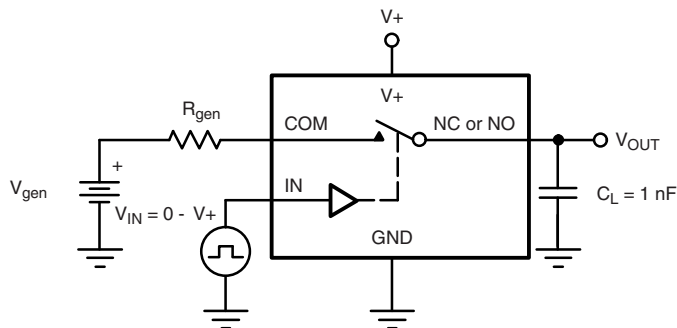


Figure 2. Break-Before-Make Interval



IN depends on switch configuration: input polarity determined by sense of switch.

Figure 3. Charge Injection

TEST CIRCUITS

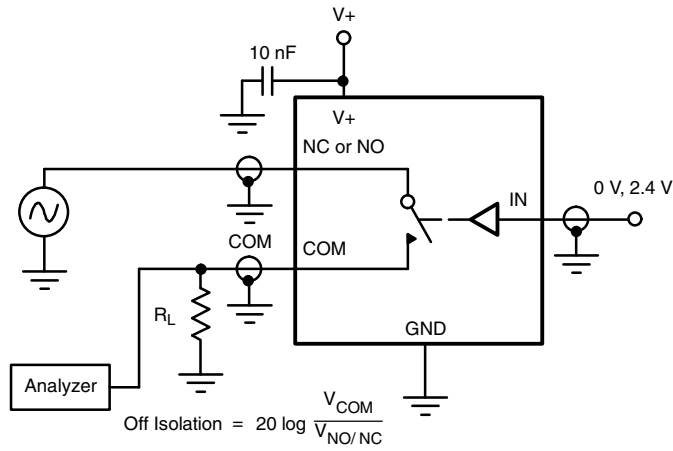


Figure 4. Off-Isolation

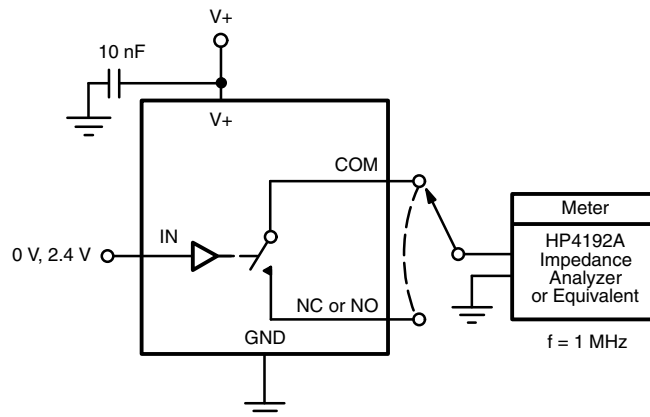
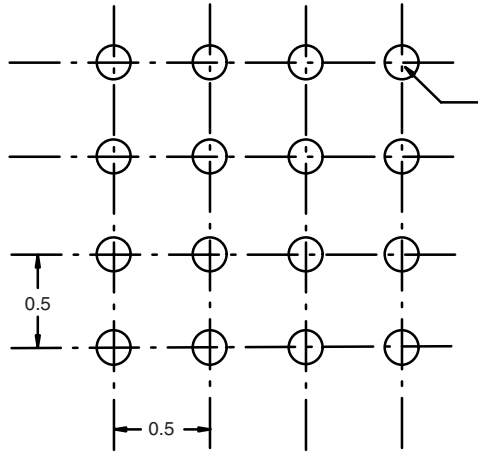


Figure 5. Channel Off/On Capacitance

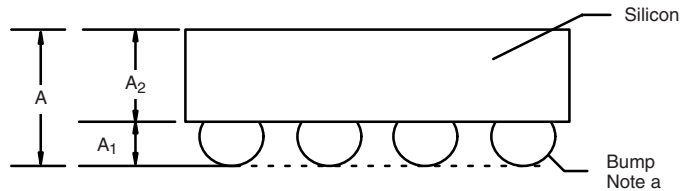


**PACKAGE OUTLINE**

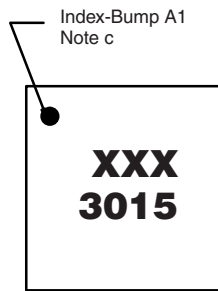
**MICRO FOOT: 16 BUMP (4 x 4, 0.5 mm PITCH, 0.238 mm BUMP HEIGHT)**



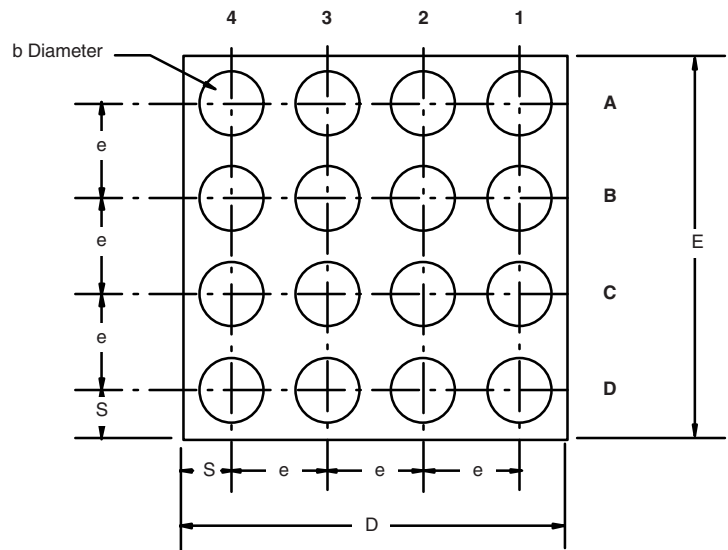
16 x  $\varnothing$  0.150 ~ 0.229  
Note b  
Solder Mask  $\varnothing$  - Pad Diameter + 0.1



Recommended Land Pattern



Top Side (Die Back)



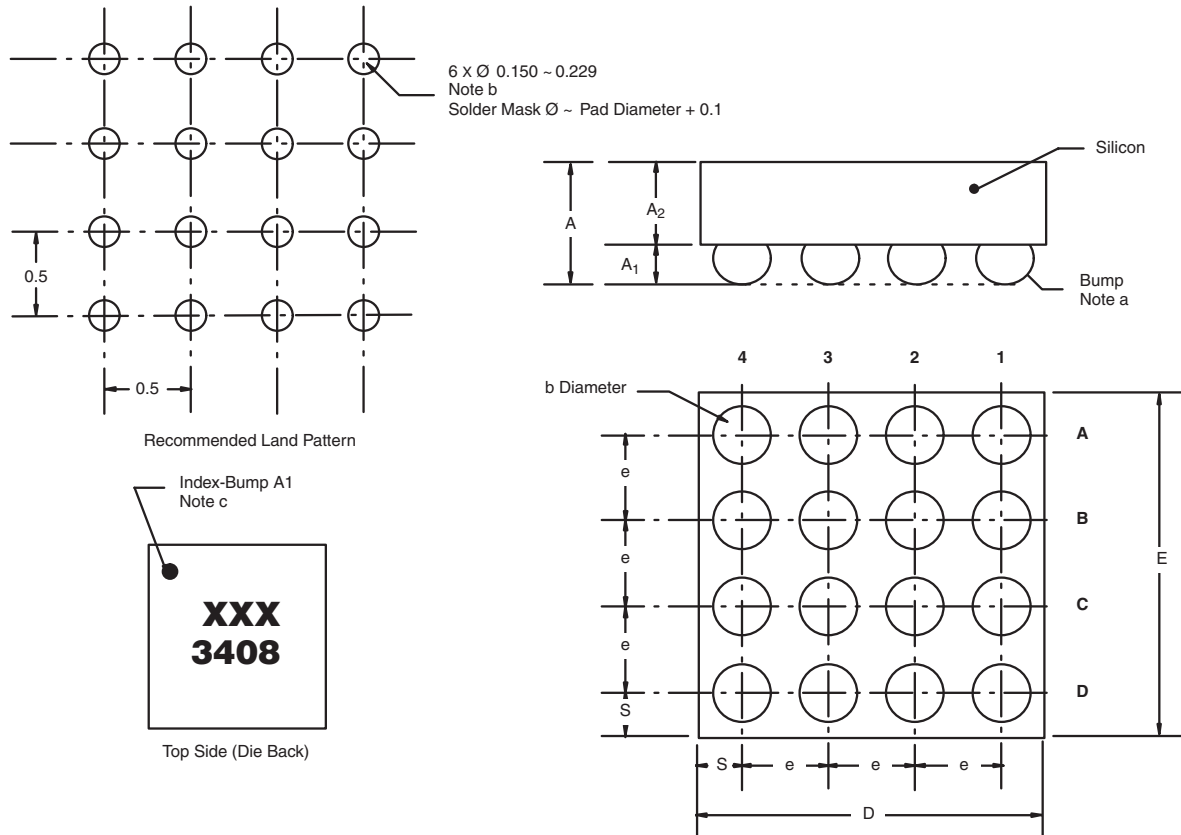
- Notes (Unless Otherwise Specified):
- a. Bump is Lead (Pb)-free Sn/Ag/Cu.
  - b. Non-solder mask defined copper landing pad.
  - c. Laser Mark on silicon die back; back-lapped, no coating. Shown is not actual marking; sample only.

Dim.	Millimeters <sup>a</sup>		Inches	
	Min.	Max.	Min.	Max.
A	0.688	0.753	0.0271	0.0296
A <sub>1</sub>	0.218	0.258	0.0086	0.0102
A <sub>2</sub>	0.470	0.495	0.0185	0.0195
b	0.306	0.346	0.0120	0.0136
D	1.980	2.020	0.0780	0.0795
E	1.980	2.020	0.0780	0.0795
e	0.5 BASIC		0.0197 BASIC	
S	0.230	0.270	0.0091	0.0106

- Notes:
- a. Use millimeters as the primary measurement.

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## MICRO FOOT: 16-BUMP (4 mm x 4 mm, 0.5 mm PITCH, 0.238 mm BUMP HEIGHT)



### Notes

(unless otherwise specified)

- a. Bump is lead (Pb)-free Sn/Ag/Cu.
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### Note

- a. Use millimeters as the primary measurement.

ECN: S11-1065-Rev. A, 13-Jun-11  
DWG: 6000





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