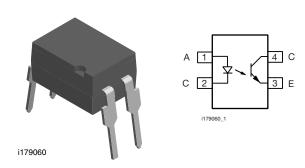
SFH615AA, SFH615AGB, SFH615AGR, SFH615AY

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Optocoupler, Phototransistor Output, High Reliability, 5300 V_{RMS}



DESCRIPTION

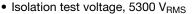
The SFH615XXX features a large assortment of current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared emitting diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of >8 mm are achieved with option 6. This version complies with 60950 (DIN VDE 0805) for reinforced insulation up to operation voltage of 400 V_{RMS} or DC.

FEATURES

- · Low CTR degradation
- Good CTR linearity depending on forward current



- High collector emitter voltage, V_{CEO} = 70 V
- · Low saturation voltage
- · Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode interference immunity (unconnected base)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- UL1577
- cUL
- DIN EN 60747-5-5 (VDE 0884) available with option 1

SFH615AY-X019T (1)

BSI

ORDERING INFORMATION s F 6 1 5 Α X 0 X X PART NUMBER PACKAGE OPTION **CTR (%) AGENCY CERTIFIED/PACKAGE** 5 mA UL, cUL, BSI, FIMKO 50 to 600 100 to 600 100 to 300 50 to 150 SFH615AY DIP-4 SFH615AGB SFH615AGR SFH615AA DIP-4, 400 mil, option 6 SFH615AA-X006 SMD-4, option 7 SFH615AA-X007 SFH615AGR-X007T SFH615AY-X007T SFH615AY-X008T SMD-4, option 8 SFH615AGB-X009T SFH615AY-X009T (1) SMD-4, option 9 UL. cUL. VDE. BSI. FIMKO 50 to 600 100 to 300 100 to 600 50 to 150 SFH615AA-X001 SFH615AGR-X001 SFH615AGR-X016 DIP-4, 400 mil, option 6 SFH615AY-X016 SMD-4, option 7 SFH615AA-X017T (1) SFH615AGR-X017T (1) SMD-4, option 8 SFH615AY-X018T (1)

Notes

SMD-4, option 9

- · Additional options may be possible, please contact sales office
- (1) Also available in tubes; do not add T to end

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| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT | |
|--|---|----------------------|--------------------|-----------|--|
| INPUT | | | | | |
| Reverse voltage | | V_{R} | 6 | V | |
| DC forward current | | I _F | 60 | mA | |
| Surge forward current $t_P \le 10 \text{ ms}$ | | I _{FSM} | 2.5 | А | |
| Power dissipation | | P _{diss} | 100 | mW | |
| OUTPUT | | | | | |
| Collector emitter voltage | | V_{CEO} | 70 | V | |
| Emitter collector voltage | | V_{ECO} | 7 | V | |
| Collector current | | Ic | 50 | mA | |
| Collector current | t _P ≤ 10 ms | I _C | 100 | mA | |
| Total power dissipation | | P_{diss} | 150 | mW | |
| COUPLER | | | | | |
| Isolation test voltage between emitter and detector | | V_{ISO} | 5300 | V_{RMS} | |
| Creepage distance | | | ≥7 | mm | |
| Clearance distance | | | ≥7 | mm | |
| Isolation thickness between emitter and detector Comparative tracking index per DIN IEC 112/VDE 0303, part 1 | | CTI | ≥ 175 | | |
| Isolation resistance | $V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$ | R _{IO} | ≥ 10 ¹² | Ω | |
| isolation resistance | V _{IO} = 500 V, T _{amb} = 100 °C | R _{IO} | ≥ 10 ¹¹ | Ω | |
| Storage temperature range | | T _{stg} | -55 to +150 | °C | |
| Ambient temperature range | | T _{amb} | -55 to +100 | °C | |
| Soldering temperature (1) | max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm | T _{sld} 260 | | °C | |

Notes

⁽¹⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

| ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified) | | | | | | | | |
|--|---|-----------|--------------------|------|------|------|------|--|
| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT | |
| INPUT | | | | | | | | |
| Forward voltage | $I_F = 60 \text{ mA}$ | | V _F | - | 1.25 | 1.65 | V | |
| Reverse current | $V_R = 6 V$ | | I _R | - | 0.01 | 10 | μA | |
| Capacitance | $V_R = 0 V$, $f = 1 MHz$ | | Co | - | 13 | - | pF | |
| Thermal resistance | | | R _{thja} | - | 750 | - | K/W | |
| OUTPUT | | | | | | | | |
| Collector emitter capacitance | $V_{CE} = 5 \text{ V}, f = 1 \text{ MHz}$ | | C _{CE} | - | 5.2 | - | pF | |
| Thermal resistance | | | R _{thja} | - | 500 | - | K/W | |
| Collector emitter saturation voltage | $I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$ | | V _{CEsat} | - | 0.25 | 0.4 | V | |
| Coupling capacitance | | | C _C | - | 0.4 | - | pF | |
| COUPLER | | | | | | | | |
| Collector emitter leakage | | SFH615AA | I _{CEO} | - | 10 | 100 | nA | |
| | V 10 V | SFH615AGB | I _{CEO} | - | 10 | 100 | nA | |
| current | V _{CEO} = 10 V | SFH615AGR | I _{CEO} | - | 10 | 100 | nA | |
| | | SFH615AY | I _{CEO} | - | 10 | 100 | nA | |

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

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| CURRENT TRANSFER RATIO | | | | | | | |
|--------------------------------|---|-----------|--------|------|------|------|------|
| PARAMETER | TEST CONDITION | PART | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| I _C /I _F | | SFH615AA | CTR | 50 | - | 600 | % |
| | $I_{\rm F} = 5 \text{mA}, V_{\rm CF} = 5 \text{V}$ | SFH615AGB | CTR | 100 | ı | 600 | % |
| | $I_F = 3 \text{ IIIA}, V_{CE} = 3 \text{ V}$ | SFH615AGR | CTR | 100 | - | 300 | % |
| | | SFH615AY | CTR | 50 | - | 150 | % |

| SWITCHING CHARACTERISTICS | | | | | | | |
|---------------------------|----------------------|------------------|------|------|------|------|--|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT | |
| Turn-on time | $I_F = 5 \text{ mA}$ | t _{on} | - | 2 | - | μs | |
| Turn-off time | $I_F = 5 \text{ mA}$ | t _{off} | - | 25 | - | μs | |

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

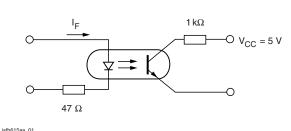


Fig. 1 - Switching Operation (with saturation)

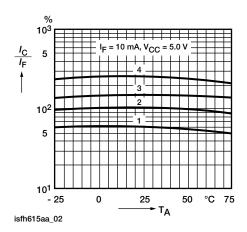


Fig. 2 - Current Transfer Ratio (typ.) vs. Temperature

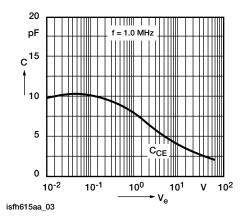


Fig. 3 - Transistor Capacitance (typ.) vs. Collector Emitter Voltage

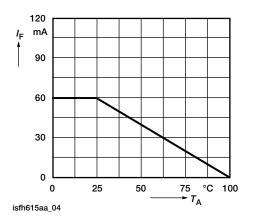


Fig. 4 - Permissible Diode Forward Current vs.
Ambient Temperature

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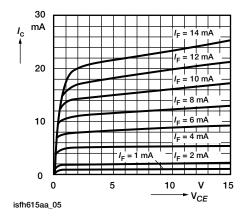


Fig. 5 - Output Characteristics (typ.) Collector Current vs. Collector Emitter Voltage

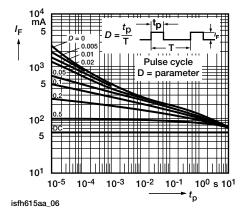


Fig. 6 - Permissible Pulse Handling Capability Forward Current vs. Pulse Width

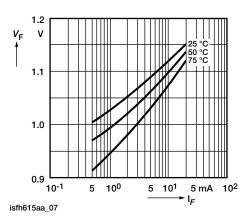


Fig. 7 - Diode Forward Voltage (typ.) vs. Forward Current

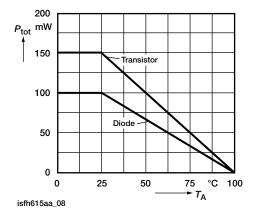


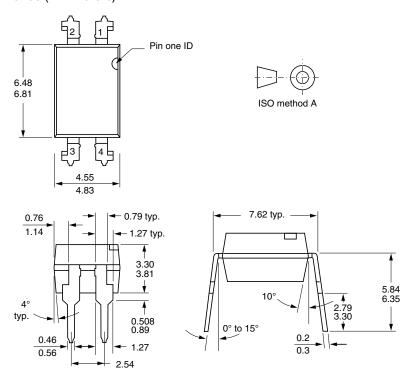
Fig. 8 - Permissible Power Dissipation vs. Temperature



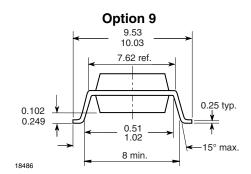
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PACKAGE DIMENSIONS in inches (millimeters)



i178027





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