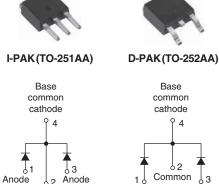
VS-20CUT10, VS-20CWT10FN

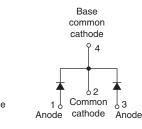
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High Performance Schottky Generation 5.0, 2 x 10 A



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I-PAK (TO-251AA)



Common cathode VS-20CUT10

VS-20CWT10FN

PRODUCT SUMMARY					
Package	D-PAK (TO-252AA), I-PAK (TO-251AA)				
I _{F(AV)}	2 x 10 A				
V _R	100 V				
V _F at I _F	0.66 V				
I _{RM} max.	4 mA at 125 °C				
T _J max.	175 °C				
Diode variation	Common cathode				
E _{AS}	54 mJ				

FEATURES

- 175 °C high performance Schottky diode
- Very low forward voltage drop
- Extremely low reverse leakage
- Optimized V_F vs. I_B trade off for high efficiency
- Increased ruggedness for reverse avalanche capability
- RBSOA available
- Negligible switching losses
- Submicron trench technology
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- High efficiency SMPS
- High frequency switching
- Output rectification
- Reverse battery protection
- Freewheeling
- DC/DC systems
- Increased power density systems

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UNITS						
V _{RRM}		100	V				
V _F	10 Apk, T _J = 125 °C (typical, per leg)	0.615	V				
TJ	Range	- 55 to 175	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VS-20CUT10 VS-20CWT10FN	UNITS		
Maximum DC reverse voltage	V _R	T _J = 25 °C	100	V		

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ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	DITIONS	VALUES	UNITS		
Maximum average per leg		50 % dute such at T 150 % restance law was from		50 % duty cycle at $T_{\rm C}$ = 159 °C, rectangular waveform		10	А
forward current per device	I _{F(AV)}	30% duty cycle at $1_{\rm C} = 139\%$ C	, rectangular wavelonn	20	~		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	610	А		
non-repetitive surge current per leg		10 ms sine or 6 ms rect. pulse V _{RRM} applied	110				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 12 mH		54	mJ		
Repetitive avalanche current per leg	I _{AR}	Limited by frequency of operation and time pulse duration so that $T_J < T_J$ max. I_{AS} at T_J max. as a function of time pulse (see fig. 8)		I _{AS} at T _J max.	А		

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS			MAX.	UNITS	
Forward voltage drop per leg		10 A	T 05 00	0.735	0.810	v	
	V (1)	20 A	T _J = 25 °C	0.840	0.890		
	V _{FM} ⁽¹⁾	10 A	T _J = 125 °C	0.615	0.660		
		20 A		0.730	0.770		
Reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C		-	50	μA	
		T _J = 125 °C	V _R = Rated V _R	-	4	mA	
Junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		400	-	pF	
Series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	-	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	TJ, T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg	D		2		
Maximum thermal resistance, junction to case per device	- R _{thJC}	DC operation	1	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}		0.3		
Approvimate weight			0.3	g	
Approximate weight			0.01	oz.	
		Case style I-PAK	20Cl	JT10	
Marking device		Case style D-PAK	20CW	T10FN	

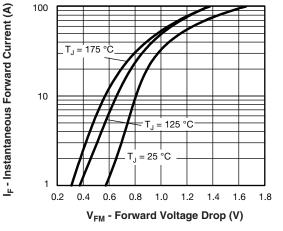
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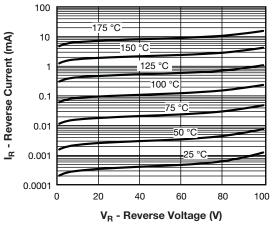
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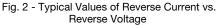
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Fig. 1 - Maximum Forward Voltage Drop Characteristics





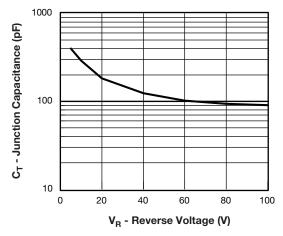
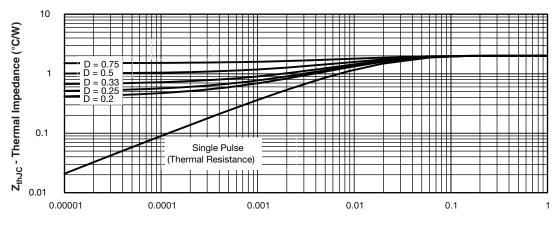


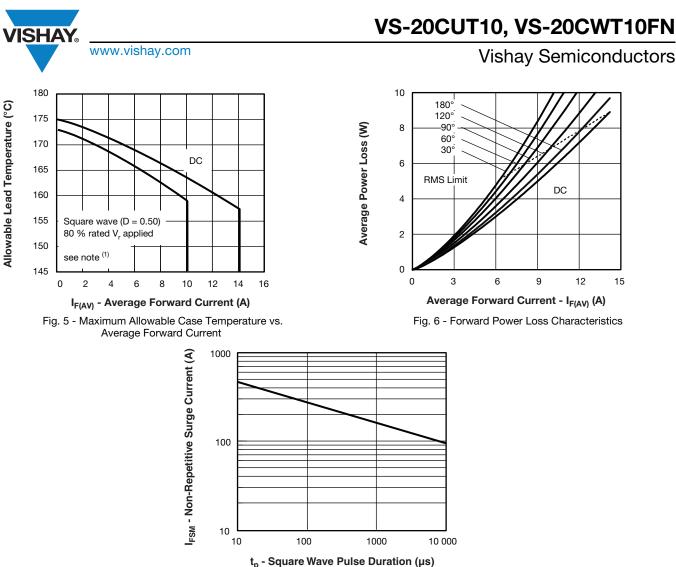
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

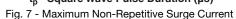


t₁ - Rectangular Pulse Duration (s)

Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Note

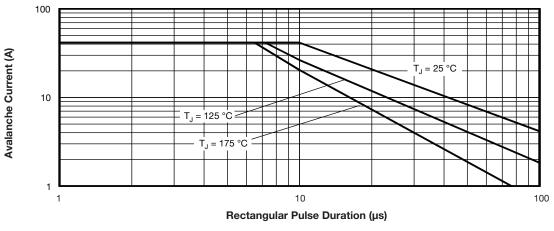


Fig. 8 - Reverse Bias Safe Operating Area (Avalanche Current vs. Rectangular Pulse Duration)

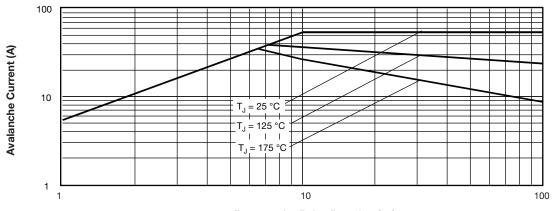
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VS-20CUT10, VS-20CWT10FN

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Rectangular Pulse Duration (µs)

Fig. 9 - Reverse Bias Safe Operating Area (Avalanche Energy vs. Rectangular Pulse Duration)

ORDERING INFORMATION TABLE

Device code	VS-	20	с	U	т	10	FN	TRL
	1	2	3	4	5	6	7	8
	1	- Vis	hay Sen	niconduo	ctors pro	oduct		
	2	- Cur	rrent rati	ng (20 A	۹)			
	3	- Ciro	cuit conf	iguratio	n:			
		C =	Comm	on catho	ode			
	4	- Pao	ckage:					
		۰U	= I-PAK	<u> </u>				
		•W	' = D-PA	K				
	5	- T=	Trench					
	6	- Vol	tage rat	ing (10 =	= 100 V)		
	7	- TO	-252AA	(D-PAK)			
	8	- D-F	PAK, I-P	AK:				
		Nor	ne = Tul	be (75 pi	ieces)			
		D-F	PAK only	/:				
		• TF	R = Tap	e and re	el			
		• TF	RL = Ta	pe and r	eel (left	oriente	d)	
		• TF	RR = Ta	pe and ı	reel (rig	ht orien	ted)	

LINKS TO RELATED DOCUMENTS					
Dimensions	I-PAK (TO-251AA)	www.vishay.com/doc?95024			
Dimensions	D-PAK (TO-252AA)	www.vishay.com/doc?95448			
Part marking information	I-PAK (TO-251AA)	www.vishay.com/doc?95025			
	D-PAK (TO-252AA)	www.vishay.com/doc?95059			
Packaging information		www.vishay.com/doc?95033			
SPICE model		www.vishay.com/doc?95041			

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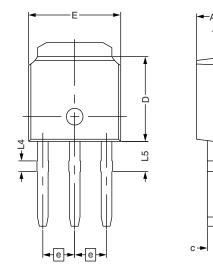
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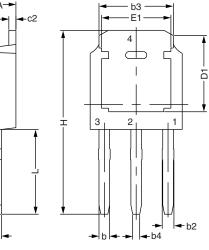


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I-PAK - S

DIMENSIONS FOR I-PAK - S in millimeters





CYMPOL	DIMENSIONAL REQUIREMENTS					
SYMBOL	MIN.	NOM.	MAX.			
E	6.40	6.60	6.70			
L	3.98	4.13	4.28			
L4	0.66	0.76	0.86			
L5	1.96	2.16	2.36			
D	6.00	6.10	6.20			
н	11.05	11.25	11.45			
b	0.64	0.76	0.88			
b2	0.77	0.84	1.14			
b3	5.21	5.34	5.46			
b4	0.41	0.51	0.61			
е		2.286 BSC				
A	2.20	2.30	2.38			
с	0.40	0.50	0.60			
c2	0.40	0.50	0.60			
D1	5.30	-	-			
E1	4.40	-	-			

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