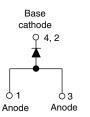
Vishay Semiconductors

High Performance Schottky Rectifier, 3.5 A



www.vishay.com



Single die

8 mJ

D-PAK (TO-252AA)

Diode variation

EAS

$D^{-1}AR(10^{-2}32AR)$				
PRODUCT SUMMARY				
Package	D-PAK (TO-252AA)			
I _{F(AV)}	3.5 A			
V _R	40 V			
V _F at I _F	See Electrical table			
I _{RM}	24 mA at 125 °C			
T _J max.	150 °C			

FEATURES

- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30WQ04FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.5	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	500	А		
V _F	3 A _{pk} , T _J = 125 °C	0.49	V		
TJ		-40 to +150	°C		

VOLTAGE RATINGS				
PARAMETER SYMBOL		VS-30WQ04FNPbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 135 °C, rectangular waveform		3.5	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	500	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		80	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 16 mH		8.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	A





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.53	V
		6 A		0.67	
		3 A	- T _J = 125 °C	0.49	
		6 A		0.62	
Maximum reverse leakage current	Im reverse leakage current	$T_J = 25 \ ^{\circ}C$	V _R = Rated V _R	2	mA
See fig. 2	IRM W	T _J = 125 °C		24	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.34	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 $^{\circ}\text{C}$		189	pF
Typical series inductance	Ls	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

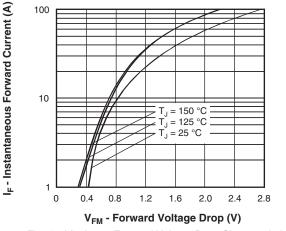
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W
Approximate weight		0.3	g	
		0.01	oz.	
Marking device		Case style D-PAK (similar to TO-252AA)	30WC	04FN

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

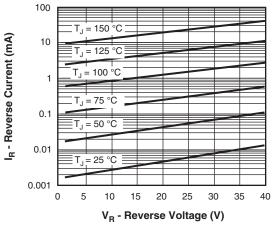
VS-30WQ04FNPbF

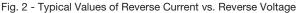
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Fig. 1 - Maximum Forward Voltage Drop Characteristics





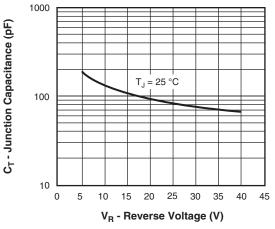


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

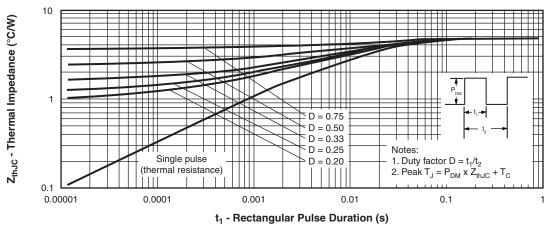
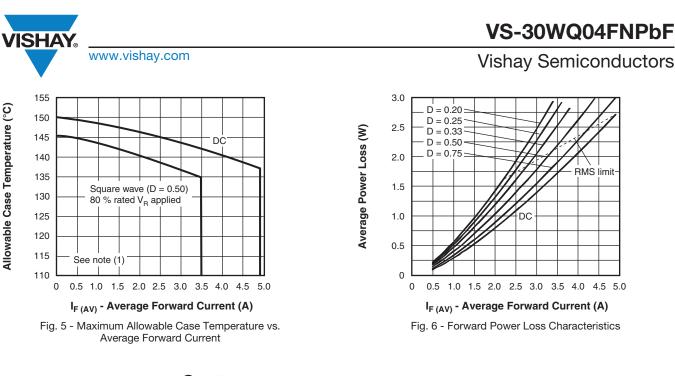


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



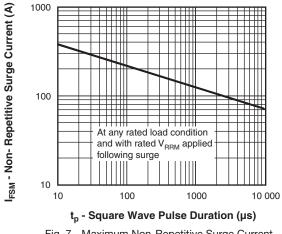


Fig. 7 - Maximum Non-Repetitive Surge Current

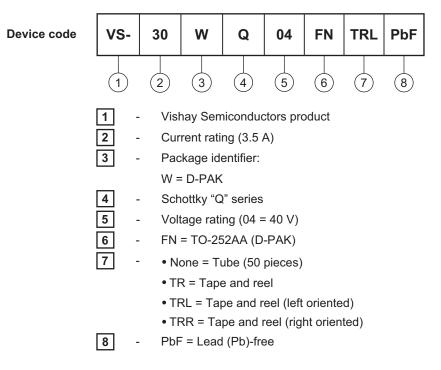
Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			
SPICE model	www.vishay.com/doc?95288			
	www.vishay.com/doc?95630			



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