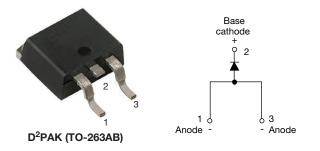
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Surface Mount Fast Soft Recovery Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	10 A						
V _R	600 V						
V _F at I _F	1.2 V						
I _{FSM}	140 A						
t _{rr}	50 ns						
T _J max.	150 °C						
Snap factor	0.6						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						

FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Input rectification
- On-board and off-board EV / HEV battery chargers

DESCRIPTION

The VS-10ETF06SLHM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time and low forward voltage drop.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
V _{RRM}		600	V						
I _{F(AV)}	Sinusoidal waveform	10	^						
I _{FSM}		140	— A						
t _{rr}	1 A, 100 A/µs	50	ns						
V _F	10 A, T _J = 25 °C	1.2	V						
TJ	Range	-40 to +150	°C						

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-10ETF06SLHM3	600	700	2.5

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum average forward current	I _{F(AV)}	T_C = 128 °C, 180° conduction half sine wave	10					
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	115	А				
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	140					
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V_{RRM} applied	66	A ² s				
Maximum -t for fusing	1-1	10 ms sine pulse, no voltage reapplied	94	A-5				
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	940	A²√s				

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 1
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COMPLIANT HALOGEN





ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
Maximum forward voltage drop	V _{FM}	10 A, T _J = 25 °C		1.2	V			
Forward slope resistance	r _t	T.I = 150 °C	12.7	mΩ				
Threshold voltage	V _{F(TO)}	1) = 150 C	$I_{\rm J} = 150 {}^{\circ}{\rm C}$					
Maximum reverse leakage current	1	T _J = 25 °C	V_{B} = rated V_{BBM}	0.1	mA			
Maximum reverse leakage current	IRM	$T_{\rm J} = 150 \ ^{\circ}{\rm C}$		2.5	ШA			

RECOVERY CHARACTERISTICS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	· •			
Reverse recovery time	t _{rr}	l⊨ at 10 A _{nk}	200	ns	I _{FM} t			
Reverse recovery current	I _{rr}	l _F at 10 A _{pk} 25 A/μs	2.75	А				
Reverse recovery charge	Q _{rr}	25 °C	0.32	μC				
Snap factor	S		0.6		I IRM(REC)			

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C				
Maximum thermal resistance junction to case	R _{thJC}	DC operation	1.5	°C (M)				
Maximum thermal resistance junction to ambient (PCB mount)	R _{thJA} ⁽¹⁾		40	°C/W				
Approvimete weight			2	g				
Approximate weight			0.07	oz.				
Marking device		Case style D ² PAK (TO-263AB)	10ETF06SH					

Note

⁽¹⁾ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W.

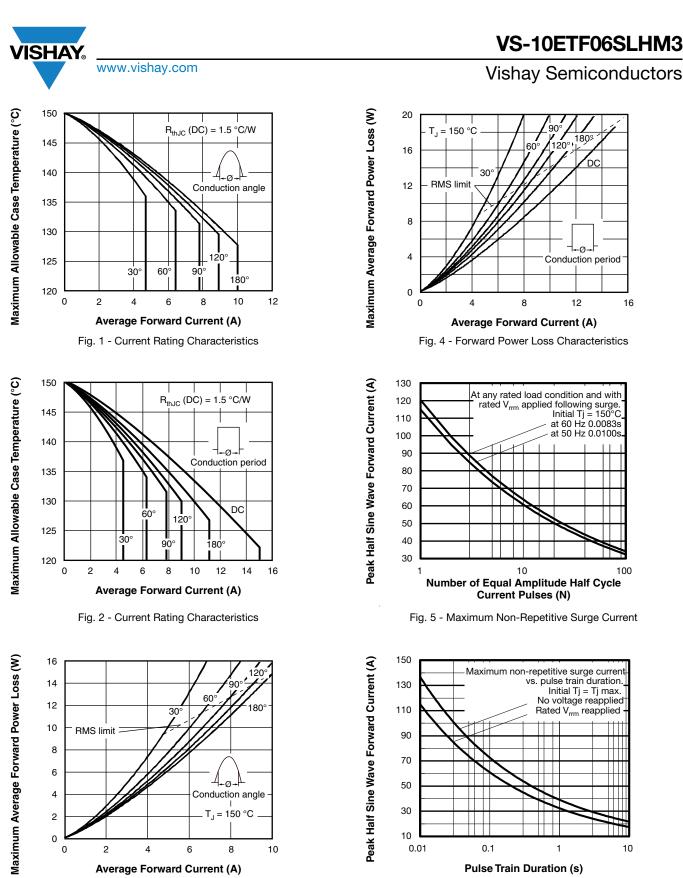


Fig. 3 - Forward Power Loss Characteristics Fig. 6 - Maximum Non-Repetitive Surge Current

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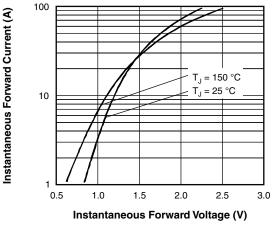


Fig. 7 - Forward Voltage Drop Characteristics

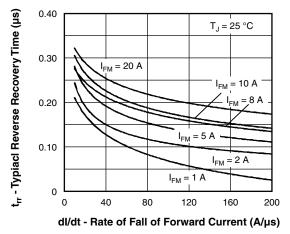


Fig. 8 - Recovery Time Characteristics, T_J = 25 °C

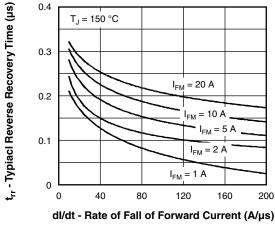
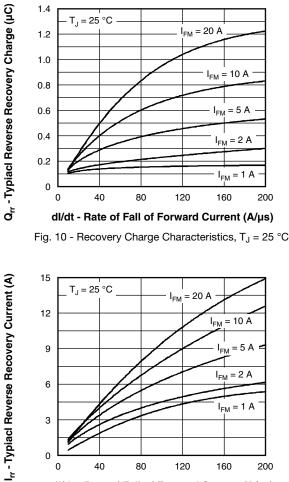
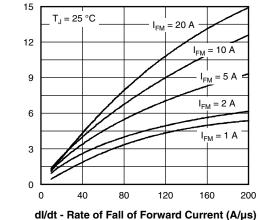


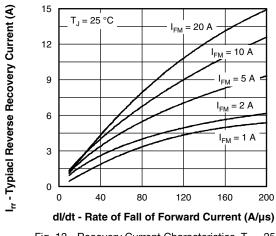
Fig. 9 - Recovery Time Characteristics, $T_J = 150$ °C

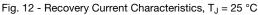
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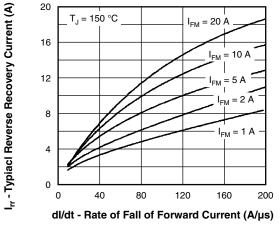
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VS-10ETF06SLHM3

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Fig. 13 - Recovery Current Characteristics, T_J = 150 °C

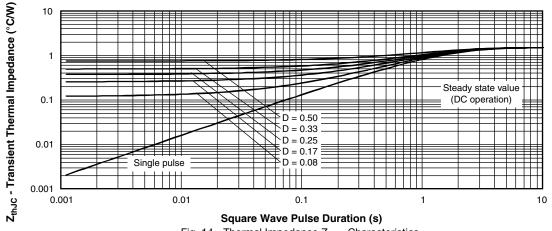


Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

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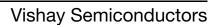
ORDERING INFORMATION TABLE

Device code	VS-	10	Е	т	F	06	s	L	н	М3	
		(2)	(3)	(4)		(6)		(8)			
	\bigcirc	(2)	9	4	(5)	\bigcirc	(7)	\odot	(9)	(10)	
	1	- Visl	nav Sen	nicondu	ctors pr	oduct					
	2	2 - Current rating (10 = 10 A)									
			Circuit configuration:								
			E = single								
	4	- Pac	kage:								
		T =	D ² PAK	(TO-26	3AB)						
	5	- Тур	e of sili	con:							
		F =	fast so	ft recove	ery recti	fier					
	6	- Vol	tage co	de x 100	$0 = V_{RRN}$	Λ	06 = 6	600 V			
	7	- S=	surface	e mounta	able						
	8		L = tape and reel (left oriented), for different orientation, contact factory								
	9	- H=	AEC-Q	101 qua	alified						
	10	- Env	vironme	ntal digi	t:						
		М3	= halog	en-free,	RoHS-	complia	ant, and	termina	ations le	ad (Pb)-	

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-10ETF06SLHM3	800	800	13" diameter reel					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95046					
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?96317					

Outline Dimensions

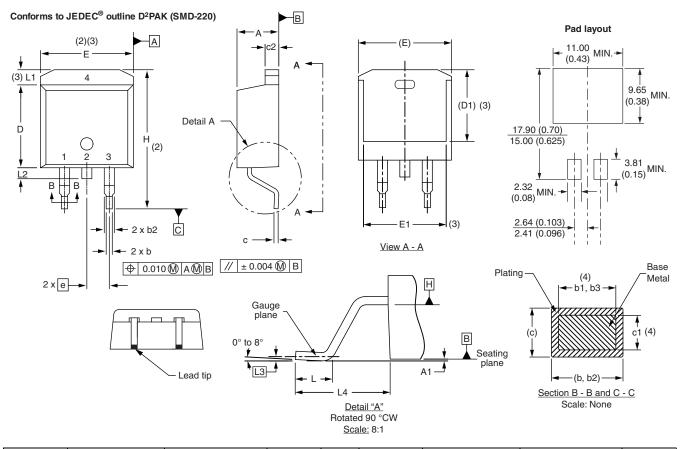


D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIM	IETERS	INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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