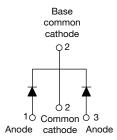


## Vishay Semiconductors

## Schottky Rectifier, 2 x 15 A



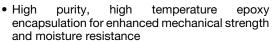


TO	-262

PRODUCT SUMMARY					
Package	TO-262AA				
I <sub>F(AV)</sub>	2 x 15 A				
$V_{R}$	30 V				
V <sub>F</sub> at I <sub>F</sub>	0.37 V				
I <sub>RM</sub>	350 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	15 mJ				

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Center tap configuration
- Very low forward voltage drop
- High frequency operation





ROHS COMPLIANT HALOGEN FREE

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Halogen-free according to IEC 61249-2-21 definition
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

#### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	30	A		
V <sub>RRM</sub>		30	V		
V <sub>F</sub>	15 Apk, T <sub>J</sub> = 125 °C (per leg)	0.37	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-30L30CT-1PbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	30	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	30	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum average per device forward current per leg			50 % duty cycle at T <sub>C</sub> = 140 °C, rectangular waveform		30	
		I <sub>F(AV)</sub>			15	
Maximum peak one cycle n	Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1450	A
surge current		IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	220	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 7.5 mH		15	mJ
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	Α

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# VS-30L30CT-1PbF

# Vishay Semiconductors

## Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		
	V <sub>FM</sub> <sup>(1)</sup>	15 A	T 05.00	0.46	V
Maximum forward voltage drap per leg		30 A	- T <sub>J</sub> = 25 °C	0.57	
Maximum forward voltage drop per leg		15 A	T 105 00	0.37	
		30 A	T <sub>J</sub> = 125 °C	0.50	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1.50	· mA
Maximum reverse leakage current per leg		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	350	
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	ge 100 kHz to 1 MHz), 25 °C	1500	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}</sup>$  Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C
Maximum thermal resistance, junction to case per leg		Б	DC operation	1.5	°C/W
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>		0.8	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mayorting toyaya				6 (5)	kgf · cm
Mounting torque –	maximum			12 (10)	(lbf $\cdot$ in)
Marking device			Case style TO-262	30L30	OCT-1



#### Schottky Rectifier, 2 x 15 A

## Vishay Semiconductors

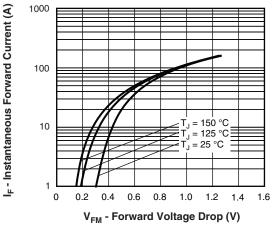


Fig. 1 - Maximum Forward Voltage Drop Characteristics

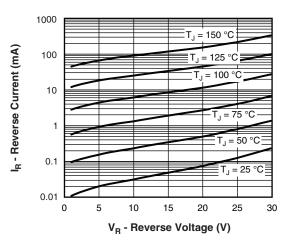


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

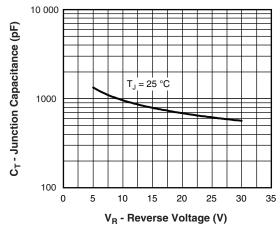


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

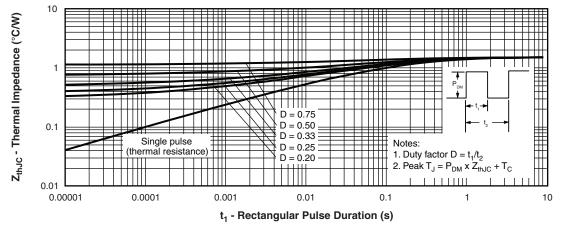


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

## Vishay Semiconductors

#### Schottky Rectifier, 2 x 15 A



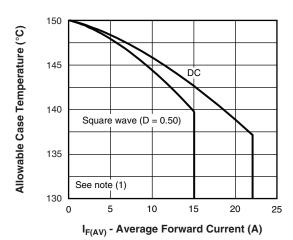


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

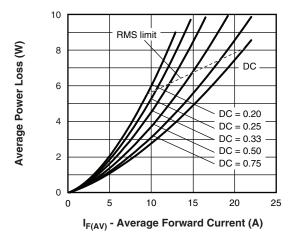


Fig. 6 - Forward Power Loss Characteristics

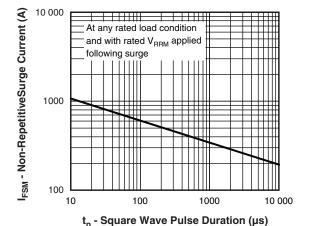


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

 $^{(1)}$  Formula used:  $T_C = T_J$  - Pd x  $R_{thJC};$  Pd = Forward power loss =  $I_{F(AV)}$  x  $V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6)

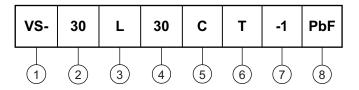


#### Schottky Rectifier, 2 x 15 A

## Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

Device code



Vishay Semiconductors product

2 - Current rating (30 A)

3 - L = Low V<sub>F</sub>

4 - Voltage rating (30 = 30 V)

5 - Circuit configuration: C = Common cathode

**6** - T = TO-220

**7** - -1 = TO-262

PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95419</u>			
Part marking information	www.vishay.com/doc?95420		
SPICE model <u>www.vishay.com/doc?95287</u>			

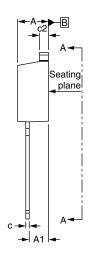


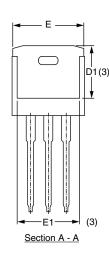
## Vishay Semiconductors

#### **TO-262**

#### **DIMENSIONS** in millimeters and inches

# 



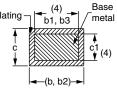


**♦**0.010**M**|**AM**|**B**|

#### Lead assignments



<u>Diodes</u>
1. - Anode (two die)/open (one die)
2., 4. - Cathode
3. - Anode



Section B - B and C - C Scale: None

CVMPOL	MILLIMETERS		INCH	NOTEO	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	2.54 BSC		BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

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Vishay

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