Vishay General Semiconductor

# Surface Mount Ultrafast Plastic Rectifier



SMB (DO-214AA)

2.0 A

100 V, 150 V, 200 V

25 ns

0.93 V

175 °C

DO-214AA (SMB)

Single

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

t<sub>rr</sub>

 $V_F$  at  $I_F = 2 A$ 

T<sub>J</sub> max.

Package

Circuit configuration

### **FEATURES**

- Glass passivated pellet chip junction
- · Ideal for automated placement
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power loss
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available Automotive ordering code: base P/NHE3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converter and inverter for both consumer and automotive.

## **MECHANICAL DATA**

Case: DO-214AA (SMB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3\_X - RoHS-compliant, AEC-Q101 qualified ("\_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

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Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	ESH2B	ESH2C	ESH2D	UNIT	
Device marking code		EHB	EHC	EHD		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	150	200	V	
Maximum RMS voltage	V <sub>RMS</sub>	70	105	140	V	
Maximum DC blocking voltage	V <sub>DC</sub>	100	150	200	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub>	2.0			А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60			А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +175			°C	





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	VALUE	UNIT	
Maximum instantaneous forward voltage	I <sub>F</sub> = 2 A		$V_{F}$ <sup>(1)</sup>	0.93	V	
Maximum DC reverse current at rated DC blocking voltage		T <sub>A</sub> = 25 °C	L_	2.0	μΑ	
		T <sub>A</sub> = 125 °C	I <sub>R</sub>	50		
Maximum reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A		t <sub>rr</sub>	25	ns	
Typical reverse recovery time	$I_F = 2 \text{ A}, V_R = 30 \text{ V}, \\ dI/dt = 50 \text{ A/}\mu\text{s}, I_{rr} = 10 \text{ \% } I_{RM}$	T <sub>J</sub> = 25 °C	- t <sub>rr</sub>	35	ns	
		T <sub>J</sub> = 100 °C		55		
Typical stored charge	$I_{\rm F} = 2 \Lambda, V_{\rm R} = 30 V,$	T <sub>J</sub> = 25 °C	- Q <sub>rr</sub>	20	nC	
		T <sub>J</sub> = 100 °C		35		
Typical junction capacitance	4.0 V, 1 MHz		CJ	30	pF	

#### Note

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	ESH2B	ESH2B ESH2C ESH2D		UNIT		
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>		°C/W				
	$R_{\theta JL}$ <sup>(1)</sup>						

#### Note

 $^{(1)}\,$  Units mounted on PCB with 8.0 mm x 8.0 mm land areas

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
ESH2D-E3/52T	0.096	52T	750	7" diameter plastic tape and reel		
ESH2D-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel		
ESH2DHE3_A/H <sup>(1)</sup>	0.096	Н	750	7" diameter plastic tape and reel		
ESH2DHE3_A/I <sup>(1)</sup>	0.096		3200	13" diameter plastic tape and reel		

Note

<sup>(1)</sup> AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

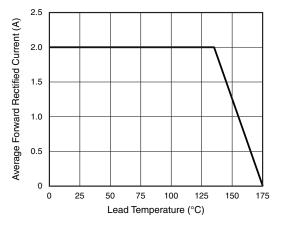


Fig. 1 - Maximum Forward Current Derating Curve

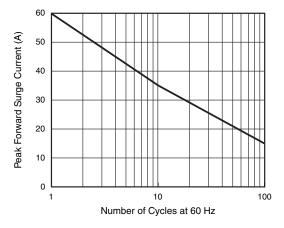


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

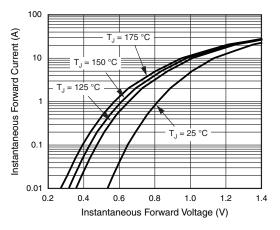


Fig. 3 - Typical Instantaneous Forward Characteristics

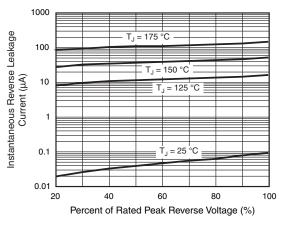


Fig. 4 - Typical Reverse Leakage Characteristics

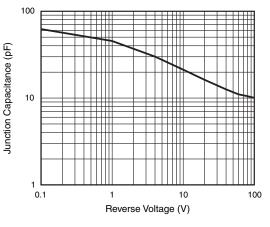


Fig. 5 - Typical Junction Capacitance

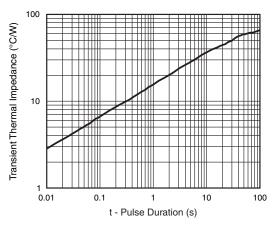


Fig. 6 - Typical Transient Thermal Impedance

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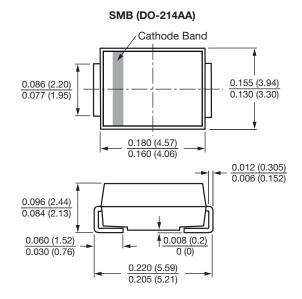
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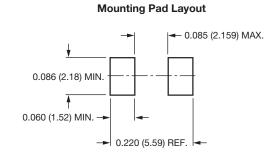


# ESH2B, ESH2C, ESH2D

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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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