

$$I_{F(AV)} = 40\text{Amp}$$

$$V_R = 40\text{V}$$

Major Ratings and Characteristics

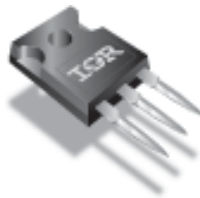
Characteristics	Value	Units
$I_{F(AV)}$ Rectangular waveform	40	A
V_{RRM}	40	V
I_{FSM} @ tp = 5 μ s sine	3500	A
V_F @ 20 Apk, $T_J=125^\circ\text{C}$ (per leg)	0.43	V
T_J	-55 to 150	$^\circ\text{C}$

Description/ Features

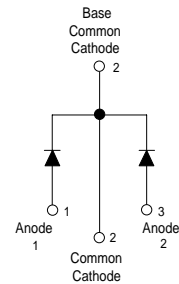
The STPS40L40CW center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150° C junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- 150° C T_J operation
- Center tap TO-247 package
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability

Case Styles



TO-247AC



Voltage Ratings

Part number	STPS40L40CW
V_R Max. DC Reverse Voltage (V)	40
V_{RWM} Max. Working Peak Reverse Voltage (V)	

Absolute Maximum Ratings

Parameters	Value	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current * See Fig. 5	40	A	50% duty cycle @ $T_C = 120^\circ\text{C}$, rectangular wave form
I_{FSM} Max. Peak One Cycle Non-Repetitive Surge Current (Per Leg) * See Fig. 7	3500	A	Following any rated load condition and with rated V_{RRM} applied
	430		
E_{AS} Non-Repetitive Avalanche Energy (Per Leg)	27	mJ	$T_J = 25^\circ\text{C}$, $I_{AS} = 4$ Amps, $L = 3.4$ mH
I_{AR} Repetitive Avalanche Current (Per Leg)	4	A	Current decaying linearly to zero in 1 μsec Frequency limited by T_J max. $V_A = 1.5 \times V_R$ typical

Electrical Specifications

Parameters	Value	Units	Conditions
V_{FM} Max. Forward Voltage Drop (Per Leg) * See Fig. 1 (1)	0.49	V	@ 20A
	0.59	V	@ 40A
	0.43	V	@ 20A
	0.56	V	@ 40A
I_{RM} Max. Reverse Leakage Current (Per Leg) * See Fig. 2 (1)	0.8	mA	$T_J = 25^\circ\text{C}$
	60	mA	$T_J = 100^\circ\text{C}$
C_T Max. Junction Capacitance(Per Leg)	1850	pF	$V_R = 5V_{DC}$, (test signal range 100Khz to 1Mhz) 25°C
L_S Typical Series Inductance (Per Leg)	7.5	nH	Measured lead to lead 5mm from package body
dv/dt Max. Voltage Rate of Change (Rated V_R)	10000	V/ μs	

(1) Pulse Width < 300 μs , Duty Cycle <2%

Thermal-Mechanical Specifications

Parameters	Value	Units	Conditions
T_J Max. Junction Temperature Range	-55 to 150	$^\circ\text{C}$	
T_{stg} Max. Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
R_{thJC} Max. Thermal Resistance Junction to Case (Per Leg)	1.25	$^\circ\text{C/W}$	DC operation * See Fig. 4
R_{thJC} Max. Thermal Resistance Junction to Case (Per Package)	0.63	$^\circ\text{C/W}$	DC operation
R_{thCS} Typical Thermal Resistance, Case to Heatsink	0.24	$^\circ\text{C/W}$	Mounting surface, smooth and greased
wt Approximate Weight	6 (0.21)	g (oz.)	
T Mounting Torque	Min.	6 (5)	Non-lubricated threads
	Max.	12 (10)	
Case Style	TO-247AC(TO-3P)	JEDEC	

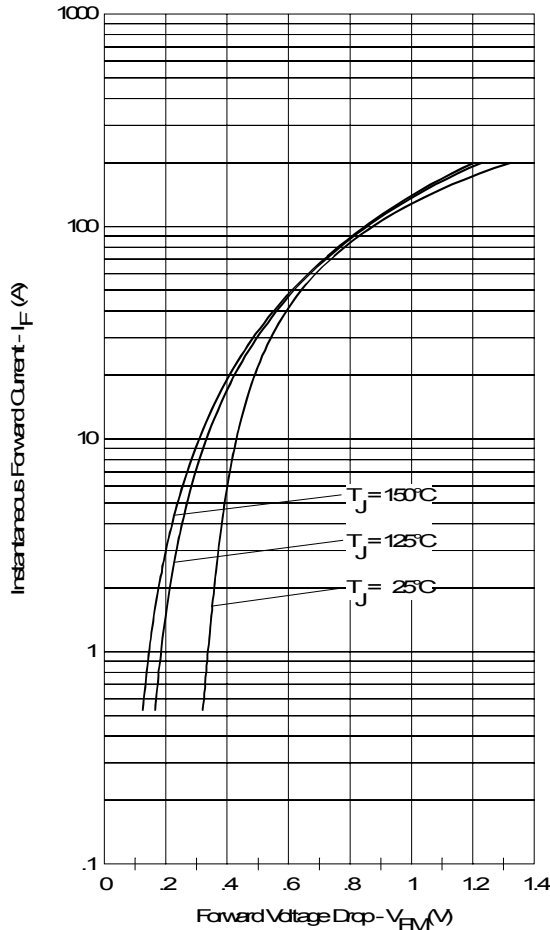


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

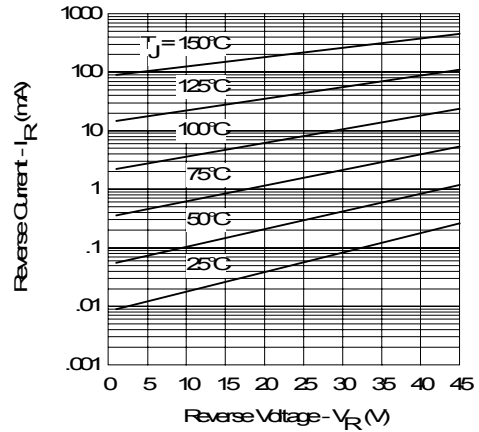


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

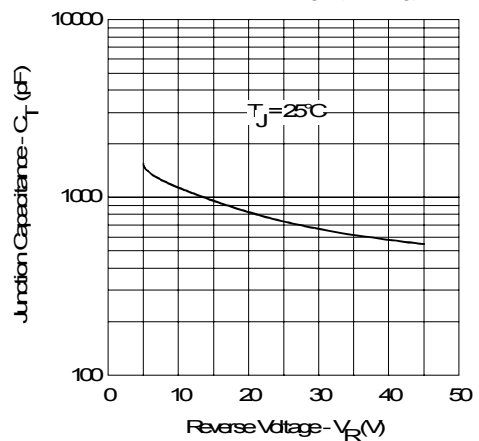


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

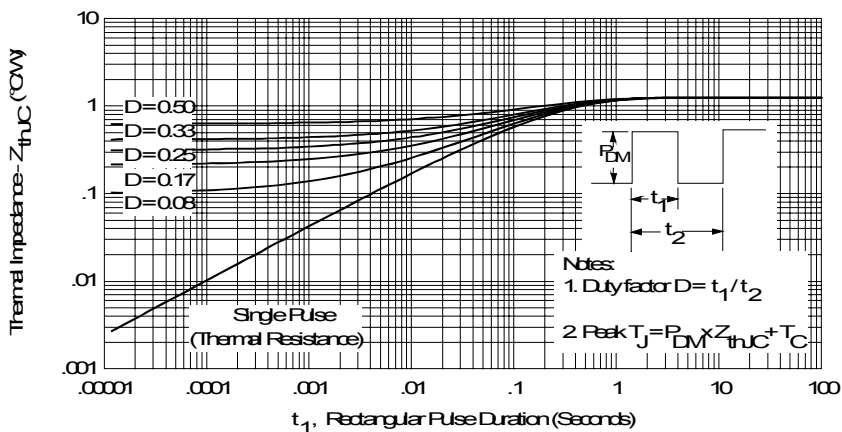


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

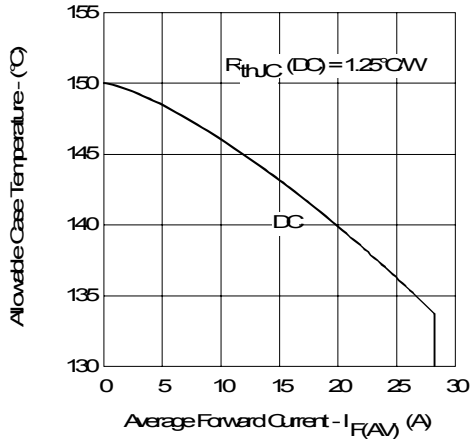


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

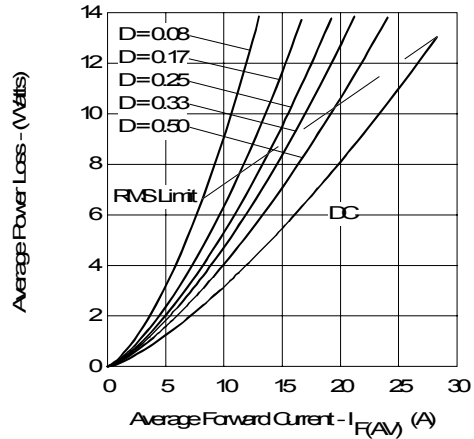


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

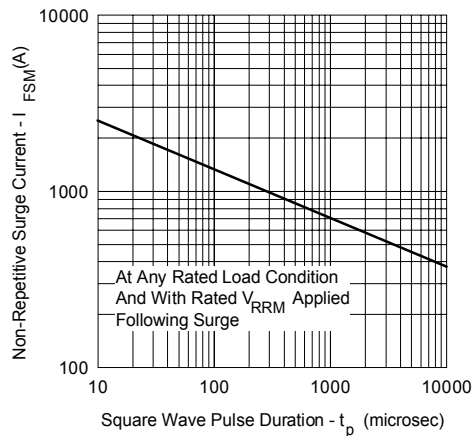


Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

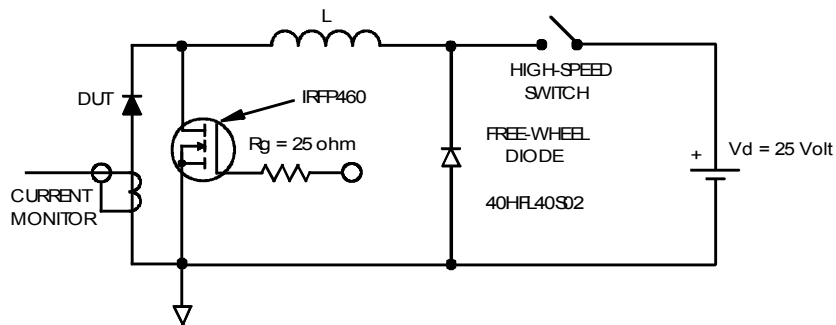


Fig. 8 - Unclamped Inductive Test Circuit

Outline Table

Conform to JEDEC outline TO-247AC (TO-3P)
Dimensions in millimeters and (inches)

SYMBOL	DIMENSIONS				NOTES
	INCHES		MILLIMETERS		
A	.185	.209	4.65	5.31	
A1	.087	.102	2.21	2.59	
A2	.059	.088	1.50	2.24	
b	.039	.050	0.99	1.40	
b1	.039	.053	0.99	1.35	
b2	.065	.094	1.65	2.39	
b3	.065	.087	1.65	2.24	
b4	.102	.135	2.59	3.43	
b5	.102	.133	2.59	3.38	
c	.015	.035	0.38	0.89	
c1	.015	.033	0.38	0.84	4
D	.778	.815	19.71	20.70	
D1	.515	-	13.08	-	4
D2	.530	-	13.46	-	4
E	.602	.625	15.29	15.87	4
E1	.530	-	13.46	-	
E2	.178	.218	4.52	5.49	
e	.215	BSC	5.46	BSC	
h	.032		0.79		
L	.550	6.54	14.20	16.10	
L1	.448	1.69	4.29	4.29	
L2	.140	.184	3.56	4.66	
ap	-	.291	-	7.39	
Q	.209	2.04	5.31	5.69	
S	.217	BSC	5.51	BSC	

LEAD ASSIGNMENTS

HEXCELL

1- GATE
2- COLLECTOR
3- SOURCE
4- DRAIN

WELDED CAPACES

1- GATE
2- COLLECTOR
3- EMITTER
4- COLLECTOR

DIODES

1- ANODE/OPEN
2- CATHODE
3- ANODE

Marking Information

EXAMPLE: THIS IS A STPS40L40CW WITH ASSEMBLY LOT CODE 5657 ASSEMBLED ON WW 35, 2000 IN ASSEMBLY LINE "H"

INTERNATIONAL RECTIFIER LOGO
ASSEMBLY LOT CODE
PART NUMBER
DATE CODE YEAR 0 = 2000 WEEK 35 LINE H

Ordering Information Table

Device Code													
	<table border="1"> <tr> <td>STPS</td> <td>40</td> <td>L</td> <td>40</td> <td>CW</td> <td>-</td> </tr> <tr> <td>①</td> <td>②</td> <td>③</td> <td>④</td> <td>⑤</td> <td>⑥</td> </tr> </table>	STPS	40	L	40	CW	-	①	②	③	④	⑤	⑥
STPS	40	L	40	CW	-								
①	②	③	④	⑤	⑥								
1	- Schottky STPS Series												
2	- Current Ratings (40 = 40A)												
3	- L = Low Forward Voltage												
4	- Voltage Code (40 = 40V)												
5	- Package CW = TO-247												
6	- <ul style="list-style-type: none"> • none = Standard Production • PbF = Lead-Free 												
Tube Standard Pack Quantity : 25 pieces													

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.



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单击下面可查看定价，库存，交付和生命周期等信息

[>>Vishay\(威世\)](#)