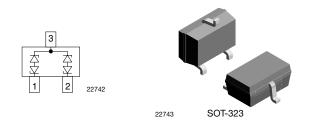
VCAN16A2-03G

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SHA

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Bidirectional Symmetrical (BiSy) Low Capacitance, Dual-Line ESD Protection Diode in SOT-323



MARKING (example only)



22744

ABC = type code (see table below) WW = date code working week VY = date code year

LINKS TO ADDITIONAL RESOURCES



FEATURES

- For CAN applicationsSmall SOT-323 package
- T_J max. = 175 °C
- 2-line ESD protection
- Working range ± 16 V
- Low leakage current $I_{\rm R}$ < 0.05 μ A
- Low load capacitance C_D < 17 pF
- ESD immunity acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

ORDERING I	INFORMATION							
	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
PART NUMBER (EXAMPLE)	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
		STANDARD	GREEN	FLAIED	15K/BOX = MOQ	10K/BOX = MOQ		
VCAN16A2-03G	-	E	-	3	-08	-	VCAN16A2-03G-E3-08	
VCAN16A2-03G	Н	E	-	3	-08	-	VCAN16A2-03GHE3-08	
VCAN16A2-03G	-	E	-	3	-	-18	VCAN16A2-03G-E3-18	
VCAN16A2-03G	Н	E	-	3	_	-18	VCAN16A2-03GHE3-18	

PACKAGE DATA								
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
VCAN16A2-03G	SOT-323	16A	5.65 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	T_A = 25 °C, acc. IEC 61000-4-5; t_p = 8/20 µs; single shot	I _{PPM}	5	А			
Peak pulse power	$T_A = 25$ °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s$; single shot	P _{PP}	140	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, T_A = 25 °C	V _{ESD}	± 30	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses, T_A = 25 °C	V ESD	± 30	kV			
Operating temperature	Junction temperature	TJ	-55 to +175	°C			
Storage temperature		T _{STG}	-55 to +175	°C			



RoHS

COMPLIANT

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ELECTRICAL CHARACTERISTICS (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	16	V		
Reverse voltage	At I _R = 0.05 μA	V _R	16	-	-	V		
Reverse current	At V _{RWM} = 16 V	I _R	-	-	0.05	μA		
Reverse breakdown voltage	At I _R = 1 mA	V _{BR}	17.1	18.6	20	V		
	At I _{PP} 1 A; t _p = 8/20 μs	V _C	-	20	2 16 - 0.05	V		
Reverse clamping voltage	At $I_{PP} = I_{PPM} = 5 \text{ A}$; $t_p = 8/20 \mu\text{s}$	V _C	-	25		V		
	At $V_R = 0 V$, f = 1 MHz	CD	14	15.5	17	pF		
Capacitance	Diode capacitance matching at $V_R = 0 V$, $C_{D13} vs. C_{D23}$	CD	-	-	1	pF		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

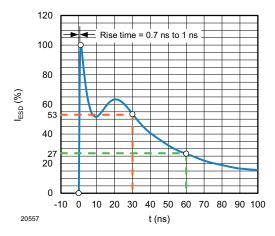


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

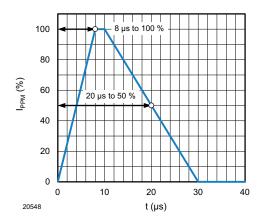


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

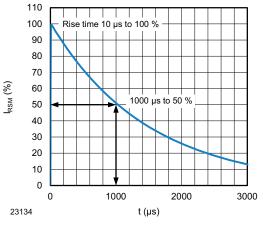
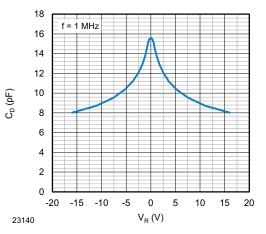
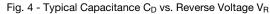


Fig. 3 - 10/1000 µs Peak Pulse Current Wave Form



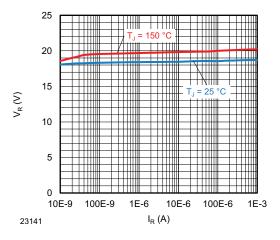


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SHAY

Fig. 5 - Typical Reverse Voltage V_R vs. Reverse Current I_R

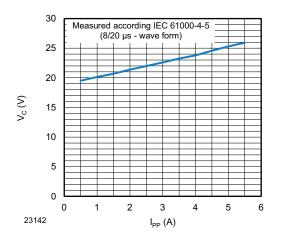


Fig. 6 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

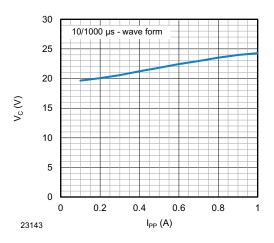


Fig. 7 - Typical Peak Clamping Voltage V_{C-TLP} vs. Peak Pulse Current I_{TLP}

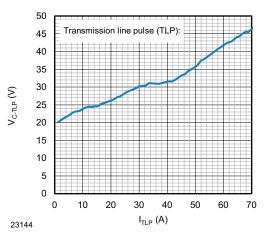


Fig. 8 - Typical Clamping Voltage V_{C-TLP} vs. Pulse Current I_{TLP}

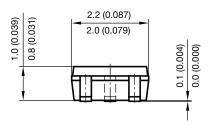
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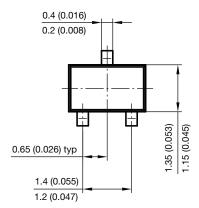
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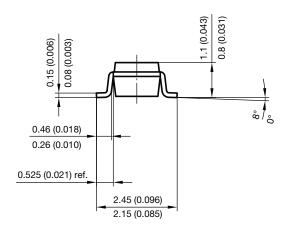
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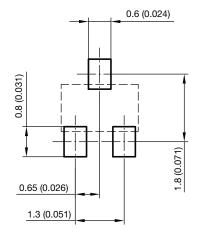
PACKAGE DIMENSIONS in millimeters (inches) SOT-323





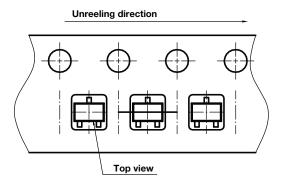


foot print recommendation:



Document no.: 6.541-5040.02-4 Rev. 1 - Date: 06. April 2010 21113

ORIENTATION IN CARRIER TAPE SOT-323



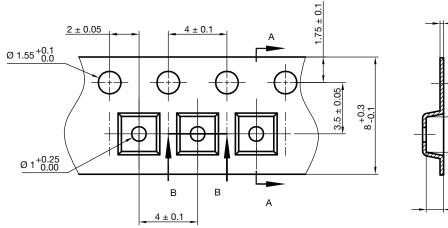
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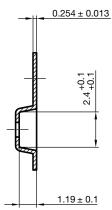
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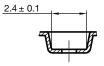
CARRIER TAPE SOT-323





A-A Section

B-B Section



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