RoHS

COMPLIANT

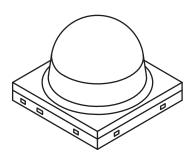
HALOGEN FREE

GREEN



# Vishay Semiconductors

# High Power Infrared Emitting Diode, 850 nm, Surface Emitter Technology



### **DESCRIPTION**

As part of the <u>SurfLight</u><sup>TM</sup> portfolio, the VSMY98525DS is an infrared, 850 nm emitting diode based on surface emitter technology with high radiant power and high speed, molded in low thermal resistance SMD package with lens. A 42 mil chip provides outstanding radiant intensity and allows DC operation of the device up to 1 A. Superior ESD characteristics are ensured by an integrated Zener diode.

## **FEATURES**

- Package type: surface-mount
- Double stack technology
- · Package form: power QFN
- Dimensions (L x W x H in mm): 3.85 x 3.85 x 3.00
- Peak wavelength:  $\lambda_p = 850 \text{ nm}$
- Zener diode for ESD protection up to 2 kV
- High radiant power
- · High radiant intensity
- Angle of half intensity:  $\varphi = \pm 25^{\circ}$
- Designed for high drive currents: up to 1 A (DC) and up to 5 A pulses
- Low thermal resistance: R<sub>thJP</sub> = 9 K/W
- Floor life: 168 h, MSL 3, according to J-STD-020
- · Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **APPLICATIONS**

- Infrared illumination for CMOS cameras (CCTV)
- · Illumination for cameras (3D gaming)
- Machine vision

PRODUCT SUMMARY				
COMPONENT	I <sub>e</sub> (mW/sr)	φ (deg)	λ <sub>p</sub> (nm)	t <sub>r</sub> (ns)
VSMY98525DS	1000	± 25	850	14

#### Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMY98525DS	Tape and reel	MOQ: 600 pcs, 600 pcs/reel	High power with lens		

#### Note

MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_{R}$	5	V	
Forward current		I <sub>F</sub>	1	Α	
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I <sub>FM</sub>	2	Α	
Surge forward current	t <sub>p</sub> = 100 μs	I <sub>FSM</sub>	5	Α	
Power dissipation		P <sub>V</sub>	3.5	W	
Junction temperature		Tj	115	°C	
Operating temperature range		T <sub>amb</sub>	-40 to +85	°C	
Storage temperature range		T <sub>stg</sub>	-55 to +100	°C	
Soldering temperature	According to Fig. 7, J-STD-20	T <sub>sd</sub>	260	°C	
Thermal resistance junction-to-pin	JESD 51	R <sub>thJP</sub>	9	K/W	



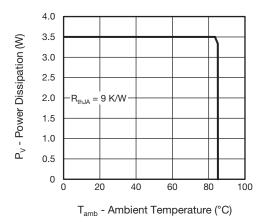


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

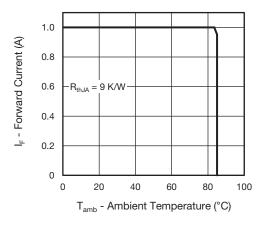


Fig. 2 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	$V_{F}$	-	3.1	3.5	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 1 A		-	-3	-	mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	-	-	10	μΑ
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz, E} = 0 \text{ mW/cm}^2$	CJ	-	130	-	pF
Radiant intensity	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	l <sub>e</sub>	800	1000	1600	mW/sr
Radiant power	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	фe	-	1300	-	mW
Temperature coefficient of φ	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	$TK_{\phi}$	-	-0.3	-	%/K
Angle of half intensity		φ	-	± 25	-	deg
Peak wavelength	I <sub>F</sub> = 1 A	$\lambda_{p}$	830	850	870	nm
Spectral bandwidth	I <sub>F</sub> = 1 A	Δλ	-	35	-	nm
Temperature coefficient of $\lambda_p$	$I_F = 1 \text{ A}, t_p = 20 \text{ ms}$	$TK_{\lambdap}$	-	0.3	-	nm/K
Rise time	I <sub>F</sub> = 1 A	t <sub>r</sub>	-	14	-	ns
Fall time	I <sub>F</sub> = 1 A	t <sub>f</sub>	-	17	-	ns

## BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

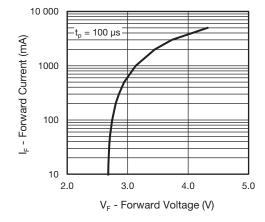


Fig. 3 - Forward Current vs. Forward Voltage

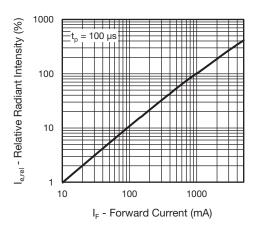
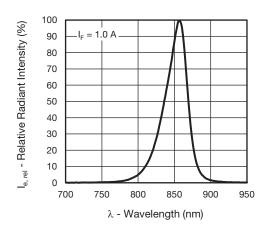
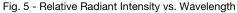


Fig. 4 - Relative Radiant Intensity vs. Forward Current





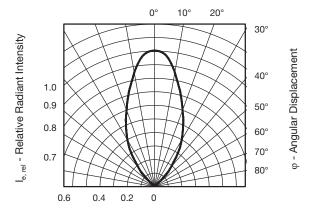
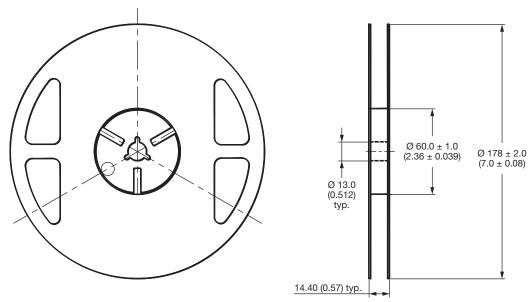


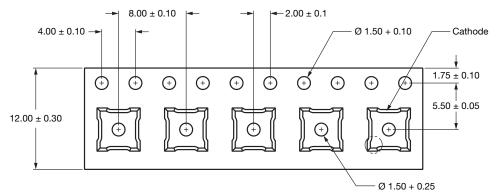
Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

## **TAPING DIMENSIONS** in millimeters



#### **Notes**

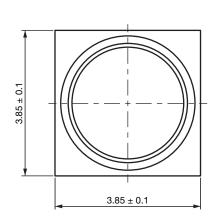
- Empty component pockets sealed with top cover tape
- 7 inch reel 600 pieces per reel
- The maximum number of consecutive missing lamps is two
- In accordance with ANSI/EIA 481-1-A-1994 specifications

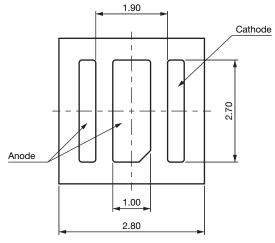


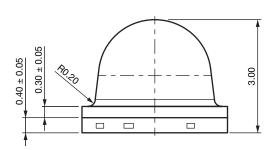
Rev. 1.0, 21-Mar-18 3 Document Number: 84908

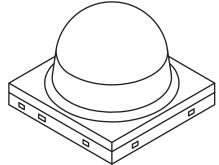


## **PACKAGE DIMENSIONS** in millimeters



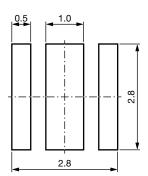






### Notes

- Tolerance is ± 0.10 mm (0.004") unless otherwise noted
- Specifications are subject to change without notice





## **SOLDER PROFILE**

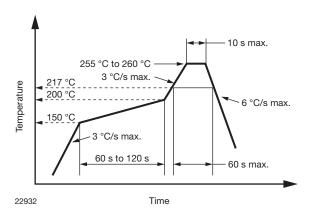


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

## **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-020B

### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



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