



N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A)	Q _g (Typ.)		
30	0.021 at V _{GS} = 10 V	12 ^a	3.7 nC		
30	0.033 at $V_{GS} = 4.5 \text{ V}$	6	0.7 110		

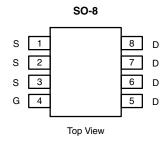
FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

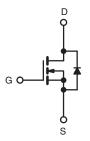


APPLICATIONS

- Notebook System Power
- Low Current DC/DC



Ordering Information: Si4178DY-T1-E3 (Lead (Pb)-free)



N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 25	V	
Continuous Drain Current (T _J = 150 °C)	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	I _D	12 ^a 9.7 ^a 8.3 ^{b, c} 6.7 ^{b, c}	
Pulsed Drain Current		I _{DM}	40	A
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}C$ $T_A = 25 ^{\circ}C$	I _S	4.2 2 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	10	
Single Pulse Avalanche Energy		E _{AS}	5	mJ
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	P _D	5 3.2 2.4 ^{b, c} 1.5 ^{b, c}	w
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	42	53	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	19	25	<i>5/</i> V V		

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 85 °C/W.

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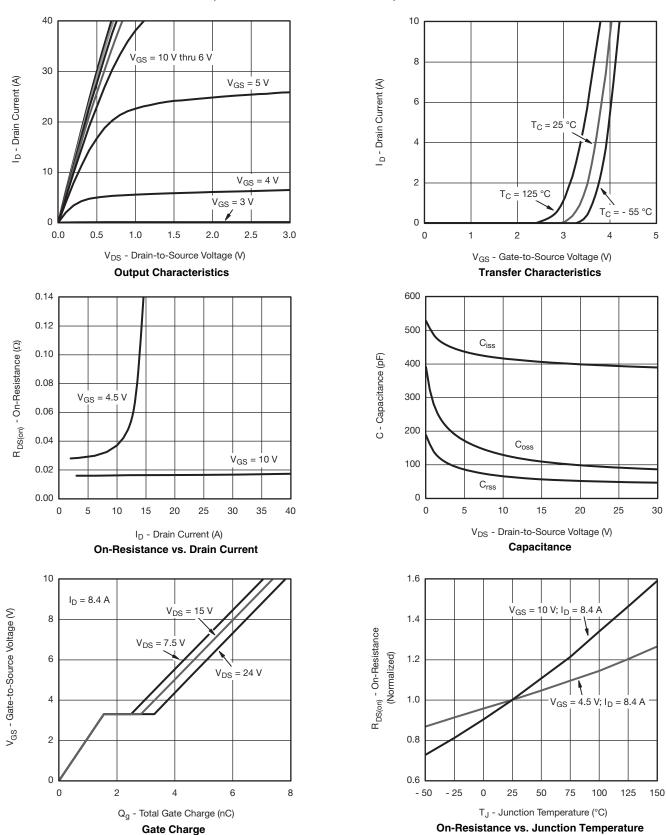
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					l.		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050 ·· A		25		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1.4		2.8	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA	
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	V _{GS} = 0 V		1	_	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	20			Α	
	, ,	$V_{GS} = 10 \text{ V}, I_D = 8.4 \text{ A}$		0.017	0.021		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 2 A		0.027	0.033	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 8.4 A		22		S	
Dynamic ^b	-				_		
Input Capacitance	C _{iss}			405			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		110		pF	
Reverse Transfer Capacitance	C _{rss}			56			
	0	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8.4 \text{ A}$		7.5	12		
Total Gate Charge	Q_g			3.7	5.6		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 8.4 \text{ A}$		1.6		nC	
Gate-Drain Charge	Q _{gd}			1.3		1	
Gate Resistance	R _g	f = 1 MHz	0.5	2.6	5.2	Ω	
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = 15 V, R_L = 2.2 Ω		15	25	1	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 6.7$ A, $V_{GEN}=4.5$ V, $R_g=1$ Ω		11	20		
Fall Time	t _f			10	15	1	
Turn-On Delay Time	t _{d(on)}			7	15	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 2.2 Ω		10	15		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong 6.7~\text{A},~\text{V}_\text{GEN}$ = 10 V, R_g = 1 Ω		12	20		
Fall Time	t _f			10	15		
Drain-Source Body Diode Characteristi	cs				•		
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			4.2	۸	
Pulse Diode Forward Current	I _{SM}				40	A	
Body Diode Voltage	V_{SD}	$I_S = 6.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.85	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			15	30	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 6.7 A, dI/dt = 100 A/μs, T _{.I} = 25 °C		8	16	nC	
Reverse Recovery Fall Time	t _a	$I_F = 6.7 \text{ A}, \text{ u/u} = 100 \text{ A/}\mu\text{s}, I_J = 25 \text{ °C}$		8.5			
Reverse Recovery Rise Time	t _b	_		6.5		ns	

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

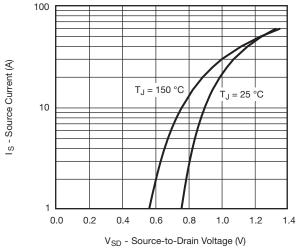


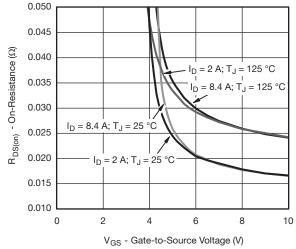
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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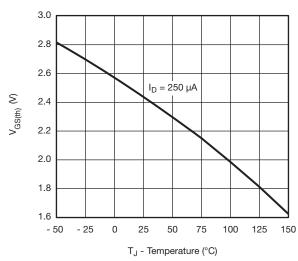
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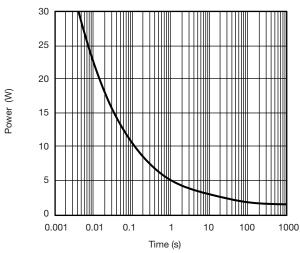




Source-Drain Diode Forward Voltage

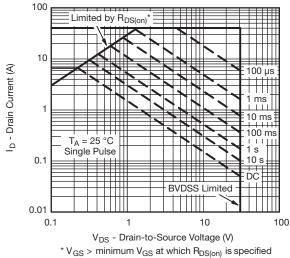






Threshold Voltage

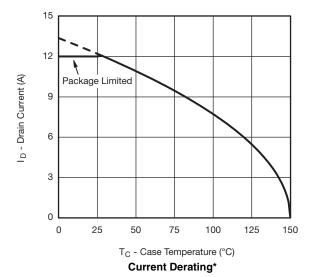
Single Pulse Power

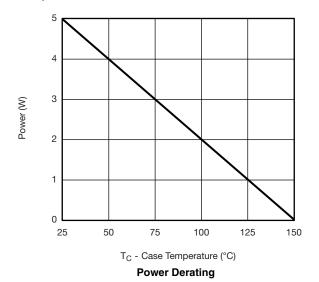


Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

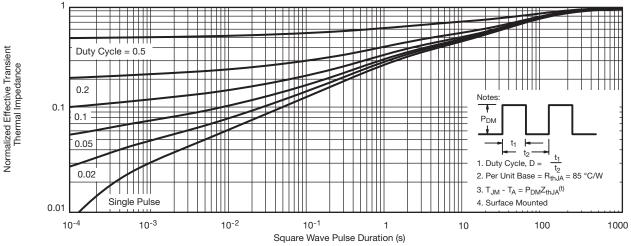




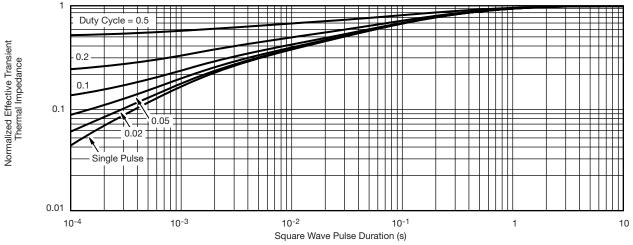
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



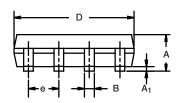
Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS INCHES			HES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27 BSC		0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
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DWG: 5498

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RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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