

# High Voltage, Input Rectifier Diode, 80 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> 80 A					
$V_{R}$	1600 V				
V <sub>F</sub> at I <sub>F</sub>	1.17 V				
I <sub>FSM</sub>	1150 A				
T <sub>J</sub> max.	150 °C				
Package	TO-247AC 3L				
Circuit configuration	Single				

#### **FEATURES**

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47





#### **APPLICATIONS**

- · Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

#### **DESCRIPTION**

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Sinusoidal waveform	80	A			
V <sub>RRM</sub>		1600	V			
I <sub>FSM</sub>		1150	А			
V <sub>F</sub>	80 A, T <sub>J</sub> = 25 °C	1.17	V			
T <sub>J</sub>		-40 to +150	°C			

VOLTAGE RATINGS			
PART NUMBER	V <sub>RRM</sub> , MAXIMUM PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I <sub>RRM</sub> AT 150 °C mA
VS-80APS16-M3	1600	1700	1

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 100 °C, 180° conduction half sine wave	80		
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	10 ms sine pulse, rated V <sub>RRM</sub> applied	965	Α	
		10 ms sine pulse, no voltage reapplied	1150		
Maximum 12t for funing	l <sup>2</sup> t	10 ms sine pulse, rated V <sub>RRM</sub> applied	4655	Λ2α	
Maximum I <sup>2</sup> t for fusing	I <sup>-</sup> t	10 ms sine pulse, no voltage reapplied	6585	A <sup>2</sup> s	
Maximum I <sup>2</sup> √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	65 850	A²√s	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CON	IDITIONS	VALUES	UNITS
Maximum forward voltage drop	$V_{FM}$	80 A, T <sub>J</sub> = 25 °C		1.17	V
Forward slope resistance	r <sub>t</sub>	T 450.00		3.17	mΩ
Threshold voltage	V <sub>F(TO)</sub>	T <sub>J</sub> = 150 °C		0.73	V
Maximum reverse leakage current	_	T <sub>J</sub> = 25 °C		0.1	mA
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 150 °C	V <sub>R</sub> = Rated V <sub>RRM</sub>	1.0	IIIA

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	)	T <sub>J</sub> , T <sub>Stg</sub>		-40 to +150	°C
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	0.35	
Maximum thermal resistance, junction to ambient		R <sub>thJA</sub>		40	°C/W
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.2	
Approximate weight				6	g
Approximate weight				0.21	OZ.
Mounting toyour	minimum			6 (5)	kgf ⋅ cm
Mounting torque	maximum			12 (10)	(lbf · in)
Marking device Case style TO-247AC 3L 80APS1		PS16			

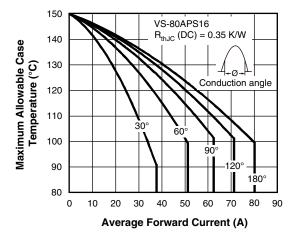


Fig. 1 - Current Rating Characteristics

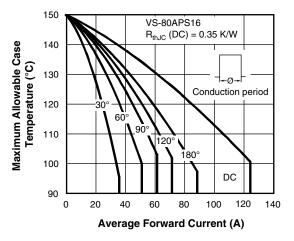


Fig. 2 - Current Rating Characteristics



### www.vishay.com

## Vishay Semiconductors

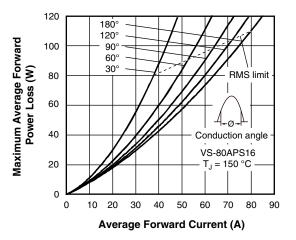


Fig. 3 - Forward Power Loss Characteristics

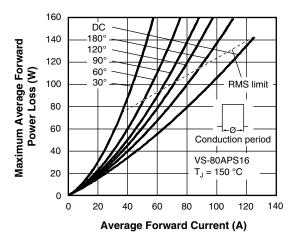


Fig. 4 - Forward Power Loss Characteristics

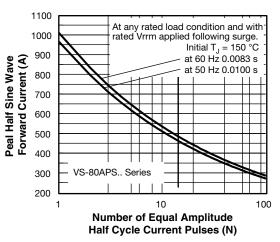


Fig. 5 - Maximum Non-Repetitive Surge Current

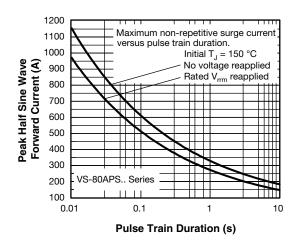


Fig. 6 - Maximum Non-Repetitive Surge Current

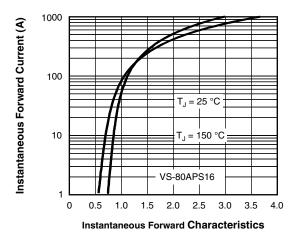


Fig. 7 - Forward Voltage Drop Characteristics

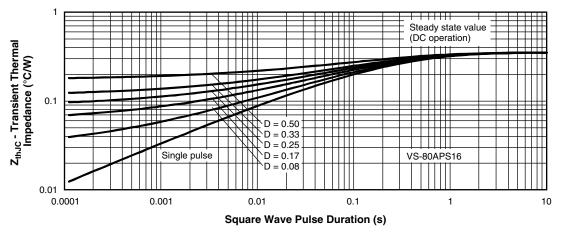
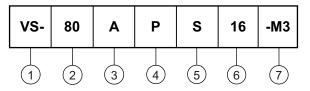


Fig. 8 - Thermal Impedance Z<sub>thJC</sub> Characteristics

### **ORDERING INFORMATION TABLE**

### **Device code**



- Vishay Semiconductors product
- 2 Current rating (80 = 80 A)
- 3 Circuit configuration:

A = single diode, 3 pins

- 4 Package:
  - P = TO-247AC 3L
- 5 Type of silicon:

S = standard recovery rectifier

- 6 Voltage rating (16 = 1600 V)
- 7 Environmental digit:

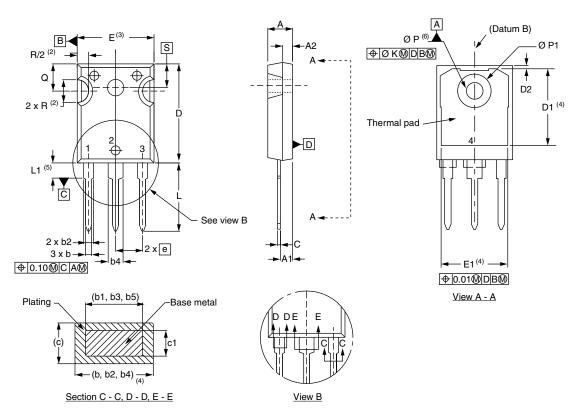
-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-80APS16-M3	25	500	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96138
Part marking information	www.vishay.com/doc?95007
SPICE model	www.vishay.com/doc?96695

### **TO-247AC 3L**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
OTHIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	0.254		)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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