Vishay General Semiconductor

TRANSZORB[®] Transient Voltage Suppressors



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| PRIMARY CHARACTERISTICS | | | | |
|---------------------------------|------------------|--|--|--|
| V _{WM} | 477 V, 495 V | | | |
| V _{BR} uni-directional | 530 V, 550 V | | | |
| P _{PPM} | 300 W | | | |
| PD | 1.0 W | | | |
| V _C | 760 A | | | |
| T _J max. | 150 °C | | | |
| Polarity | Uni-directional | | | |
| Package | DO-41 (DO-204AL) | | | |

FEATURES

- Glass passivated chip junction
- · Available in uni-directional only
- Excellent clamping capability
- · Very fast response time
- Low incremental surge resistance
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lighting on ICs, MOSFET, signal lines of sensor units for consumer, computer, industrial, and telecommunication.

MECHANICAL DATA

Case: DO-41 (DO-204L), molded epoxy over passivated chip

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: color band denotes cathode end

| MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted) | | | | | |
|--|-----------------------------------|-------------|--|------|--|
| PARAMETER SYMBOL P4KE530 P4KE550 | | | | UNIT | |
| Peak pulse power dissipation ⁽¹⁾⁽²⁾ (fig.1) | P _{PPM} | 300 | | W | |
| Power dissipation on infinite heatsink at $T_L = 75$ °C (fig. 4) | PD | 1.0 | | W | |
| Operating junction and storage temperature range | T _J , T _{STG} | -55 to +150 | | °C | |

Notes

⁽¹⁾ Non-repetitive current pulse, per fig. 3 and derated above $T_A = 25$ °C per fig. 2

⁽²⁾ Peak pulse power waveform is 10/1000 µs

| ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | |
|---|--|-----|---|--|
| DEVICE TYPE | BREAKDOWN VOLTAGE V _{BR} AT I _T (V) (µA) BREAKDOWN VOLTAGE TEST CURRENT I _T (µA) | | STAND-OFF VOLTAGE V _{WM} (V) | |
| | MIN. | (μ) | (•) | |
| P4KE530 | 530 | 100 | 477 | |
| P4KE550 | 550 | 100 | 495 | |

| ADDITIONAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | | |
|--|-------------------------------|----------------|---------|---------|-------|
| PARAMETER | TEST CONDITIONS | SYMBOL | P4KE530 | P4KE550 | UNIT |
| Max. clamping voltage | 400 mA, 10/1000 µs waveform | V _C | 76 | 60 | V |
| Maximum DC reverse leakage current | at V _{WM} | I _D | 1. | 0 | μA |
| Typical temperature coefficient | of V _{BR} | | 65 | 50 | mV/°C |
| Typical capacitance | 1 MHz, V _R = 0 V | CJ | 90 | | pF |
| | 1 MHz, V _R = 200 V | CJ | 7.5 | | pF |

Revision: 04-Mar-2019



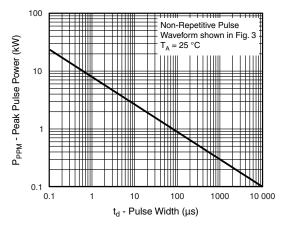


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| THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted) | | | | |
|--|---------------------|-------|------|--|
| PARAMETER | SYMBOL | VALUE | UNIT | |
| Typical thermal resistance, junction to lead | $R_{	ext{	heta}JL}$ | 75 | °C/W | |
| Typical thermal resistance, junction to ambient | $R_{	ext{	heta}JA}$ | 125 | C/ W | |

| ORDERING INFORMATION (Example) | | | | | |
|--------------------------------|-----------------|------------------------|---------------|----------------------------------|--|
| PREFERRED PIN | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE | |
| P4KE530-E3/54 | 0.350 | 54 | 5500 | 13" diameter paper tape and reel | |
| P4KE550-E3/54 | 0.350 | 54 | 5500 | 13" diameter paper tape and reel | |

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)





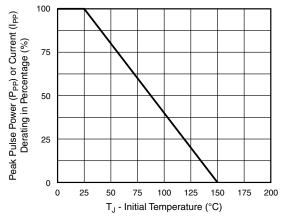


Fig. 2 - Pulse Power or Current vs. Initial Junction Temperature

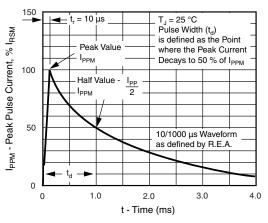


Fig. 3 - Pulse Waveform

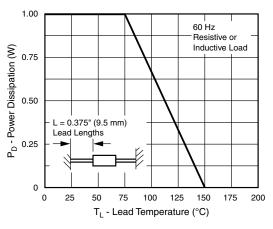


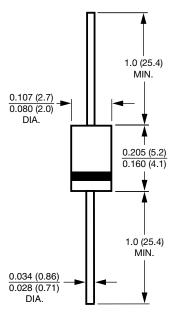
Fig. 4 - Pulse Derating Curve



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters) DO-41 (DO-204AL)



APPLICATION NOTES

- Respect thermal resistance (PCB Layout) as the temperature coefficient also contributes to the clamping voltage
- Select minimum breakdown voltage, so you get acceptable power dissipation and PCB tie point temperature. Devices with higher breakdown voltage will have a shorter conduction time and will dissipate less power
- · Clamping voltage is influenced by internal resistance design approximation is 7 V per 100 mA slope
- Keep temperature of TVS lower than TOPSwitch® as a recommendation
- Maximum current is determined by the maximum T_J and can be higher than 300 mA. Contact supplier for different clamping voltage/current arrangements
- Minimum breakdown voltage can be customized for other applications. Contact supplier
- TOPSwitch® is a registered trademark of Power Integrations, Inc.



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