

## Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.375 \text{ V}$  at  $I_F = 5 \text{ A}$ 



TMBS <sup>®</sup>
23
TO-247AD (TO-3P)
PIN 1 OPIN 2
PIN 3 O CASE

#### **FEATURES**





· Low forward voltage drop, low power losses



· High efficiency operation

- · Low thermal resistance
- Solder dip 260 °C, 40 s
- · Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **TYPICAL APPLICATIONS**

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

### **MECHANICAL DATA**

Case: TO-247AD (TO-3P)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class

1A whisker test Polarity: As marked

Mounting Torque: 10 in-lbs Maximum

<sup>I</sup> F(AV)	2 X 25 A
V <sub>RRM</sub>	100 V
I <sub>FSM</sub>	350 A
V <sub>F</sub> at I <sub>F</sub> = 25 A	0.64 V
T <sub>J</sub> max.	150 °C

**PRIMARY CHARACTERISTICS** 

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V50100P	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	V
Maximum average forward rectified current (Fig. 1)	per device per diode	I <sub>F(AV)</sub>	50 25	А
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	per diode	I <sub>FSM</sub>	350	А
Peak repetitive reverse current per diode at $t_p$ = 2 $\mu$ s,	1 kHz	I <sub>RRM</sub>	1.0	Α
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>J</sub> = 25 °C	V <sub>BR</sub>	100	-	V
Instantaneous forward voltage per diode <sup>(1)</sup>	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A I <sub>F</sub> = 25 A	T <sub>J</sub> = 25 °C	V <sub>F</sub>	0.463 0.535 0.664 0.700	- - - 0.78	V
	I <sub>F</sub> = 5 A I <sub>F</sub> = 10 A I <sub>F</sub> = 20 A I <sub>F</sub> = 25 A	T <sub>J</sub> = 125 °C		0.375 0.445 0.605 0.635	- - - 0.70	
Reverse current per diode (2)	V <sub>R</sub> = 70 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub>	13.7 8.4	500 15	μA mA
	V <sub>R</sub> = 100 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C		69.6 22.5	1000 45	μA mA

#### Notes:

(1) Pulse test: 300  $\mu$ s pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V50100P	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	1.5	°C/W	

ORDERING INFORMATION						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V50100P-E3/45	6.056	45	30/tube	Tube		

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

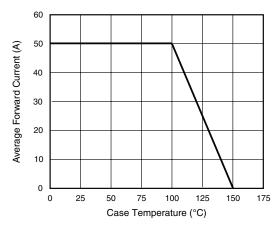


Figure 1. Forward Current Derating Curve

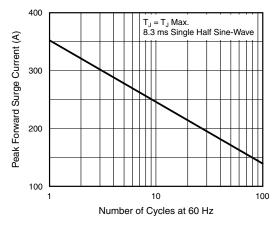


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Diode



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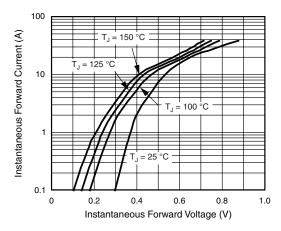


Figure 3. Typical Instantaneous Forward Characteristics Per Diode

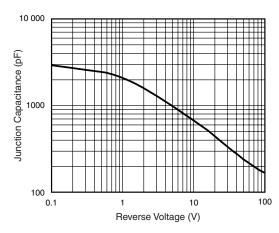


Figure 5. Typical Junction Capacitance Per Diode

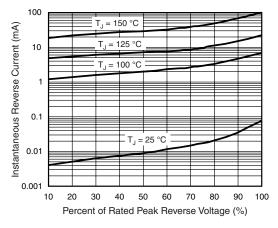


Figure 4. Typical Reverse Characteristics Per Diode

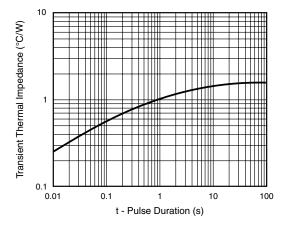
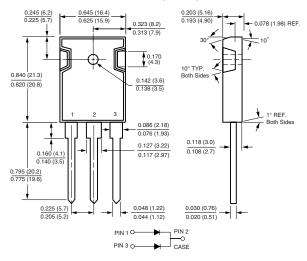


Figure 6. Typical Transient Thermal Impedance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### TO-247AD (TO-3P)



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