Vishay General Semiconductor

Dual High Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.57$ V at $I_F = 5$ A



PIN 1 0 PIN 2 CASE PIN 3 O

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _{RRM}	100 V			
I _{FSM}	110 A			
V _F at I _F = 10 A (T _A = 125 °C)	0.67 V			
T _J max.	175 °C			
Package	TO-220AB			
Diode variations	Common cathode			

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106 RoHS
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As markedp

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20M100M	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	100	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	20	٨	
	per diode		10	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	110	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-55 to +175	°C	



COMPLIANT





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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	– T _A = 25 °C	V _F ⁽¹⁾	0.65	-	V	
	$I_F = 10 \text{ A}$			0.83	1.02		
	I _F = 5 A	– T _A = 125 °C		0.57	-		
	I _F = 10 A			0.67	0.75		
Reverse current per diode	V _R = 70 V	T _A = 25 °C	I _R (2)	1.5	-	μA	
		T _A = 125 °C		0.8	-	mA	
	V _R = 100 V	T _A = 25 °C		-	500	μA	
	v _R = 100 v	T _A = 125 °C		1.5	10	mA	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)					
PARAMETER		SYMBOL	V20M100M	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	2.8	°C/W	
	per device		1.4		
	per device	R _{0JA} (1)(2)	40		

Notes

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient dP_D/dT_J < 1/R_{θ JA}

⁽²⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	V20M100M-E3/4W	1.88	4W	50/tube	Tube	



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

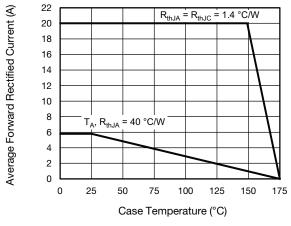


Fig. 1 - Maximum Forward Current Derating Curve

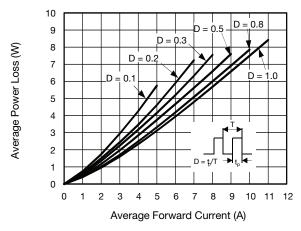


Fig. 2 - Forward Power Loss Characteristics Per Diode

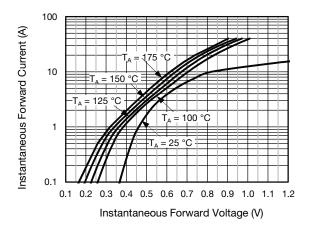


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

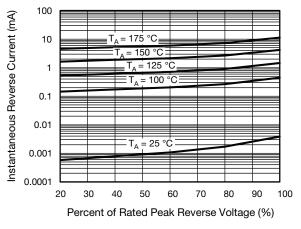


Fig. 4 - Typical Reverse Characteristics Per Diode

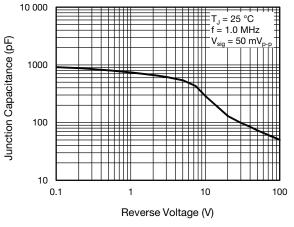


Fig. 5 - Typical Junction Capacitance Per Diode

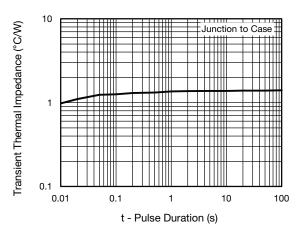


Fig. 6 - Typical Transient Thermal Impedance Per Device

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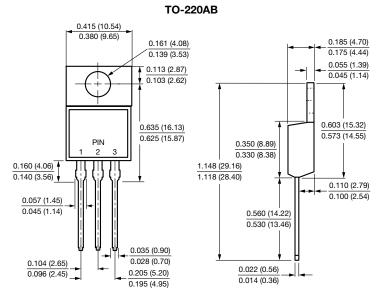
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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