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Vishay Semiconductors

Thyristor High Voltage, Phase Control SCR, 50 A



PRIMARY CHARACTERISTICS							
I _{T(AV)} 50 A							
V_{DRM}/V_{RRM}	1200 V						
V _{TM} (typ.)	1.1 V						
I _{GT} (typ.)	35 mA						
T_J	-40 °C to +150 °C						
Package	TO-247AD 3L						
Circuit configuration	Single SCR						

FEATURES

 Designed and qualified according to JEDEC®-JESD 47



FREE

Low I_{GT} designed

• 150 °C maximum operating junction temperature

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

APPLICATIONS

Typical usage is in input rectification crowbar (soft start) and AC switch motor control, UPS, welding, and battery charge.

DESCRIPTION

The VS-50TPS12 high voltage series of silicon controlled rectifiers are specifically designed for medium power switching, and phase control applications. The glass passivation technology used, has reliable operation up to 150 °C junction temperature.

MAJOR RATINGS AND CHARACTERISTICS								
PARAMETER	TEST CONDITIONS	VALUES	UNITS					
V _{RRM} /V _{DRM}		1200	V					
V _T	50 A, T _J = 125 °C	1.1	V					
I _{T(AV)}		50						
I _{RMS}		79	Α					
I _{TSM}		630						
dV/dt		500	V/µs					
T _J , T _{Stg}		-40 to +150	°C					

VOLTAGE RATINGS								
PART NUMBER	V _{RRM} /V _{DRM} , MAXIMUM REPETITIVE PEAK AND OFF-STATE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA					
VS-50TPS12AL-M3	1200	1300	10					



ABSOLUTE MAXIMUM RATINGS									
DADAMETED	CVMDOL	TEST CONDITIONS	TEST COMPLETIONS			UNITS			
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS			
Maximum average on-state current	I _{T(AV)}	T _C = 112 °C, 180° conduction half sine wa	ive	-	50				
Maximum continuous RMS on-state current as AC switch	I _{T(RMS)}			-	79	Α			
Peak, one-cycle non-repetitive surge current	l	10 ms sine pulse, rated V _{RRM} applied		-	530				
reak, one-cycle non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	Initial $T_J = T_J$	-	630				
I ² t for fusing	I ² t	10 ms sine pulse, rated V _{RRM} applied	maximum	-	1405	A ² s			
I-t for fusing	1-1	10 ms sine pulse, no voltage reapplied		-	1986				
$I^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied,	T _J = 125 °C	-	19 850	A²√s			
Low level value of threshold voltage	V _{T(TO)1}			-	0.89	V			
High level value of threshold voltage	V _{T(TO)2}	T _J = 125 °C		-	0.97	, v			
Low level value of on-state slope resistance	r _{t1}	1j=125 C		-	6.77	mΩ			
High level value of on-state slope resistance	r _{t2}			-	6.32				
On atota valtaga	V	50 A, T _J = 25 °C			1.32	V			
On-state voltage	V _T	100 A, T _J = 25 °C			1.6	V			
Rate of rise of turned-on current	dl/dt	T _J = 25 °C		-	150	A/µs			
Holding current	I _H	Anada ayanlır. C.V. raniatiya laad. T. OF	. 00	-	300				
Latching current	ΙL	Anode supply = 6 V, resistive load, T _J = 25 °C		-	350	mA			
Deverse and direct leakage current	1/1	T _J = 25 °C		-	0.05	111/4			
Reverse and direct leakage current	I _{RRM} /I _{DRM}	T _J = 125 °C			10				
Rate of rise of off-state voltage	dV/dt	T _J = T _J maximum, linear to 80 % V _{DRM} , R _g	_j -k = 100 Ω	-	500	V/µs			

TRIGGERING									
PARAMETER	SYMBOL		TEST CONDITIONS	TYP.	MAX.	UNITS			
Peak gate power	P _{GM}	10 ma aina nula	se, no voltage reapplied	-	10	W			
Average gate power	P _{G(AV)}	TO THIS SITTLE PURS	se, no voltage reapplied	-	2.5	l vv			
Peak gate current	I _{GM}			-	2.5	Α			
Peak negative gate voltage	-V _{GM}			-	10				
		T _J = -40 °C		-	1.6	V			
Required DC gate voltage to trigger	V_{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	-	1.5	V			
		T _J = 150 °C		-	1				
		T _J = -40 °C		-	100				
Required DC gate to trigger	I _{GT}	T _J = 25 °C	Anode supply = 6 V resistive load	35	60	mA			
		T _J = 150 °C		-	40				
DC gate voltage not to trigger	V_{GD}	T _{.I} = 150 °C, V _{DBM} = rated value			0.15	V			
DC gate current not to trigger	I_{GD}	$I_{\rm J} = 150^{\circ} \rm C, V_{\rm D}$	RM = rated value	-	2.5	mA			

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Turn-on time	t _{gt}	$I_T = 50 \text{ A}, V_D = 50 \% V_{DRM}, I_{gt} = 300 \text{ mA}, T_J = 25 ^{\circ}\text{C}$	1.5	
Turn-off time	t _q	$\begin{array}{l} I_T = 50 \text{ A, V}_D = 80 \text{ \% V}_{DRM}, dV/dt = 20 \text{ V/}\mu\text{s, t}_p = 200 \mu\text{s} \\ I_{gt} = 100 \text{ mA, dI/dt} = 10 A/\mu\text{s, V}_R = 100 \text{V, T}_J = 150 ^{\circ}\text{C} \end{array}$	92	μs



THERMAL AND MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	TYP.	MAX.	UNITS			
Maximum junction and storage	Maximum junction and storage temperature range			-40	150	°C			
Maximum thermal resistance, junction to case		R _{thJC}		-	0.35				
Maximum thermal resistance, ju	Maximum thermal resistance, junction to ambient			-	40	°C/W			
Typical thermal resistance, case	to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.2	-				
Mounting torque	minimum			6	(5)	kgf · cm			
Mounting torque	maximum			12 (10)		(lbf · in)			
Marking device			Case style Super TO-247AD 3L	,	50TPS12A	L			

△R _{thJ-HS} CONDUCTION PER JUNCTION											
SINE HALF-WAVE CONDUCTION				RECTANGULAR WAVE CONDUCTION				LINUTC			
DEVICE	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	UNITS
VS-50TPS12L-M3	0.143	0.166	0.208	0.299	0.490	0.099	0.168	0.223	0.311	0.494	°C/W

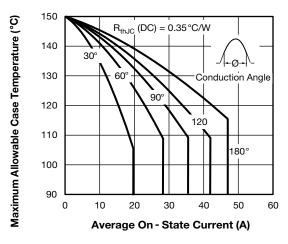


Fig. 1 - Current Rating Characteristics

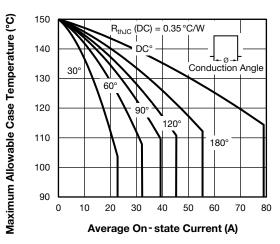


Fig. 2 - Current Rating Characteristics

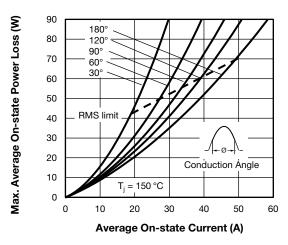


Fig. 3 - On-State Power Loss Characteristics

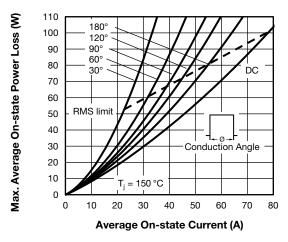


Fig. 4 - On-State Power Loss Characteristics

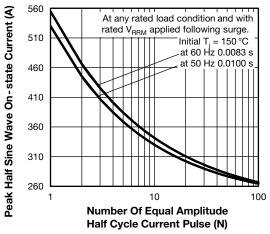


Fig. 5 - Maximum Non-Repetitive Surge Current

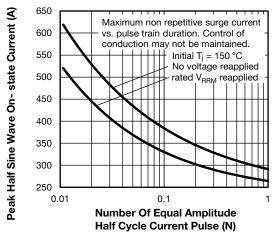


Fig. 6 - Maximum Non-Repetitive Surge Current

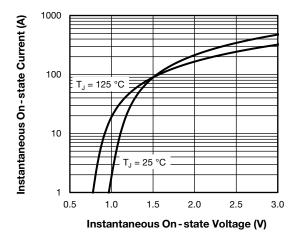


Fig. 7 - On-State Voltage Drop Characteristics

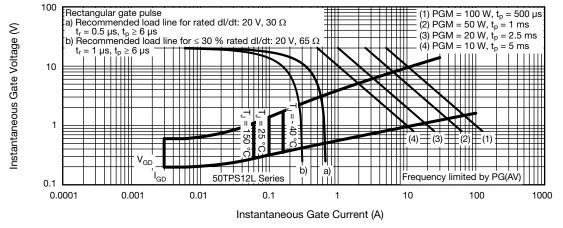


Fig. 8 - Gate Characteristics

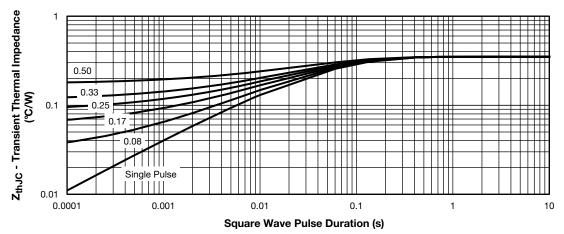


Fig. 9 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	VS-	50	Т	Р	s	12	Α	L	-M3
		(2)	(3)	(4)	(5)	6	(7)	(8)	9)

1 - Vishay Semiconductors product

2 - Current code (50 = 50 A)

3 - Circuit configuration:

T = thyristor

4 - P = TO-247AD 3L package

5 - Type of silicon:

S = standard recovery rectifier

6 - Voltage code (12 = 1200 V)

7 - A = Low I_{GT} selection

8 - Package L = long lead

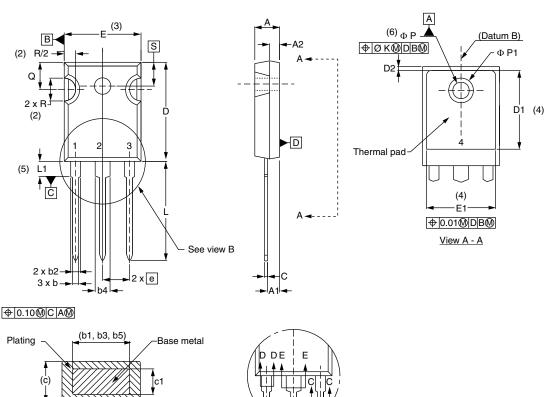
9 - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (example)								
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-50TPS12AL-M3	25	500	Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95626					
Part marking information	www.vishay.com/doc?95007					

TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIDGE	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
О	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

Section C - C, D - D, E - E

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46 BSC		0.215 BSC		
ØΚ	0.254		0.010		
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	ı	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217 BSC		
•	•			•	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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