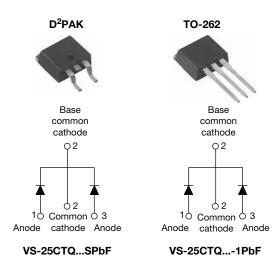


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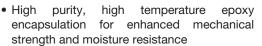
High Performance Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK), TO-262AA
I _{F(AV)}	2 x 15 A
V _R	35 V to 45 V
V _F at I _F	0.56 V
I _{RM}	70 mA at 125 °C
T _J max.	150 °C
Diode variation	Common cathode
E _{AS}	20 mJ

FEATURES

- 150 °C T_J operation
- Center tap TO-220 package
- · Very low forward voltage drop
- · High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-25CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	30	А				
V _{RRM}	Range	35 to 45	V				
I _{FSM}	$t_p = 5 \mu s sine$	990	Α				
V _F	15 A _{pk} , T _J = 125 °C (per leg)	0.50	V				
T _J	Range	-55 to +150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-25CTQ035SPbF VS-25CTQ035-1PbF	VS-25CTQ040SPbF VS-25CTQ040-1PbF	VS-25CTQ045SPbF VS-25CTQ045-1PbF	UNITS	
Maximum DC reverse voltage	V_R	35	40	45	V	
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	ITIONS	VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 102 °C	C, rectangular waveform	30		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	990	Α	
non-repetitive surge current per leg See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	250		
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.40 mH		20	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim		3	Α	

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VS-25CTQ...SPbF, VS-25CTQ...-1PbF Series

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1		15 A	T 05 %0	0.56	V	
	V (1)	30 A	T _J = 25 °C	0.71		
	V _{FM} ⁽¹⁾	15 A	T 105 00	0.50		
		30 A	T _J = 125 °C	0.64		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	1.75	mA	
See fig. 2		T _J = 125 °C	v _R = Hateu v _R	70	MA	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal rang	ge 100 kHz to 1 MHz), 25 °C	900	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	3.25		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Annyayinasta waight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
Marking device			Case style D ² PAK	25CT0	Q045S	
			Case style TO-262	25CTC	045-1	

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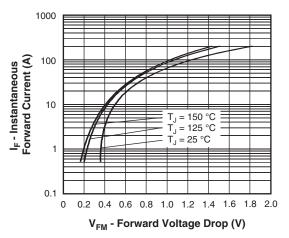


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

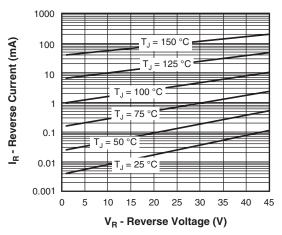


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

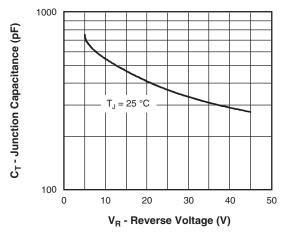


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

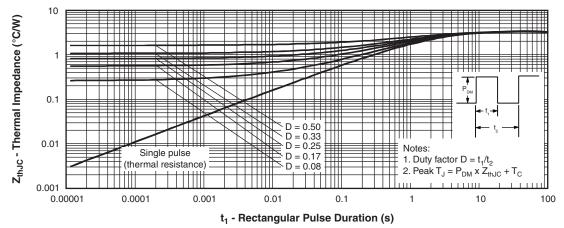


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)





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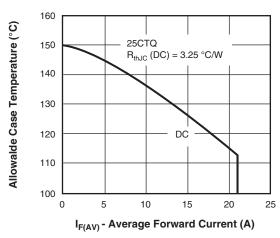


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

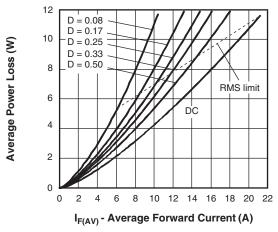


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

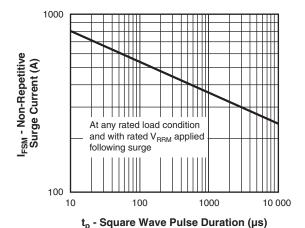


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

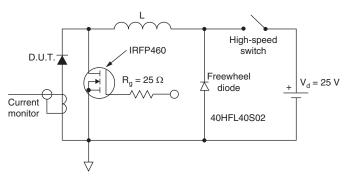


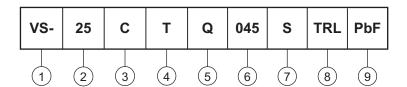
Fig. 8 - Unclamped Inductive Test Circuit

VS-25CTQ...SPbF, VS-25CTQ...-1PbF Series

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (25 A)

3 - Circuit configuration: C = common cathode

4 - T = TO-220

5 - Schottky "Q" series

035 = 35 V

Voltage ratings

040 = 40 V045 = 45 V

7 - • S = D²PAK

Ar

• -1 = TO-262

8 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented - for D²PAK only)

• TRR = tape and reel (right oriented - for D²PAK only)

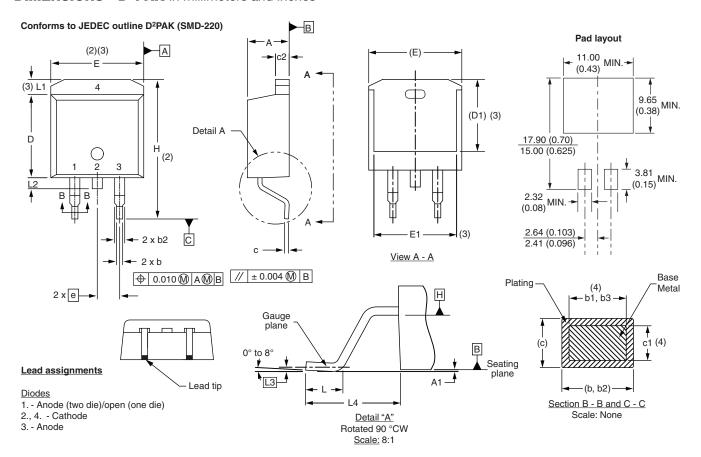
9 - PbF = lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95014				
Part marking information	www.vishay.com/doc?95008				
Packaging information	www.vishay.com/doc?95032				
SPICE model	www.vishay.com/doc?95285				

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D²**PAK**, **TO**-262

DIMENSIONS - D²PAK in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190		
A1	0.00	0.254	0.000	0.010		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	

SYMBOL	MILLIN	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	NOTES	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100 BSC			
Н	14.61	15.88	0.575	0.625		
L	1.78	2.79	0.070	0.110		
L1	-	1.65	-	0.066	3	
L2	1.27	1.78	0.050	0.070		
L3	0.25 BSC		0.010	BSC		
L4	4.78	5.28	0.188	0.208		

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

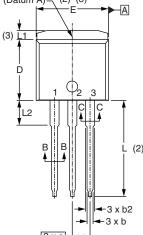
(7) Outline conforms to JEDEC outline TO-263AB

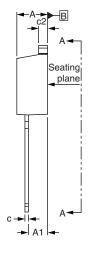
Document Number: 95014 Revision: 31-Mar-09

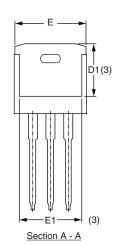


DIMENSIONS - TO-262 in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) - (2) (3)







⊕ 0.010 **M** A **M** B

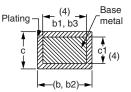
Lead assignments



Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIM	MILLIMETERS		INCHES		
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- $^{(1)}$ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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