

Dual P-Channel 12-V (D-S) MOSFET

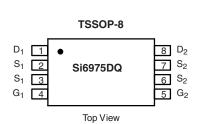
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
	0.027 at V _{GS} = - 4.5 V	- 5.1		
- 12	0.036 at V _{GS} = - 2.5 V	- 4.5		
	0.046 at V _{GS} = - 1.8 V	- 3.9		

FEATURES

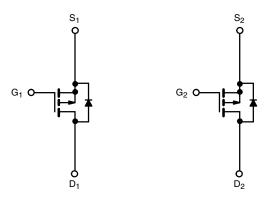
- · Halogen-free
- TrenchFET® Power MOSFETs: 1.8 V Rated



ROHS COMPLIANT



Ordering Information: Si6975DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 12		V
Gate-Source Voltage		V_{GS}	± 8		
Continuous Dunin Courset /T 150 90\8	T _A = 25 °C	- I _D	- 5.1	- 4.3	٨
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 4.1	- 3.5	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	- 30		Α
Continuous Source Current (Diode Conduction)a		I _S	- 1.0	- 0.7	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.14	0.83	W
	T _A = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marian na lunation to Ambianta	t ≤ 10 s	- R _{thJA}	86	110	°C/W
Maximum Junction-to-Ambient ^a	Steady State		124	150	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	52	65	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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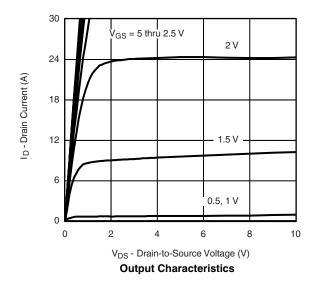
SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -5$ mA	- 0.45			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 9.6 V, V _{GS} = 0 V			- 1			
		$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 25	μΑ		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			Α		
		$V_{GS} = -4.5 \text{ V}, I_D = -5.1 \text{ A}$		0.022	0.027	Ω		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -4.5 \text{ A}$		0.028	0.035			
		$V_{GS} = -1.8 \text{ V}, I_D = -3.9 \text{ A}$		0.037	0.046			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 5.1 A		20		S		
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.0 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.65	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Q_g			23	30			
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -5.1 \text{ A}$		3.0		nC		
Gate-Drain Charge	Q_{gd}			4.3				
Turn-On Delay Time	t _{d(on)}			25	40			
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		32	50			
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_G = 6 Ω		96	140	ns		
Fall Time	t _f			62	95			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.0 A, dl/dt = 100 A/μs		60	100			

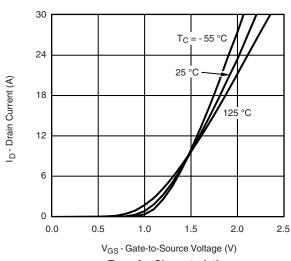
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

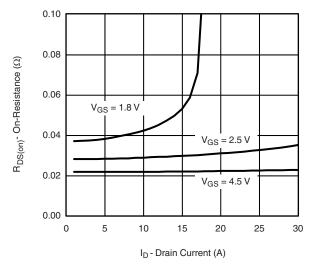




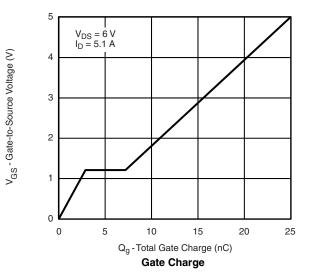
Transfer Characteristics

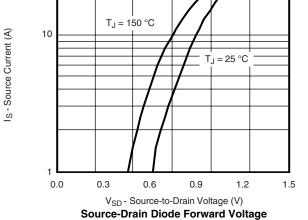


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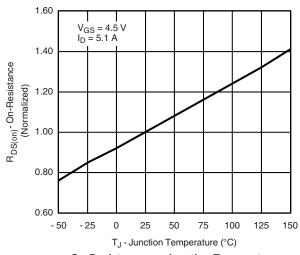
On-Resistance vs. Drain Current



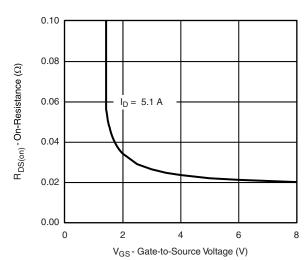


4000 3200 C_{iss} 1600 0 2400 C_{rss} 0 0 2 4 6 8 10 12

 V_{DS} - Drain-to-Source Voltage (V) $\label{eq:capacitance}$



On-Resistance vs. Junction Temperature

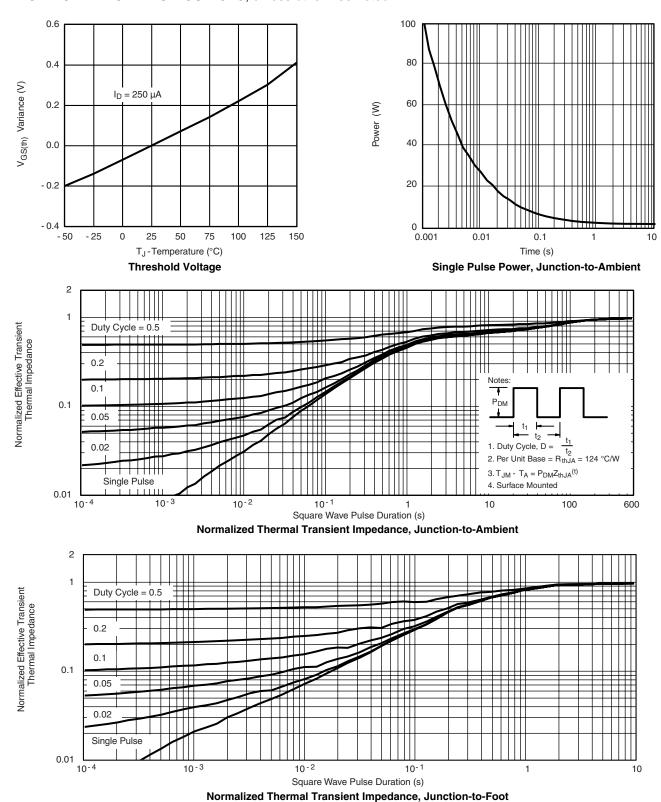


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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