

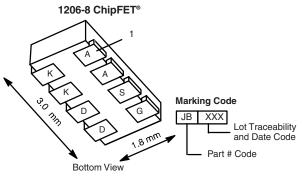
Vishay Siliconix

P-Channel 1.8 V (G-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)				
	0.110 at V _{GS} = - 4.5 V	- 3.6				
- 20	0.160 at V _{GS} = - 2.5 V	- 3.0				
	0.240 at V _{GS} = - 1.8 V	- 2.4				

SCHOTTKY PRODUCT SUMMARY

V _{KA} (V)	V _f (V) Diode Forward Voltage	I _F (A)
20	0.375 V at 1 A	1.0

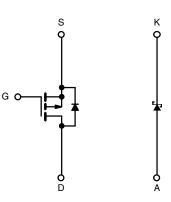


FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFETs
- Ultra Low V_f Schottky
- Si5853DC Pin Compatible
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

• Charging Circuit in Portable Devices



Ordering Information: Si5855DC-T1-E3 (Lead (Pb)-free) Si5855DC-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A :	= 25 °C, unle	ess otherwise	noted		
Parameter	Symbol	5 s	Steady State	Unit	
Drain-Source Voltage (MOSFET)	V _{DS}	- 20			
Reverse Voltage (Schottky)	V _{KA}		V		
Gate-Source Voltage (MOSFET)	V _{GS}	:			
	T _A = 25 °C	1	- 3.6	- 2.7	
Continuous Drain Current (T _J = 150 °C) (MOSFET) ^a	T _A = 85 °C	I _D	- 2.6	- 1.9	
Pulsed Drain Current (MOSFET)	I _{DM}	- 10		•	
Continuous Source Current (MOSFET Diode Conducti	۱ _S	- 1.8	- 0.9	A	
Average Forward Current (Schottky)	١ _F	1.0			
Pulsed Forward Current (Schottky)	I _{FM}	7			
	T _A = 25 °C		2.1	1.1	
Maximum Power Dissipation (MOSFET) ^a	T _A = 85 °C	Б	1.1	0.6	w
	T _A = 25 °C	P _D	1.9	1.1	vv
Maximum Power Dissipation (Schottky) ^a	T _A = 85 °C		1.0	0.56	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		00
Soldering Recommendations (Peak Temperature) ^{b, c}		2	260	°C	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See reliability manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.



HALOGEN

FREE

Available

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THERMAL RESISTANCE RATINGS									
Parameter		Device	Symbol	Typical	Maximum	Unit			
	t≤5s	MOSFET	R _{thJA}	50	60	°C/W			
hundrige to Ambiguit	1 2 5 5	Schottky		54	65				
Junction-to-Ambient ^a	Steady State	MOSFET		90	110				
		Schottky		95	115				
Junction-to-Foot	Steady State	MOSFET	R _{thJF}	30	40				
Junction-to-Foot		Schottky		30	40				

Notes:

a. Surface mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static				•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.45		- 1.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA		
Zaus Cata Malta na Duain Coursent	1	$V_{DS} = -20 V, V_{GS} = 0 V$			- 1			
Zero Gate Voltage Drain Current	DSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 5	μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS}{\leq}{-}5$ V, $V_{GS}{=}{-}4.5$ V	- 10			Α		
Drain-Source On-State Resistance ^a		V_{GS} = - 4.5 V, I _D = - 2.7 A		0.095	0.110	Ω		
	R _{DS(on)}	V_{GS} = - 2.5 V, I _D = - 2.2 A		0.137	0.160			
		V _{GS} = - 1.8 V, I _D = - 1 A		0.205	0.240			
Forward Transconductance ^a	9 _{fs}	V_{DS} = - 10 V, I_{D} = - 2.7 A		7		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 0.9 A, V _{GS} = 0 V		- 0.8	- 1.2	V		
Dynamic ^b								
Total Gate Charge	Qg			5.1	7.7			
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2.7 \text{ A}$		1.2		nC		
Gate-Drain Charge	Q _{gd}			1.0				
Turn-On Delay Time	t _{d(on)}			16	25			
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		30	45			
Turn-Off Delay Time	t _{d(off)}	$t_{d(off)}$ I _D \cong - 1 A, V _{GEN} = - 4.5 V, R _g = 6 Ω		30	45	ns		
Fall Time	t _f			27	40			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 0.9 A, dl/dt = 100 A/μs		20	40			

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

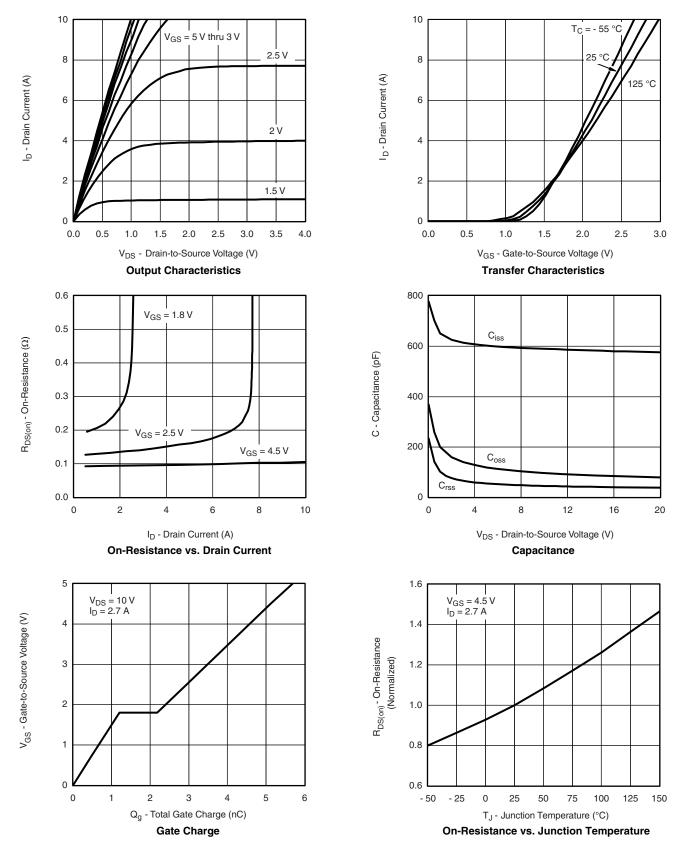
SCHOTTKY SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V _F	I _F = 1 A		0.34	0.375	V		
		I _F = 1 A, T _J = 125 °C		0.255	0.290	v		
Maximum Reverse Leakage Current	I _{rm}	V _r = 20 V		0.05	0.500			
		V _r = 20 V, T _J = 85 °C		2	20	mA		
		V _r = 20 V, T _J = 125 °C		10	100			
Junction Capacitance	CT	V _r = 10 V		90		pF		



Si5855DC

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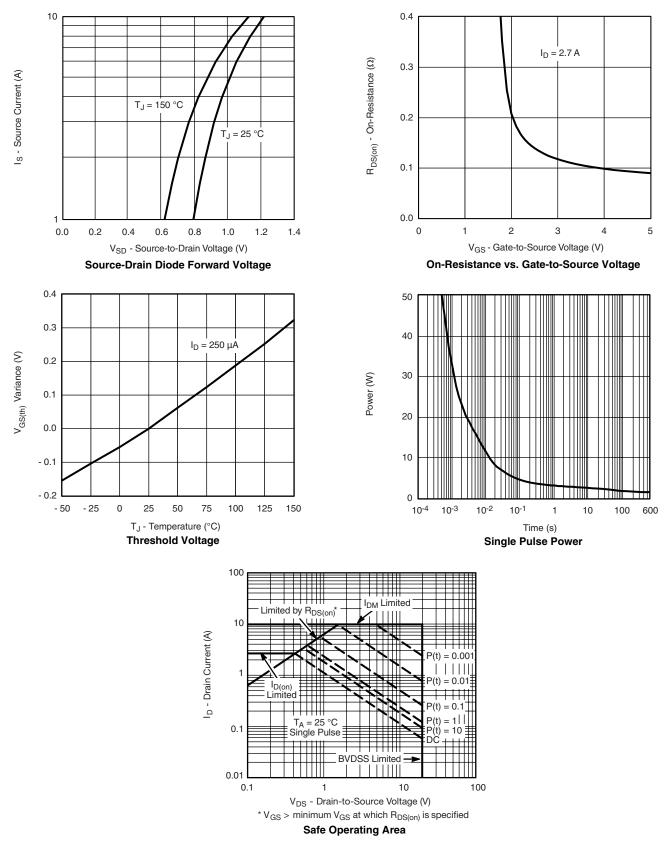
MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



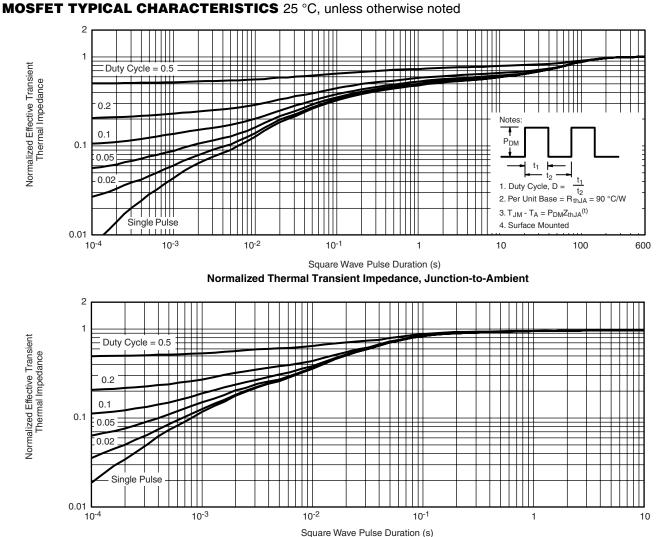
Si5855DC

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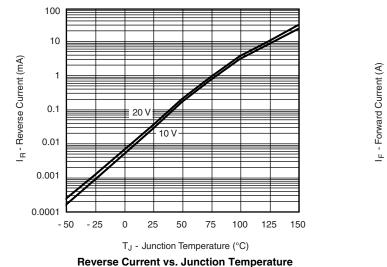


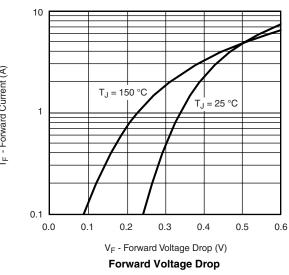
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Normalized Thermal Transient Impedance, Junction-to-Foot







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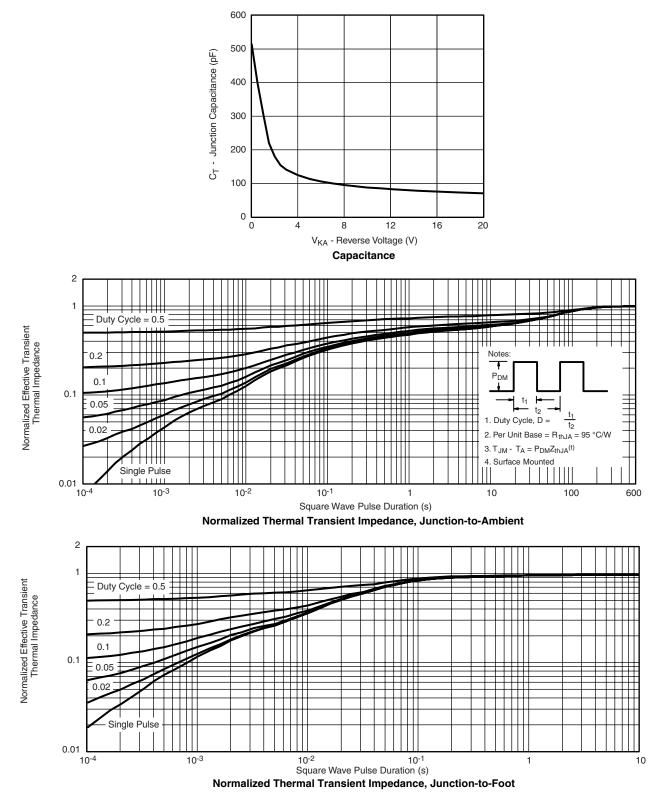
Si5855DC

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