

Vishay Siliconix

# P-Channel 1.25-W, 1.8-V (G-S) MOSFET

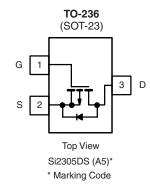
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (Ω)</b>	I <sub>D</sub> (A)		
	0.052 at V <sub>GS</sub> = - 4.5 V	± 3.5		
- 8	0.071 at V <sub>GS</sub> = - 2.5 V	± 3		
	0.108 at V <sub>GS</sub> = - 1.8 V	± 2		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFETs: 1.8 V Rated



HALOGEN FREE Available



Ordering Information: Si2305DS-T1 Si2305DS-T1-E3 (Lead (Pb)-free) Si2305DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 8	- v	
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Continuous Drain Current ( $T_{,I} = 150 \ ^{\circ}C$ )	T <sub>A</sub> = 25 °C		± 3.5		
Continuous Drain Current $(T_J = 150^{\circ} C)$	T <sub>A</sub> = 70 °C	<sup>I</sup> D	± 2.8		
Pulsed Drain Current		I <sub>DM</sub> ± 12		A	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		۱ <sub>S</sub>	- 1.6	1	
	T <sub>A</sub> = 25 °C	P	1.25	w	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C	PD	0.8		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	R <sub>thJA</sub>		100	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		130		0/10

Notes:

a. Surface Mounted on FR4 board.

b. t  $\leq$  5 s.

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

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<b>SPECIFICATIONS</b> $T_J = 25 \text{ °C}$ , unless otherwise noted							
Parameter			Limits				
	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				_			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ = 0 V, I <sub>D</sub> = - 10 µA	- 8			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 0.45		- 0.8		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 8 V			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -8 V, V_{GS} = 0 V$			- 1		
	IDSS	$V_{DS}$ = - 8 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10	- μΑ	
On-State Drain Current <sup>a</sup>		$V_{DS} \leq$ - 5 V, $V_{GS}$ = - 4.5 V	- 6			•	
	I <sub>D(on)</sub>	$V_{DS} \leq$ - 5 V, $V_{GS}$ = - 2.5 V	- 3			A	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.5 A		0.044	0.052	Ω	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -3 \text{ A}$		0.060	0.071		
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 2 A		0.087	0.108		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -5 V, I_{D} = -3.5 A$		8.5		S	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1.6 A, V <sub>GS</sub> = 0 V			- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			10	15	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = - 4.5 V, $I_D$ $\cong$ - 3.5 A		2			
Gate-Drain Charge	Q <sub>gd</sub>			2			
Input Capacitance	C <sub>iss</sub>			1245			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = 0 V, f = 1 MHz		375		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			210			
Switching <sup>b</sup>							
Turn-On Time	t <sub>d(on)</sub>			13	20		
	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 4 $\Omega$		25	40		
Turn-Off Time	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ - 1.0 A, $\text{V}_{\text{GEN}}$ = - 4.5 V, $\text{R}_{\text{G}}$ = 6 $\Omega$		55	80	ns	
	t <sub>f</sub>			19	35		

Notes:

a. For DESIGN AID ONLY, not subject to production testing.

b. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

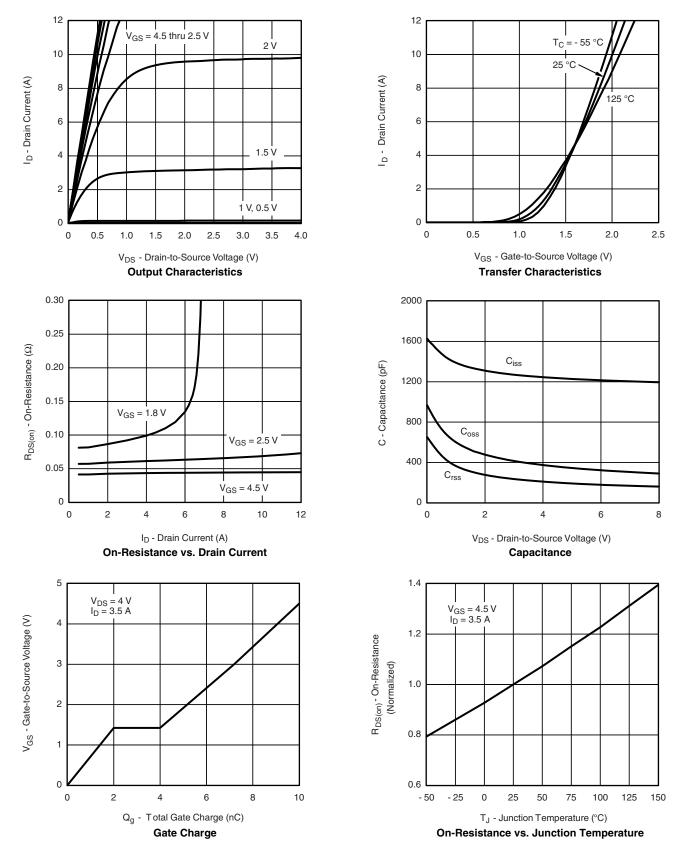
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



## Si2305DS Vishay Siliconix

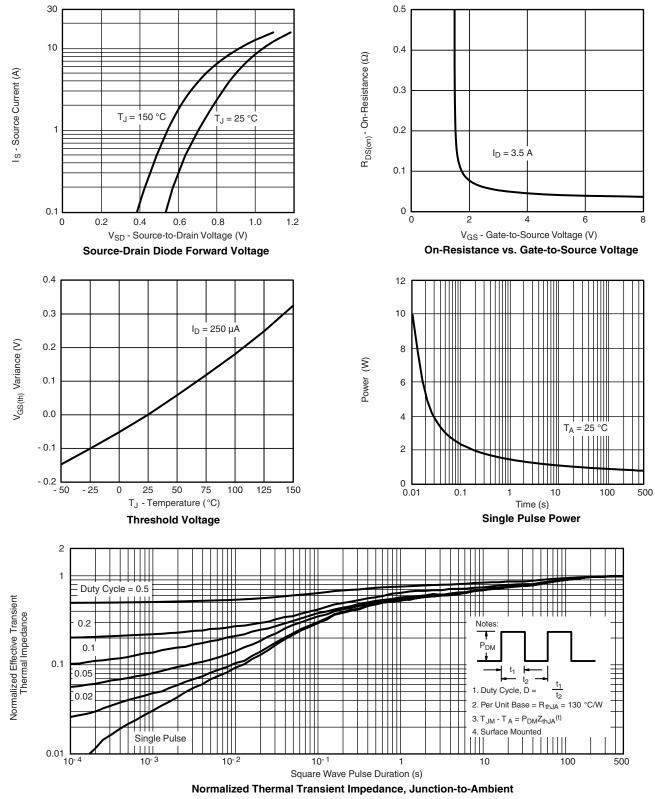
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwse noted



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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?70833">www.vishay.com/ppg?70833</a>.



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