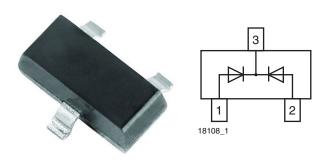


## Vishay Semiconductors

# **Dual Common Cathode Small Signal High Voltage Switching Diode**



#### **DESIGN SUPPORT TOOLS** click logo to get started



#### **MECHANICAL DATA**

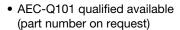
Case: SOT-23

Weight: approx. 8.1 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

#### **FEATURES**

- Silicon epitaxial planar diode
- Fast switching dual common cathode diode, especially suited for applications requiring high voltage capability



- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE

**GREEN** (5-2008)

PARTS TABLE					
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
GSD2004C-G	GSD2004C-G3-08 or GSD2004C-G3-18	Common cathode	DBK	Tape and reel	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		V <sub>R</sub>	240	V	
Peak repetitive reverse voltage		V <sub>RRM</sub>	300	V	
Forward current (continuous)		I <sub>F</sub>	225	mA	
Peak repetitive forward current		I <sub>FRM</sub>	625	mA	
Non-repetitive peak forward current	t <sub>p</sub> = 1 μs		4	Α	
Non-repetitive peak forward current	t <sub>p</sub> = 1 s	- I <sub>FSM</sub>	1	Α	
Power dissipation (1)		P <sub>tot</sub>	350	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT	
Typical thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	357	°C/W	
Junction temperature		T <sub>j</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

#### Note

<sup>(1)</sup> Device on fiberglass substrate



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I <sub>R</sub> = 100 μA	$V_{BR}$	300			V
Looke an assument	V <sub>R</sub> = 240 V	I <sub>R</sub>			100	nA
Leakage current	V <sub>R</sub> = 240 V, T <sub>j</sub> = 150 °C	I <sub>R</sub>			100	μΑ
Converd veltage	I <sub>F</sub> = 20 mA	V <sub>F</sub>		0.83	0.87	V
Forward voltage	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1	V
Diode capacitance	$V_F = V_R = 0$ , $f = 1$ MHz	C <sub>D</sub>			5	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $i_R = 3$ mA, $R_L = 100~\Omega$	t <sub>rr</sub>			50	ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

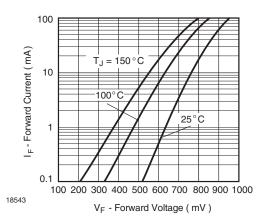


Fig. 1 - Typical Instantaneous Forward Characteristics

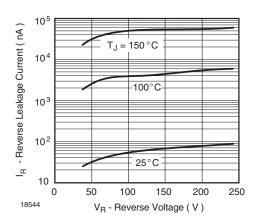
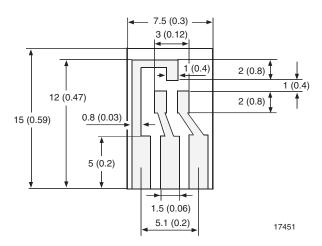


Fig. 2 - Typical Reverse Characteristics

### LAYOUT FOR $R_{thJA}$ TEST

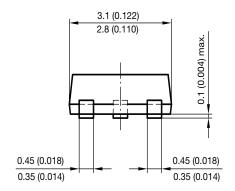
Thickness: Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)

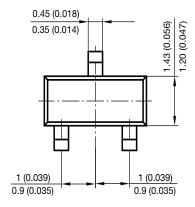




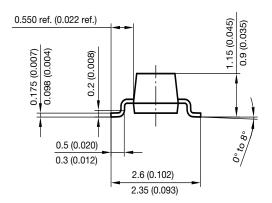
# Vishay Semiconductors

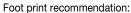
### PACKAGE DIMENSIONS in millimeters (inches): SOT-23

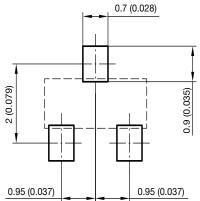




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