

Vishay Siliconix

P-Channel 1.8 V (G-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)		
- 20	0.015 at V _{GS} = - 4.5 V	- 7.4		
	0.020 at V _{GS} = - 2.5 V	- 6.3		
	0.027 at V _{GS} = - 1.8 V	- 5.5		

FEATURES

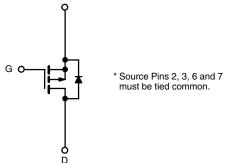
- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET

S*

Compliant to RoHS Directive 2002/95/EC



TSSOP-8
D 1 • 8 D
S 2 Si6463BDQ 77 S
G 4 5 D
Top View



Top view

Ordering Information: Si6463BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$(T_A = 25 \ ^{\circ}C, unle$	ess otherwise	noted)		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 25 °C	– I _D	- 7.4	- 6.2	٨
	T _A = 70 °C		- 5.9	- 4.9	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 30		A
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.35	- 0.95	
Maximum Power Dissipation ^a	T _A = 25 °C	– P _D	1.5	1.05	W
	T _A = 70 °C		1.0	0.67	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	R _{thJA}	65	83	
Maximum Junction-to-Ambient ^a	Steady State		100	120	°C/W
Maximum Junction-to-Foot	Steady State		46	56	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.45		- 0.8	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 70 $^{\circ}C$	= - 20 V, V _{GS} = 0 V, T _J = 70 °C			μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -7.4 \text{ A}$		0.011	0.015	Ω	
		V_{GS} = - 2.5 V, I_D = - 6.3 A		0.015	0.020		
		$V_{GS} = -1.8 \text{ V}, \text{ I}_{D} = -5.5 \text{ A}$		0.020	0.027		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 7.4 A		34		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.3 A, V _{GS} = 0 V		- 0.64	- 1.1	V	
Dynamic ^b				•			
Total Gate Charge	Qg			40	60	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 5 V, I_D = - 7.4 A		5.2			
Gate-Drain Charge	Q _{gd}			8		1	
Turn-On Delay Time	t _{d(on)}			35	55		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 15 Ω		40	60	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 4.5 V, R_{g} = 6 Ω		190	300		
Fall Time	t _f			90	150		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.3 A, dl/dt = 100 A/μs		75	120		

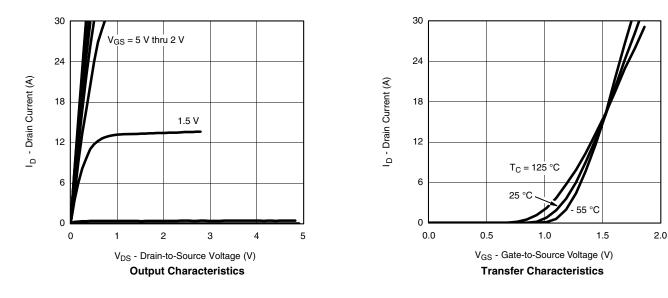
Notes:

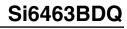
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

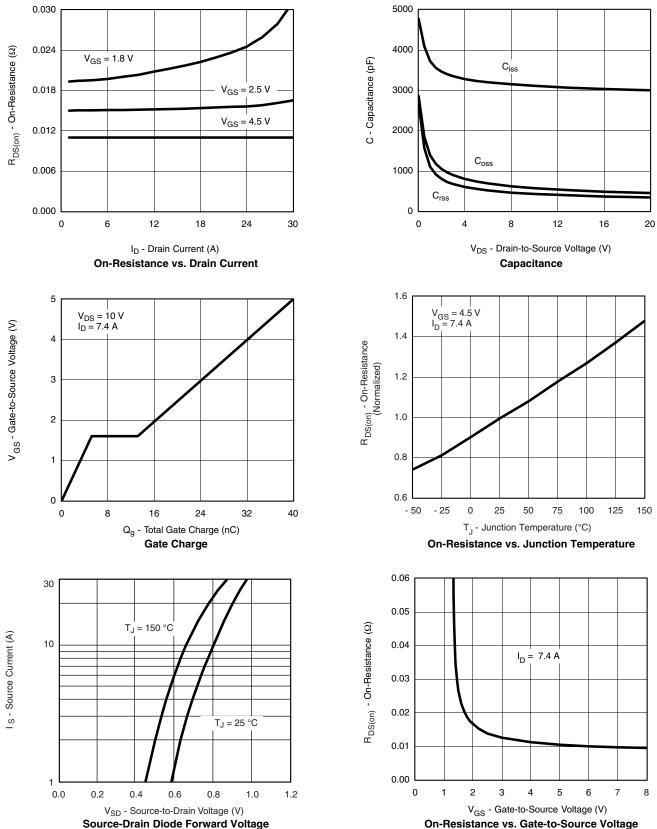




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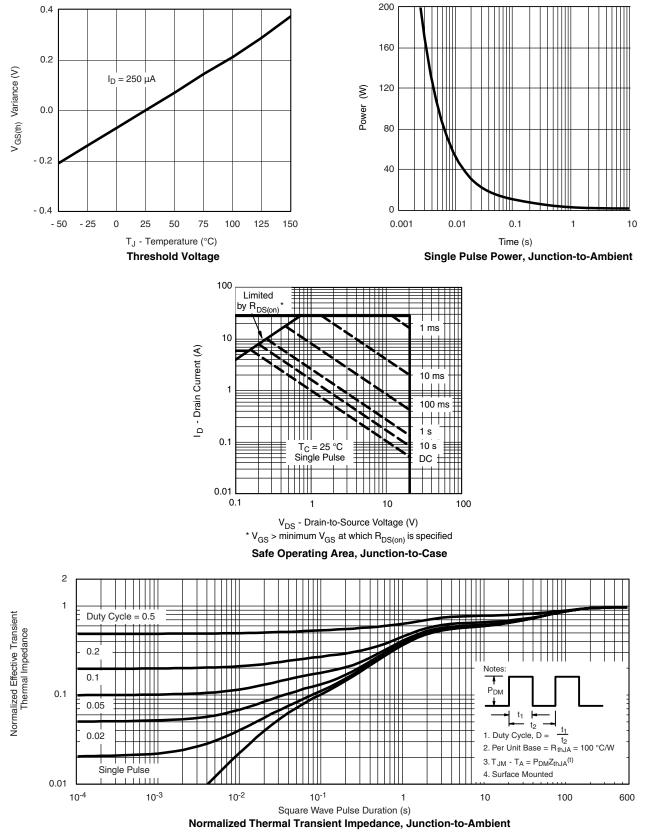
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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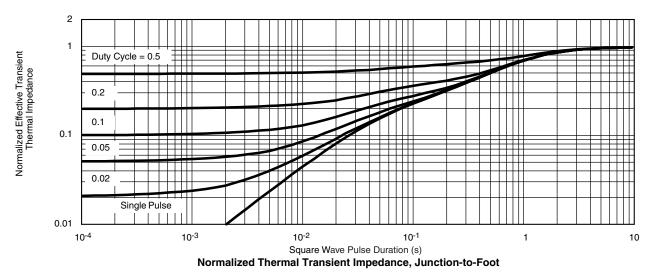




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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72018.



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