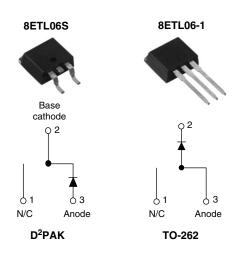


Vishay High Power Products

Ultralow V_F Hyperfast Rectifier for Discontinuous Mode PFC, 8 A FRED PtTM



PRODUCT SUMMARY				
V _F (typical)	0.96 V			
I _{F(AV)}	8 A			
V_{R}	600 V			

FEATURES

- · Benchmark ultralow forward voltage drop
- · Hyperfast recovery time
- · Low leakage current
- 175 °C operating junction temperature
- · Designed and qualified for industrial level

DESCRIPTION

State of the art, ultralow V_F , soft-switching hyperfast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC-DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC-DC power supplies.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 160 °C	8		
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	175	Α	
Peak repetitive forward current	I _{FM}		16		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	
Forward voltage V _F	I _F = 8 A	-	0.96	1.05	V	
r orward voltage		I _F = 8 A, T _J = 150 °C	-	0.81	0.86	
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.05	5	
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	=	20	100	μΑ
Junction capacitance	C _T	V _R = 600 V	=	17	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nH		

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8ETL06S/8ETL06-1

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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t _{rr}		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	60	100	
		$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	150	250	
	L _{rr}	T _J = 25 °C	$I_F = 8 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 390 \text{ V}$	-	170	-	ns
		T _J = 125 °C		-	250	-	
Peak recovery current		T _J = 25 °C		-	15	-	- А
	I _{RRM}	T _J = 125 °C		-	20	-	
Reverse recovery charge	0	T _J = 25 °C		-	1.3	-	μC
	Q _{rr}	T _J = 125 °C		-	2.6	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	BOL TEST CONDITIONS		TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	1.4	2		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Maight			-	2.0	-	g	
Weight			-	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Maddin a darin		Case style D ² PAK	8ETL06S				
Marking device	Marking device			8ETL06-1			



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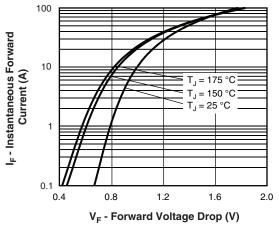


Fig. 1 - Typical Forward Voltage Drop Characteristics

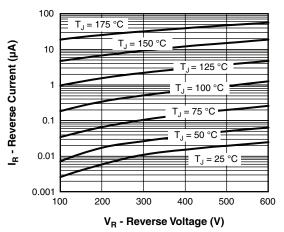


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

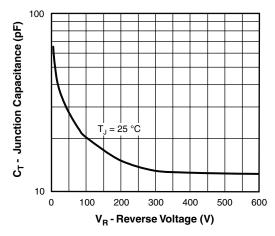


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

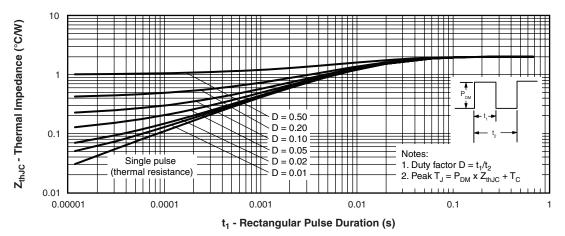


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

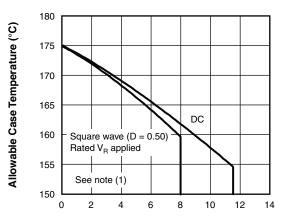
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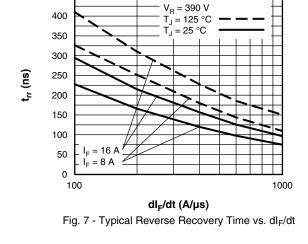
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I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current



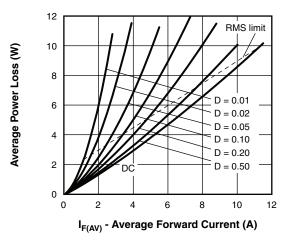


Fig. 6 - Forward Power Loss Characteristics

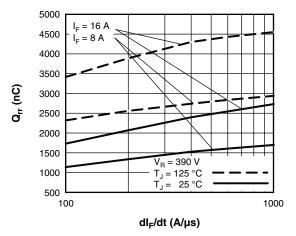


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

 $\begin{array}{l} \mbox{(1)} \;\; \mbox{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \mbox{at} \; (I_{F(AV)}/D) \; (\mbox{see fig. 6}); \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \mbox{at} \; V_{R1} = \mbox{Rated} \; V_R \\ \end{array}$



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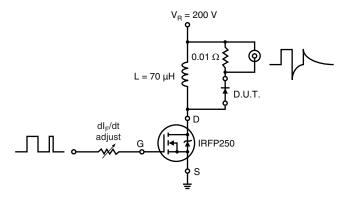
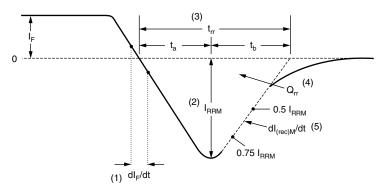


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{BBM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

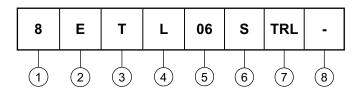
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (8 A)

2 - E = Single diode

3 - T = TO-220, D²PAK

4 - L = Ultralow V_F hyperfast recovery

5 - Voltage rating (06 = 600 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			



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