International Rectifier

12CWQ04FN

SCHOTTKY RECTIFIER

12 Amp

$$I_{F(AV)} = 12Amp$$
 $V_R = 40V$

Major Ratings and Characteristics

Cha	racteristics	Values	Units
I _{F(AV)}	Rectangular waveform	12	А
V _{RRM}	1	40	V
I _{FSM}	@ tp=5 µs sine	550	А
V _F	@6 Apk, T _J = 125°C (per leg)	0.48	V
T _J	range	-55 to 150	°C

Description/ Features

The 12CWQ04FN surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

- Popular D-PAK outline
- Center tap configuration
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability



Voltage Ratings

Part number	12CWQ04FN		
V _R Max. DC Reverse Voltage (V)	40		
V _{RWM} Max. Working Peak Reverse Voltage (V)	40		

Absolute Maximum Ratings

Parameters		12CWQ	Units	Conditions		
I _{E(AV)}	I _{E(AV)} Max. Average Forward(Per Leg)		Α	50% duty cycle @ T _C = 134°C, rectangular wave form		
'(,	Current* See Fig. 5 (Per Device)	12				
I _{FSM}	Max. Peak One Cycle Non-Repetitive	550	Α	5μs Sine or 3μs Rect. pulse	Following any rated load condition and with rated V _{RRM} applied	
	Surge Current *See Fig. 7	90	A	10ms Sine or 6ms Rect. pulse		
E _{AS}	Non-Repet. Avalan. Energy (Per Leg)	9	mJ	T _J = 25 °C, I _{AS} = 1.5 Amps, L = 8 mH		
I _{AR}	Repetitive Avalanche Current	1.2	Α	Current decaying linearly to zero in 1 µsec		
	(Per Leg)			Frequency limited by T _J max. V _A	_λ = 1.5 x V _R typical	

Electrical Specifications

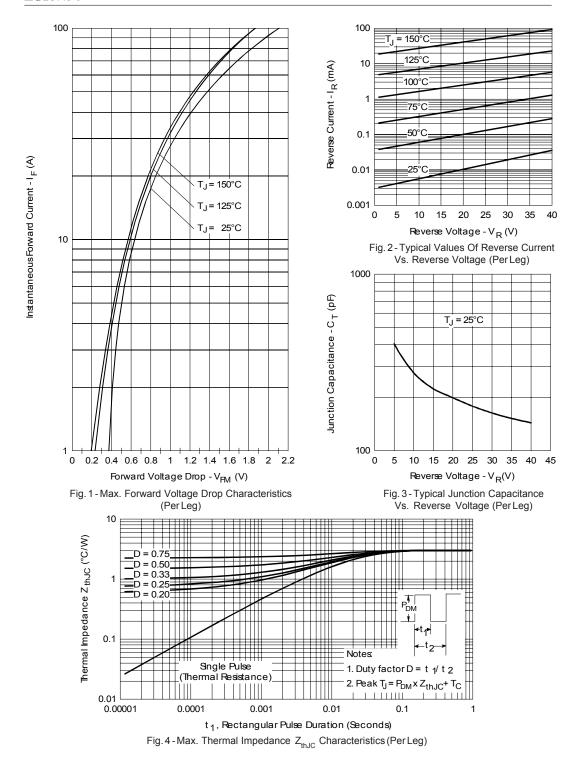
Parameters		12CWQ	Units	Conditions	
V _{FM}	Max. Forward Voltage Drop	0.53	V	@ 6A	T - 25 °C
'	(Per Leg) * See Fig. 1 (1)	0.68	V	@ 12A	$T_J = 25 ^{\circ}\text{C}$
		0.48	V	@ 6A	T 405 °C
		0.64	V	@ 12A	T _J = 125 °C
I _{RM}	Max. Reverse Leakage Current	3	mA	T _J = 25 °C	// = rated //
	(Per Leg) * See Fig. 2 (1)	40	mA	T _J = 125 °C	V _R = rated V _R
V _{F(TO}	Threshold Voltage	0.28	V	$T_J = T_J \text{ max.}$	
r _t	Forward Slope Resistance	25.58	mΩ		
C _T	C _T Typ. Junction Capacitance (Per Leg)		pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C	
L _S	L _S Typical Series Inductance (Per Leg)		nΗ	Measured lead to lead 5mm from package body	

(1) Pulse Width < 300 μ s, Duty Cycle <2%

Thermal-Mechanical Specifications

	Parameters	12CWQ	Units	Conditions	
T _J	Max. Junction Temperature Range (*)	-55 to 150	°C		
T _{stg}	Max. Storage Temperature Range	-55 to 150	°C		
R _{thJC}	Max. Thermal Resistance (Per Leg)	3.0	°C/W	DC operation *See Fig. 4	
	Junction to Case (Per Device)	1.5			
wt	Approximate Weight	0.3 (0.01)	g (oz.)		
Case Style		D-Pak		Similar to TO-252AA	
	Device Marking		4FN		

 $\frac{\text{(*) } \frac{dPtot}{dTj}}{dTj} < \frac{1}{Rth(j-a)}$ thermal runaway condition for a diode on its own heatsink



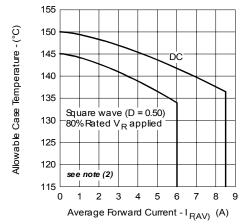


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

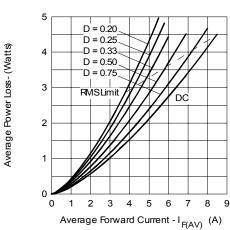


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

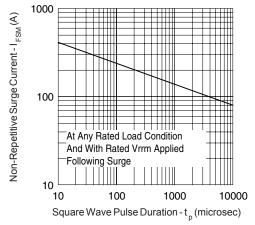
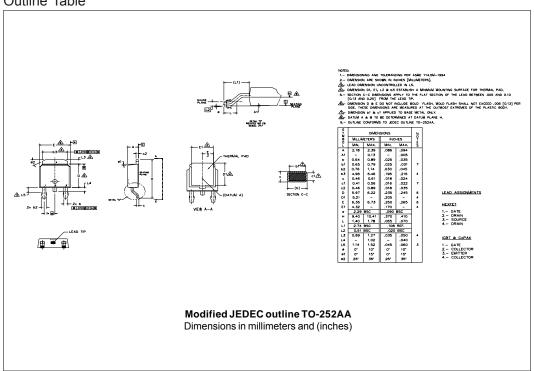


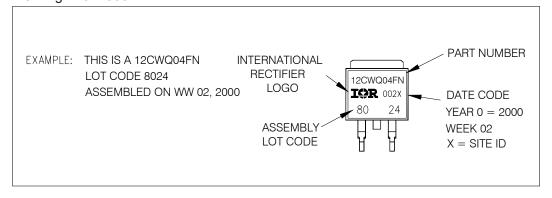
Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

 $\begin{tabular}{ll} \textbf{(2)} & Formula used: $T_C = T_J - (Pd + Pd_{REV})$ x R_{thJC}; \\ & Pd = Forward Power Loss = $I_{F(AV)}$ x $V_{FM}@(I_{F(AV)}/D)$ (see Fig. 6); \\ & Pd_{REV} = Inverse Power Loss = V_{R1} x $I_R(1-D)$; $I_R@V_{R1} = 80\%$ rated V_R \\ \end{tabular}$

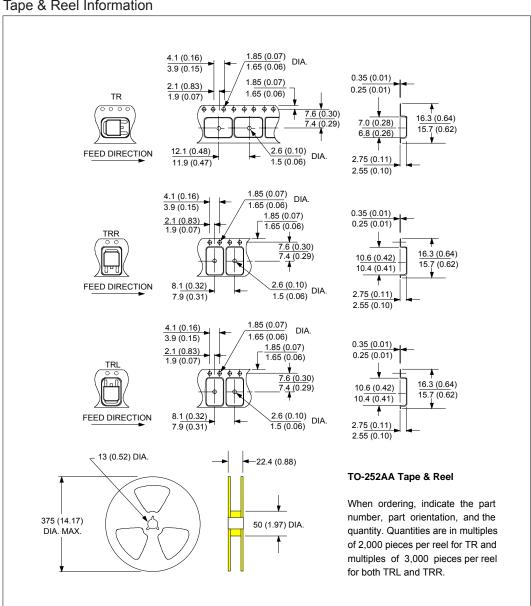
Outline Table



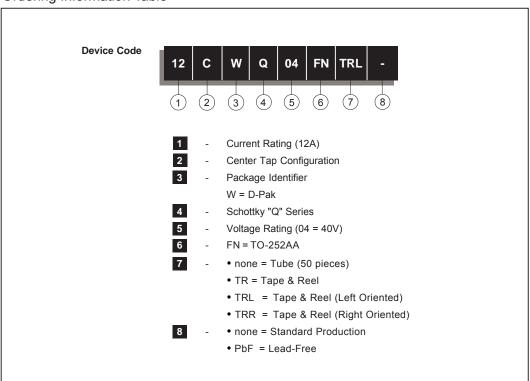
Marking Information



Tape & Reel Information



Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for AEC Q101 Level. Qualification Standards can be found on IR's Web site.



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Vishay

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