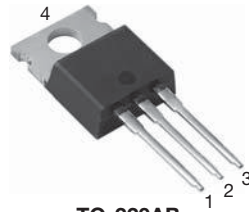
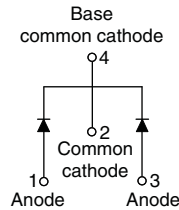


## Hyperfast Rectifier, 2 x 15 FRED Pt®


**TO-220AB**

**VS-30CTH02HN3**

### FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ( $V_{INS} = 2500 V_{RMS}$ )
- Designed and qualified according to JEDEC®-JESD 47
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### DESCRIPTION / APPLICATIONS

200 V series are the state of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY	
Package	TO-220AB
$I_{F(AV)}$	2 x 15 A
$V_R$	200 V
$V_F$ at $I_F$	0.78 V
$t_{rr}$ typ.	See Recovery table
$T_J$ max.	175 °C
Diode variation	Common cathode

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 159\text{ °C}$	15	A
			per diode	
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	200	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-55 to +175	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	200	-	-	V
Forward voltage	$V_F$	$I_F = 15\text{ A}$	-	0.92	1.05	
		$I_F = 15\text{ A}, T_J = 125\text{ °C}$	-	0.78	0.85	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	10	$\mu\text{A}$
		$T_J = 125\text{ °C}, V_R = V_R$ rated	-	5	300	
Junction capacitance	$C_T$	$V_R = 200\text{ V}$	-	57	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	8	-	nH

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_C = 25\text{ }^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1\text{ A}$ , $di_F/dt = 50\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	-	35	ns
		$I_F = 1\text{ A}$ , $di_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	-	30	
		$T_J = 25\text{ }^\circ\text{C}$	-	26	-	
		$T_J = 125\text{ }^\circ\text{C}$		40	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^\circ\text{C}$	-	2.8	-	A
		$T_J = 125\text{ }^\circ\text{C}$	-	6.0	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^\circ\text{C}$	-	37	-	nC
		$T_J = 125\text{ }^\circ\text{C}$	-	120	-	

<b>THERMAL - MECHANICAL SPECIFICATIONS</b>						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J$ , $T_{Stg}$		-55	-	175	$^\circ\text{C}$
Thermal resistance, junction to case	per diode $R_{thJC}$	Mounting surface, flat, smooth, and greased	-	-	1.1	$^\circ\text{C}/\text{W}$
Approximate weight			-	2	-	g
			-	0.07	-	oz.
Mounting torque			6	-	12	kgf · cm
			5	-	10	(lbf · cm)
Marking device		Case style TO-220AB	30CTH02H			

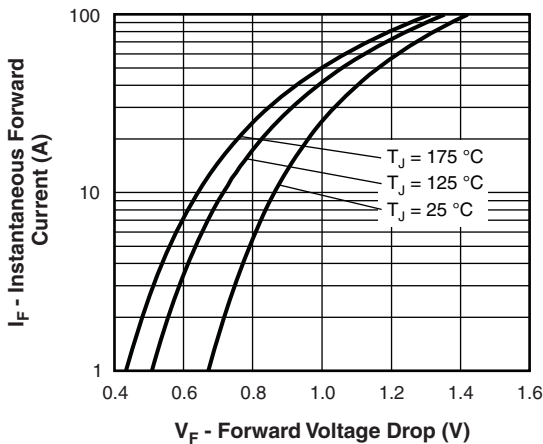


Fig. 1 - Typical Forward Voltage Drop Characteristics

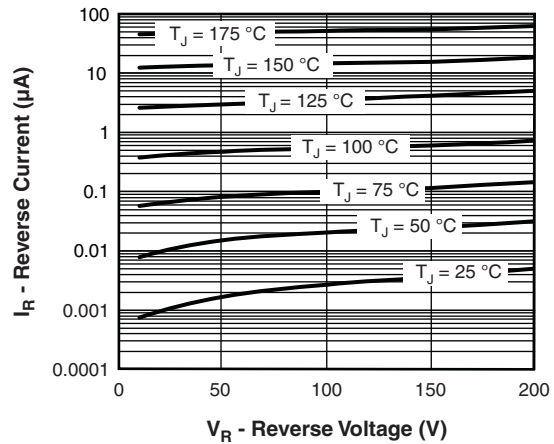


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

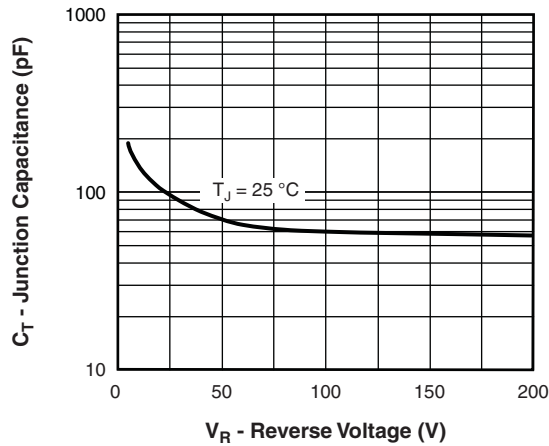


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

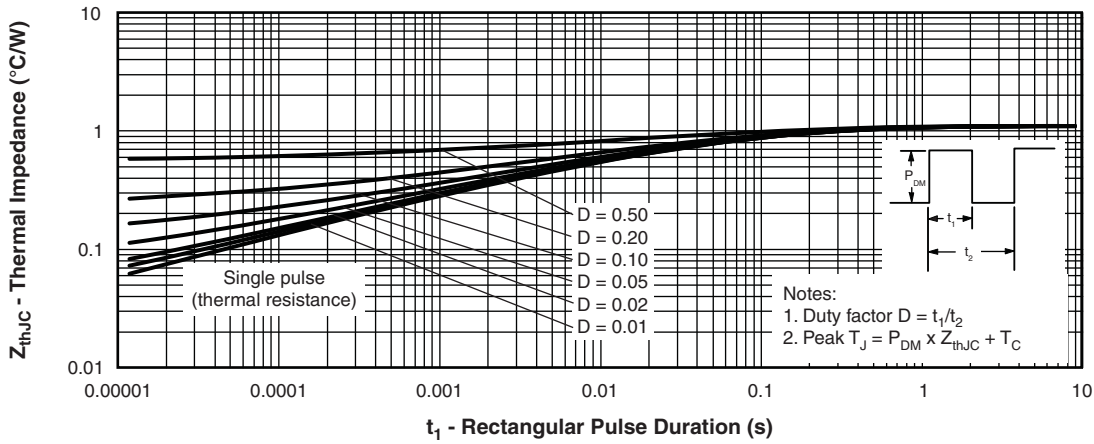


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

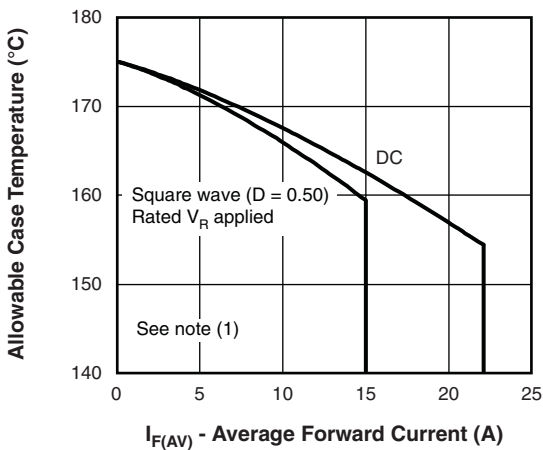


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

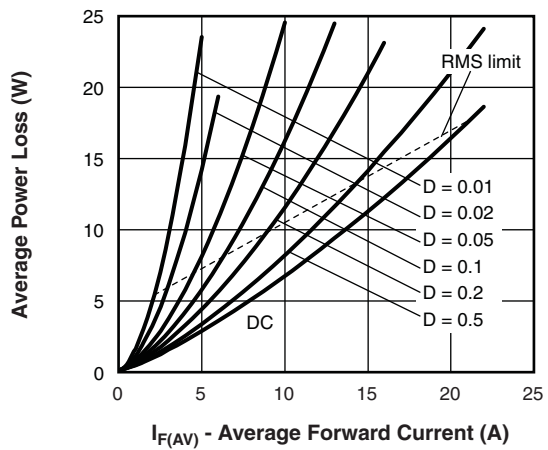


Fig. 6 - Forward Power Loss Characteristics

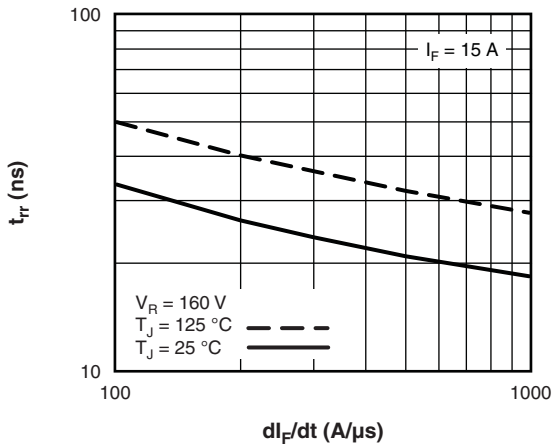


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

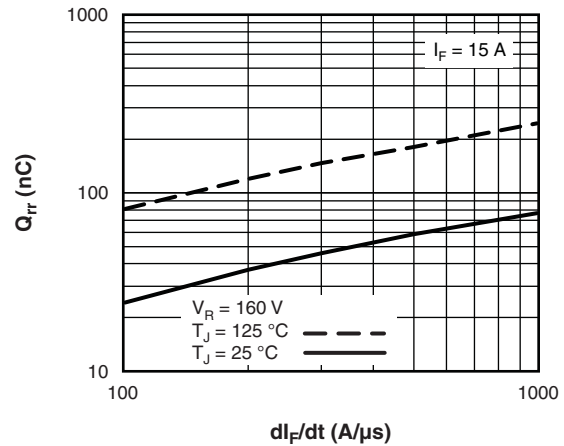
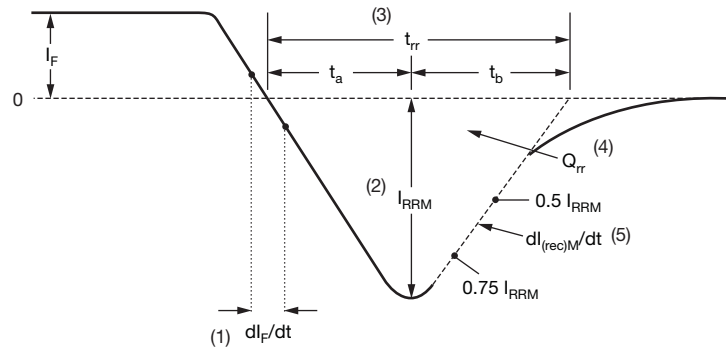


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

**Note**

- (1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  
 $Pd$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 8);  
 $Pd_{REV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

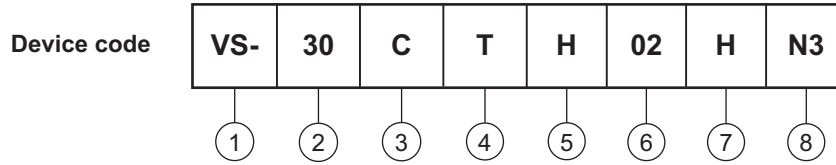


- (1)  $di_F/dt$  - rate of change of current through zero crossing
- (2)  $I_{RRM}$  - peak reverse recovery current
- (3)  $t_{rr}$  - reverse recovery time measured from zero crossing point of negative going  $I_F$  to point where a line passing through  $0.75 I_{RRM}$  and  $0.50 I_{RRM}$  extrapolated to zero current.
- (4)  $Q_{rr}$  - area under curve defined by  $t_{rr}$  and  $I_{RRM}$
- $$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
- (5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 9 - Reverse Recovery Waveform and Definitions



**ORDERING INFORMATION TABLE**



- 1** - Vishay Semiconductors product
- 2** - Current rating (30 = 30 A)
- 3** - C = common cathode
- 4** - T = TO-220
- 5** - H = hyperfast recovery
- 6** - Voltage rating (02 = 200 V)
- 7** - H = AEC-Q101 qualified
- 8** - Environmental digit:  
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

<b>ORDERING INFORMATION</b> (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-30CTH02HN3	50	1000	Antistatic plastic tube

<b>LINKS TO RELATED DOCUMENTS</b>		
Dimensions	TO-220AB	<a href="http://www.vishay.com/doc?95222">www.vishay.com/doc?95222</a>
Part marking information	TO-220AB-N3	<a href="http://www.vishay.com/doc?95028">www.vishay.com/doc?95028</a>





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