



Vishay General Semiconductor

Photovoltaic Solarcell Protection Schottky Rectifier

High Barrier Technology for Improved High Temperature Performance

This datasheet reflects specifications of product in actual application.



FEATURES

- Guardring for overvoltage protection
- Low forward voltage drop, low power losses
- High efficiency operation
- High forward surge capability
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

PRIMARY CHARACTERISTICS				
I _{F(AV)}	15 A			
V_{RRM}	45 V			
I _{FSM}	300 A			
V_F at $I_F = 15 A$	0.46 V			
T _{OP} max.	175 °C			

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: P600, molded epoxy over passivated junction Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test **Polarity:** Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	R SYMBOL SB		UNIT	
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)} (1)	15	A	
	I _{F(AV)} (2)	7		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	300	А	
Operating junction and storage temperature range	T _{OP} , T _{STG}	P, T _{STG} - 55 to + 175		
Junction temperature in DC forward current without reverse bias, $t \le 1$ h (fig. 1)	T ^J (3)	≤ 200		

Notes

- (1) With heatsink, T_L = 25 °C
- (2) Without heatsink, free air
- (3) Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

SB15H45

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.48	-	V
	I _F = 7.5 A			0.50	-	
	I _F = 15 A			0.56	0.64	
	I _F = 5 A	T _A = 125 °C		0.35	-	
	I _F = 7.5 A			0.39	-	
	I _F = 15 A			0.46	0.54	
Reverse current	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	10	300	μΑ
	v _R = 45 v	T _A = 125 °C		8	20	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	1020	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: 10 ms pulse width

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	SB15H45	UNIT	
Thermal resistance	R ₀ JA (1)	66	°C/W	
	R _{0JL} (1)	14		
Typical thermal resistance	R _{0JL} (2)	3.5	°C/W	

Notes

(1) Without heatsink, free air

 $^{(2)}$ T_A = 75 °C, T_L = 125 °C, T_J = 175 °C, infinite mass at 0.375" (9.5 mm) lead length

ORDERING INFORMATION (Example)				
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
SB15H45-E3/54	1.756	54	800	13" diameter paper tape and reel

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

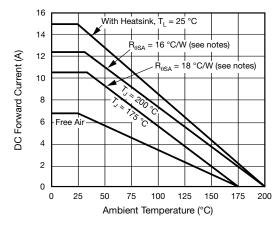


Fig. 1 - Forward Current Derating Curve

Notes

Mounted on junction box

Using DC forward current

Junction box SA (sink to ambient)

Assumes $R_{\theta LS}$ (lead to sink) of 5 $^{\circ}\text{C/W}$

Thermal resistance $R_{\theta SA}$ (sink to ambient):

$$R_{\theta SA} = \frac{(T_J - T_A)}{P_D} - (R_{\theta JL} + R_{\theta LS})$$

 P_D : Power dissipation $P_D = V_F \times I_F$



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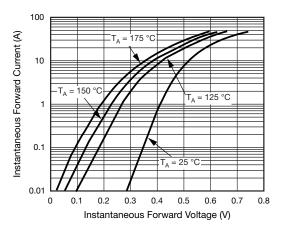


Fig. 2 - Typical Instantaneous Forward Characteristics

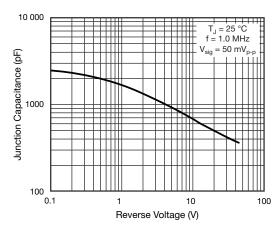


Fig. 4 - Typical Junction Capacitance

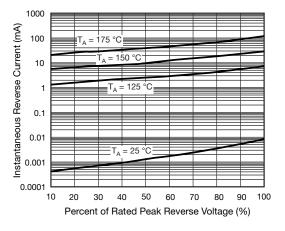
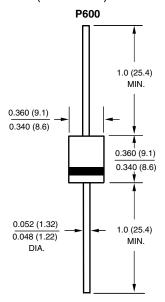


Fig. 3 - Typical Reverse Characteristics

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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