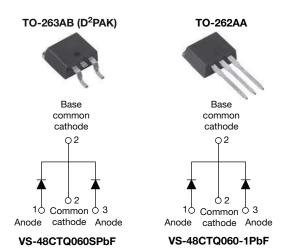


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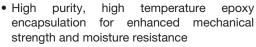
### High Performance Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	2 x 20 A
$V_R$	60 V
V <sub>F</sub> at I <sub>F</sub>	0.58 V
I <sub>RM</sub> max.	89 mA at 125 °C
T <sub>J</sub> max.	150 °C
Diode variation	Common cathode
E <sub>AS</sub>	13

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop
- High frequency operation





COMPLIANT HALOGEN FREE

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	Α			
V <sub>RRM</sub>		60	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1000	Α			
V <sub>F</sub>	20 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V			
T <sub>J</sub>	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-48CTQ060SPbF VS-48CTQ060-1PbF	UNITS		
Maximum DC reverse voltage	$V_{R}$	60	V		
Maximum working peak reverse voltage	$V_{RWM}$	00	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CON	DITIONS	VALUES	UNITS		
Maximum average per leg	1	I <sub>F(AV)</sub> 50 % duty cycle at T <sub>C</sub> = 111 °C, rectangular waveform		20			
forward current, see fig. 5 per device	IF(AV)			40			
Maximum peak one cycle non-repetitive	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1000	А		
surge current per leg, see fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		260			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 1.50  \text{A},  L = 11  ^{\circ}$	1.5 mH	13	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to ze Frequency limited by T <sub>J</sub> maxim		1.50	Α		



# VS-48CTQ060SPbF, VS-48CTQ060-1PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		20 A	T _ 25 °C	0.61	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	- T <sub>J</sub> = 25 °C	0.83	V
	VFM ('')	20 A	T <sub>J</sub> = 125 °C	0.58	
		40 A		0.75	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	2	mA
See fig. 2	IRM ('')	T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	89	IIIA
Threshold Voltage	V <sub>F(TO)</sub>	T T maximum		0.37	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J \text{ maximum}$		8.26	mΩ
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	ge 100 kHz to 1 MHz), 25 °C	1220	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	nm from package body	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Maximum thermal resistance, junction to case per leg  Maximum thermal resistance, junction to case per package		р	DC operation	2.0		
		- R <sub>thJC</sub>	DC operation	1.0	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque minim				6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf ⋅ in)	
Madina da ta			Case style TO-263AB (D2PAK)	48CT0	Q060S	
Marking device			Case style TO-262AA	48CTC	2060-1	

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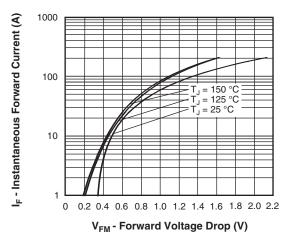


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

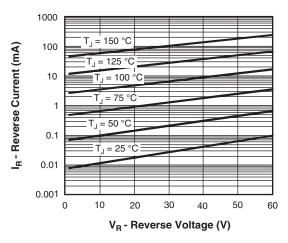


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

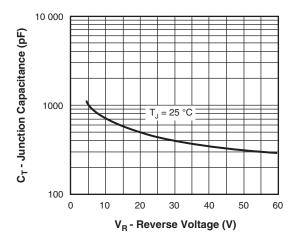


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

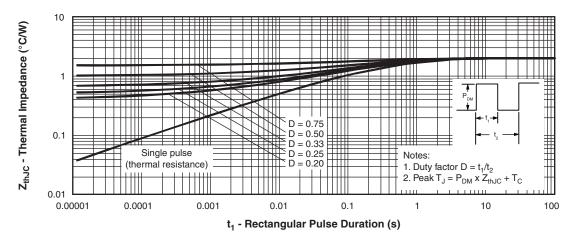


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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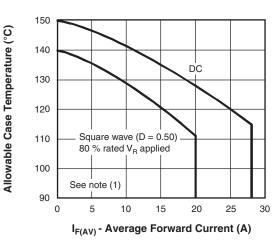


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

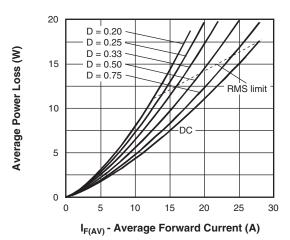


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

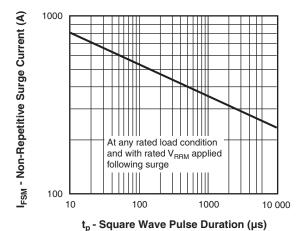


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

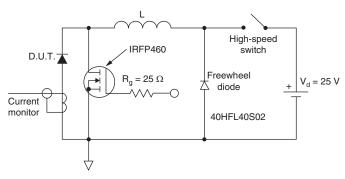


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

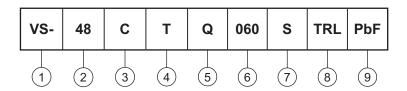
 $^{(1)}$  Formula used:  $T_C = T_J$  - (Pd + Pd\_{REV}) x R<sub>thJC</sub>; Pd = Forward power loss =  $I_{F(AV)}$  x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd\_{REV} = Inverse power loss = V\_{R1} x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 10 V



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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

- Current rating (40 A)

Circuit configuration: C = common cathode

**4** - T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (060 = 60 V)

7 - • S = D<sup>2</sup>PAK

• -1 = TO-262

8 - • None = tube (50 pieces)

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK only)

9 - PbF = lead (Pb)-free

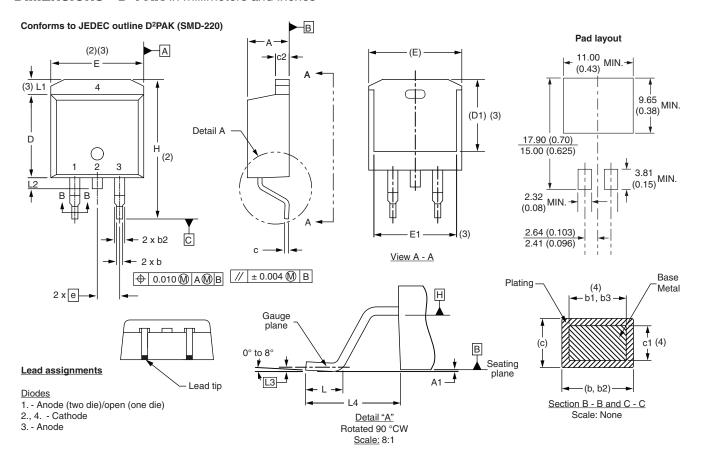
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-48CTQ060SPBF	50	1000	Antistatic plastic tubes			
VS-48CTQ060STRRPBF	800	800	13" diameter plastic tape and reel			
VS-48CTQ060STRLPBF	800	800	13" diameter plastic tape and reel			
VS-48CTQ060-1PBF	50	1000	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046			
Dimensions	TO-262AA	www.vishay.com/doc?95419			
Part marking information		www.vishay.com/doc?95008			
Packaging information		www.vishay.com/doc?95032			

### Vishay Semiconductors

### **D**<sup>2</sup>**PAK**, **TO**-262

### **DIMENSIONS - D<sup>2</sup>PAK** in millimeters and inches



SYMBOL	MILLIMETERS		INC	NOTES	
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
Е	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	·
L4	4.78	5.28	0.188	0.208	·

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}$  Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

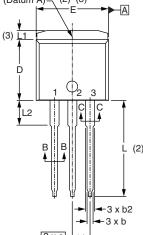
(7) Outline conforms to JEDEC outline TO-263AB

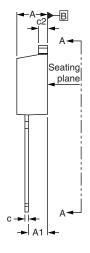
Document Number: 95014 Revision: 31-Mar-09

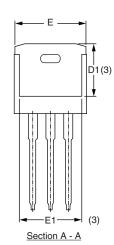


### **DIMENSIONS - TO-262** in millimeters and inches

# Modified JEDEC outline TO-262 (Datum A) - (2) (3)







**⊕** 0.010 **M** A **M** B

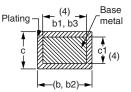
#### Lead assignments



#### Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

CYMPOL	MILLIM	ETERS	INC	HES	NOTES
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
Е	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	) BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

#### **Notes**

- $^{(1)}$  Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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