

## N Channel 100 V (D-S) MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ)
100	0.0082 at $V_{GS} = 10$ V	90 <sup>d</sup>	97

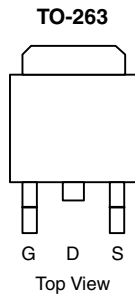
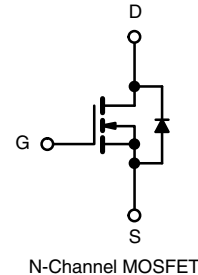
### FEATURES

- TrenchFET<sup>®</sup> Power MOSFETS
- 175 °C Junction Temperature
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC


**RoHS**  
COMPLIANT

### APPLICATIONS

- Power Supply
  - Secondary Synchronous Rectification
- Industrial
- Primary Switch


**Ordering Information:** SUM90N10-8m2P-E3 (Lead (Pb)-free)


### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	90 <sup>d</sup>
		$T_C = 70$ °C	90 <sup>d</sup>
Pulsed Drain Current	$I_{DM}$	240	A
Avalanche Current	$I_{AS}$	60	
Single Avalanche Energy <sup>a</sup>	$E_{AS}$	180	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_C = 25$ °C	300 <sup>b</sup>
		$T_A = 25$ °C <sup>c</sup>	3.75
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

### THERMAL RESISTANCE RATINGS

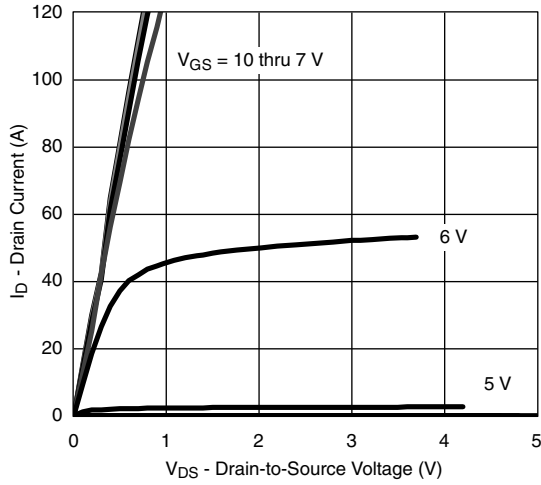
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	$R_{thJA}$	40	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	0.5	

Notes:

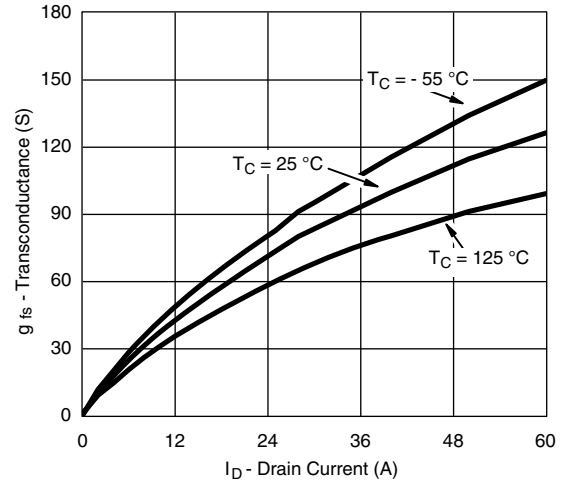
- Duty cycle  $\leq 1$  %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).
- Package limited.



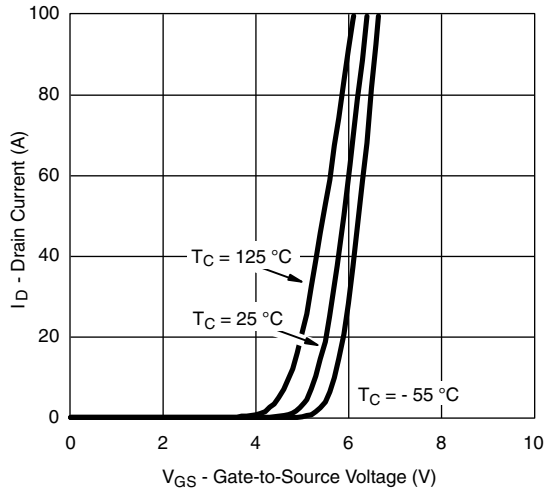
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



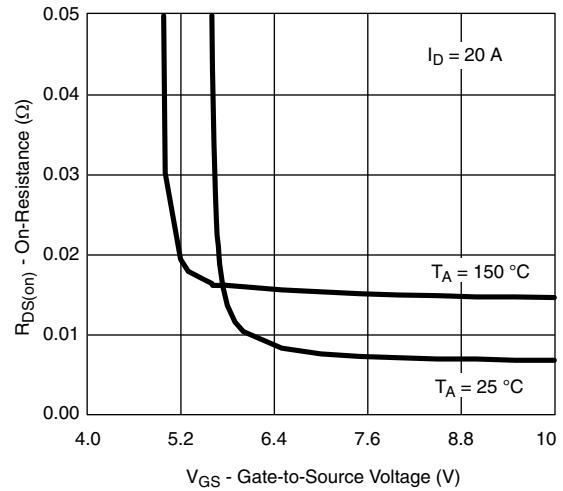
**Output Characteristics**



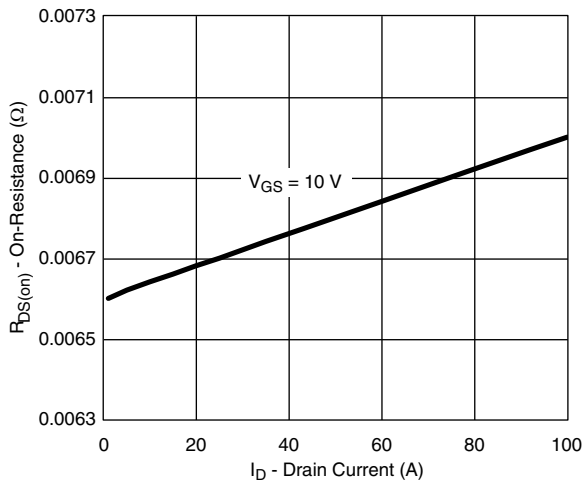
**Transconductance**



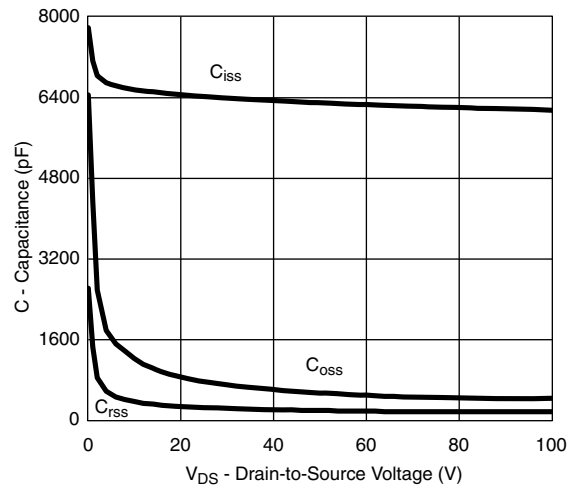
**Transfer Characteristics**



**On-resistance vs. Gate-to-Source Voltage**

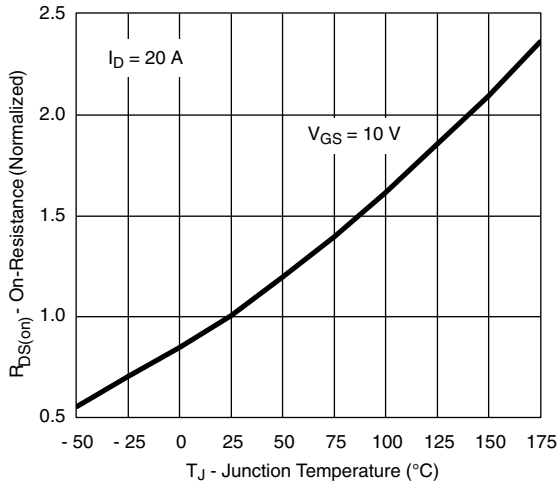


**On-Resistance vs. Drain Current**

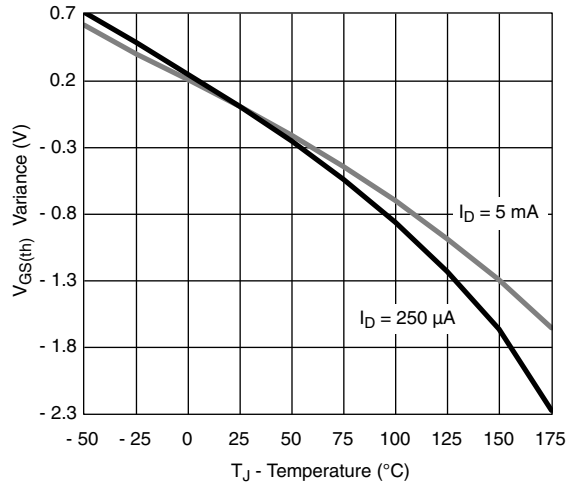


**Capacitance**

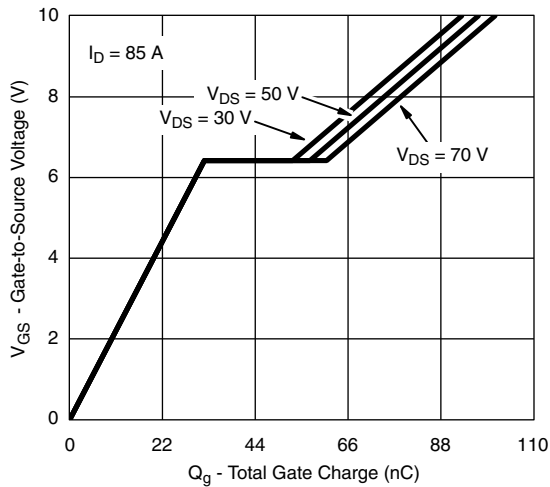
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



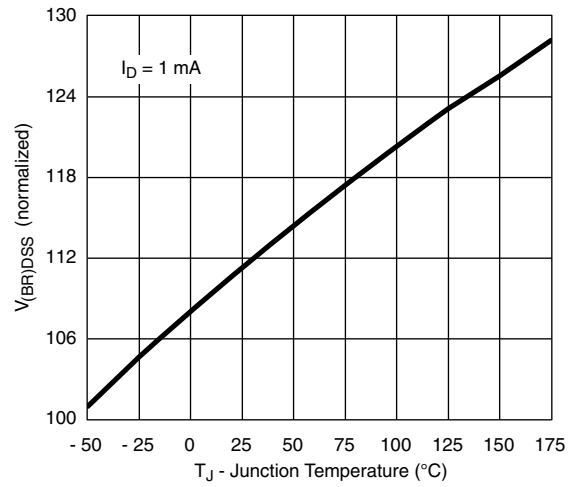
**On-Resistance vs. Junction Temperature**



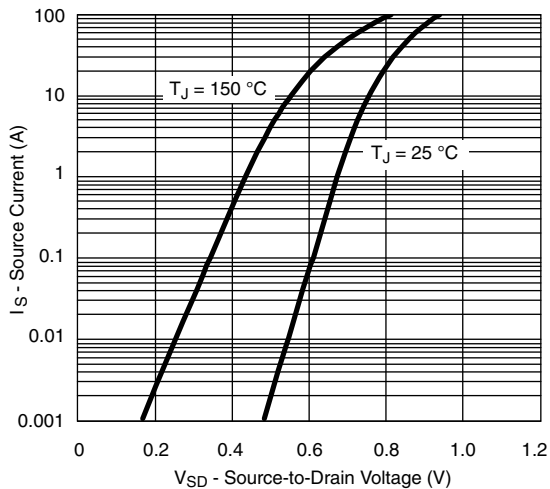
**Threshold Voltage**



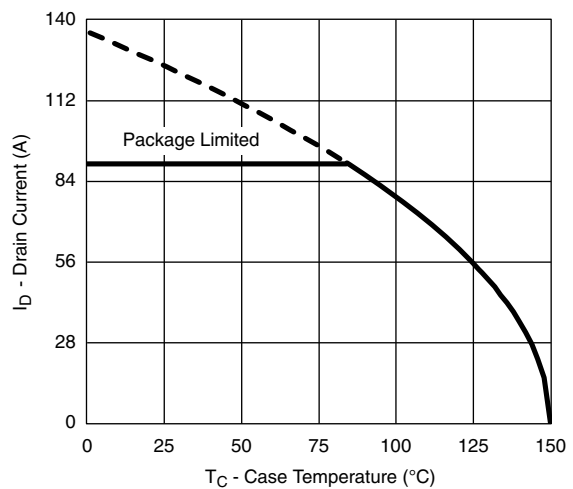
**Gate Charge**



**Drain Source Breakdown vs. Junction Temperature**

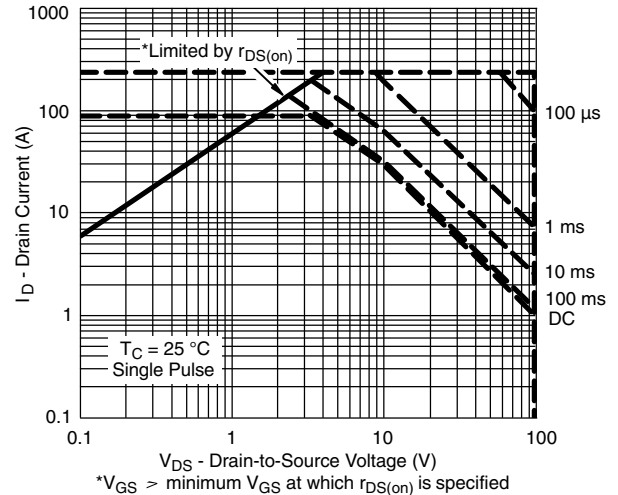
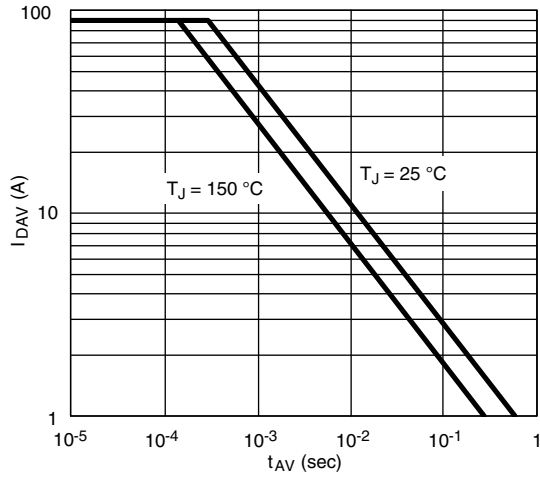


**Source-Drain Diode Forward Voltage**



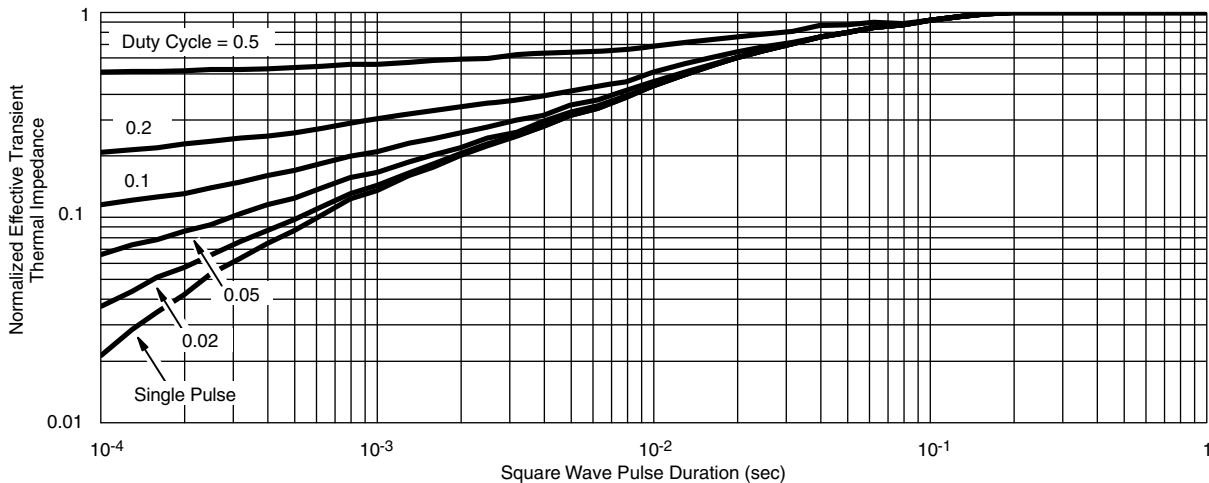
**Maximum Drain Current vs. Case Temperature**

## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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# TO-263 (D<sup>2</sup>PAK): 3-LEAD



DIM.	INCHES		MILLIMETERS		
	MIN.	MAX.	MIN.	MAX.	
A	0.160	0.190	4.064	4.826	
b	0.020	0.039	0.508	0.990	
b1	0.020	0.035	0.508	0.889	
b2	0.045	0.055	1.143	1.397	
c*	Thin lead	0.013	0.018	0.330	0.457
	Thick lead	0.023	0.028	0.584	0.711
c1	Thin lead	0.013	0.017	0.330	0.431
	Thick lead	0.023	0.027	0.584	0.685
c2	0.045	0.055	1.143	1.397	
D	0.340	0.380	8.636	9.652	
D1	0.220	0.240	5.588	6.096	
D2	0.038	0.042	0.965	1.067	
D3	0.045	0.055	1.143	1.397	
D4	0.044	0.052	1.118	1.321	
E	0.380	0.410	9.652	10.414	
E1	0.245	-	6.223	-	
E2	0.355	0.375	9.017	9.525	
E3	0.072	0.078	1.829	1.981	
e	0.100 BSC		2.54 BSC		
K	0.045	0.055	1.143	1.397	
L	0.575	0.625	14.605	15.875	
L1	0.090	0.110	2.286	2.794	
L2	0.040	0.055	1.016	1.397	
L3	0.050	0.070	1.270	1.778	
L4	0.010 BSC		0.254 BSC		
M	-	0.002	-	0.050	
ECN: T13-0707-Rev. K, 30-Sep-13					
DWG: 5843					

**Notes**

- Plane B includes maximum features of heat sink tab and plastic.
- No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- Pin-to-pin coplanarity max. 4 mils.
- \*: Thin lead is for SUB, SYB.  
Thick lead is for SUM, SYM, SQM.
- Use inches as the primary measurement.
- This feature is for thick lead.

**RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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