Vishay Siliconix

N Channel 100 V (D-S) MOSFET

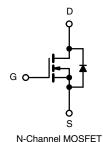
| PRODUCT SUMMARY | | | | | |
|--------------------------|--|--------------------|----------------------|--|--|
| V _{(BR)DSS} (V) | R_{DS(on)} (Ω) | I _D (A) | Q _g (Тур) | | |
| 100 | 0.0082 at V_{GS} = 10 V | 90 ^d | 97 | | |

FEATURES

- TrenchFET[®] Power MOSFETS
- 175 °C Junction Temperature
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC ٠

APPLICATIONS

- Power Supply - Secondary Synchronous Rectification
- Industrial
- **Primary Switch**



| N-Channel | М |
|-----------|---|
| | |

| ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \degree C$, unless otherwise noted) | | | | | |
|--|-------------------------------------|-----------------------------------|------------------|-----|--|
| Parameter | Symbol | Limit | Unit | | |
| Drain-Source Voltage | V _{DS} | 100 | v | | |
| Gate-Source Voltage | V _{GS} | ± 20 | v | | |
| Continuous Drain Current (T ₁ = 175 °C) | T _C = 25 °C | I _D | 90 ^d | | |
| Continuous Drain Current (1j = 173 C) | T _C = 70 °C | ^{'D} | 90 ^d | A | |
| Pulsed Drain Current | I _{DM} | 240 | | | |
| Avalanche Current | I _{AS} | 60 | | | |
| Single Avalanche Energy ^a | L = 0.1 mH | E _{AS} | 180 | mJ | |
| | T _C = 25 °C | Р | 300 ^b | \A/ | |
| Maximum Power Dissipation ^a | T _A = 25 °C ^c | – P _D – | 3.75 | W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 175 | °C | |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|-------------------|-------|--------|--|--|
| Parameter | Symbol | Limit | Unit | | |
| Junction-to-Ambient (PCB Mount) ^c | R _{thJA} | 40 | °C/W | | |
| Junction-to-Case (Drain) | R _{thJC} | 0.5 | - 'C/W | | |

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

d. Package limited.

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TO-263

G D s

Top View

Ordering Information: SUM90N10-8m2P-E3 (Lead (Pb)-free)

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|---|----------------------|---|------|--------|--------|------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{DS} = 0 V$, $I_{D} = 250 \mu A$ | 100 | | | V | |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \ \mu A$ | 2.5 | | 4.5 | | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 20 V$ | | | ± 250 | nA | |
| | | $V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | | | 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V_{DS} = 100 V, V_{GS} = 0 V, T_{J} = 125 °C | | | 50 | μA | |
| | | $V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$ | | | 250 | | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 10 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$ | 70 | | | Α | |
| | D | V _{GS} = 10 V, I _D = 20 A | | 0.0067 | 0.0082 | | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V_{GS} = 10 V, I _D = 20 A, T _J = 125 °C | | 0.0127 | 0.0170 | Ω | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 20 A | | 62 | | S | |
| Dynamic ^b | | | | | | | |
| Input Capacitance | C _{iss} | | | 6290 | | pF | |
| Output Capacitance | C _{oss} | V_{GS} = 0 V, V_{DS} = 50 V, f = 1 MHz | | 535 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 182 | | | |
| Total Gate Charge ^c | Qg | | | 97 | 150 | | |
| Gate-Source Charge ^c | Q _{gs} | $V_{DS} = 50$ V, $V_{GS} = 10$ V, $I_{D} = 85$ A | | 32 | | nC | |
| Gate-Drain Charge ^c | Q _{gd} | | | 25 | | | |
| Gate Resistance | Rg | f = 1 MHz | 0.28 | 1.4 | 2.8 | Ω | |
| Turn-On Delay Time ^c | t _{d(on)} | | | 23 | 35 | | |
| Rise Time ^c | t _r | V_{DD} = 50 V, R_L = 0.588 Ω | | 17 | 26 | | |
| Turn-Off Delay Time ^c | t _{d(off)} | $\text{I}_\text{D}\cong$ 85 A, V_GEN = 10 V, R_g = 1 Ω | | 34 | 52 | ns | |
| Fall Time ^c | t _f | | | 9 | 18 | | |
| Source-Drain Diode Ratings and Cha | aracteristics (| T _C = 25 °C) ^b | | · | | | |
| Continuous Current | ا _S | | | | 85 | ^ | |
| Pulsed Current | I _{SM} | | | | 240 | A | |
| Forward Voltage ^a | V _{SD} | $I_{F} = 30 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$ | | 0.85 | 1.5 | V | |
| Reverse Recovery Time | t _{rr} | | | 61 | 100 | ns | |
| Peak Reverse Recovery Current | I _{RM(REC)} | I _F = 75 A, di/dt = 100 A/μs | | 3 | 4.5 | Α | |
| Reverse Recovery Charge | Q _{rr} | | | 91 | 130 | μC | |

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

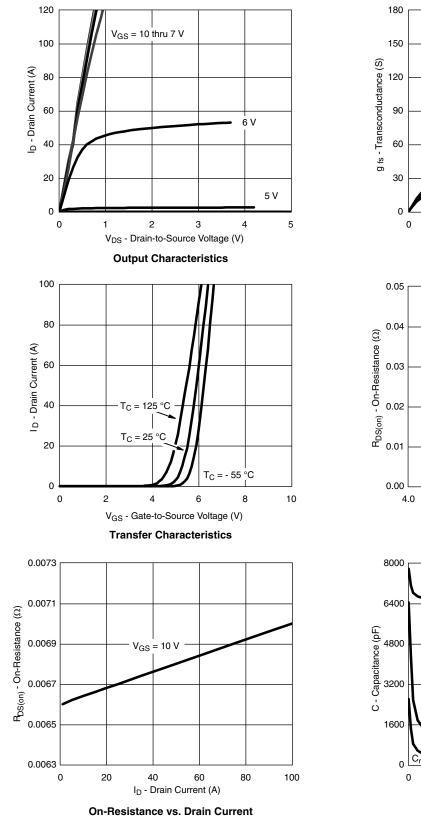
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



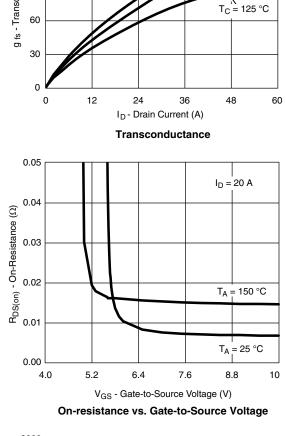
55 °C

Т_С

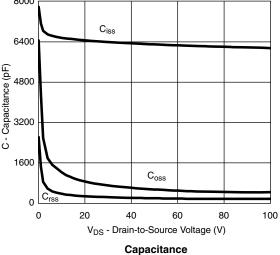
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



 $T_C = 25 \circ C$

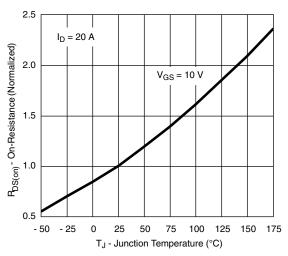


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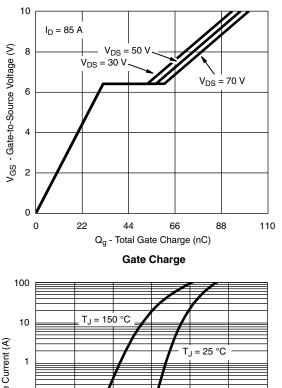
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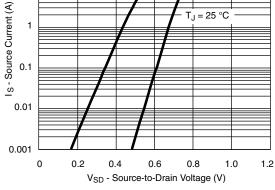
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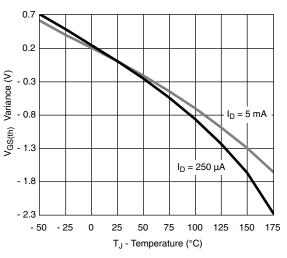


On-Resistance vs. Junction Temperature

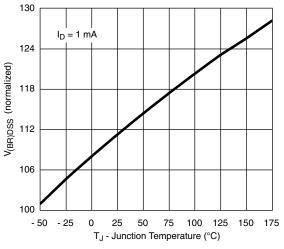




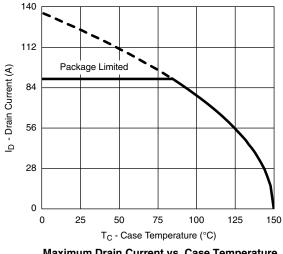
Source-Drain Diode Forward Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature



Maximum Drain Current vs. Case Temperature

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Document Number: 74643 S12-0335-Rev. B, 13-Feb-12

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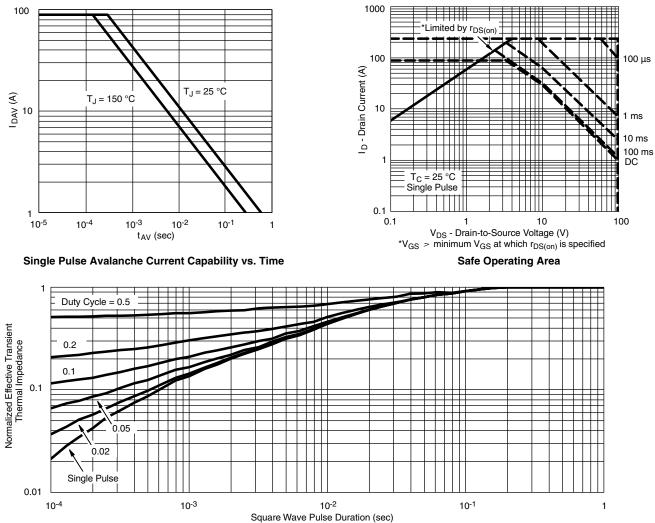
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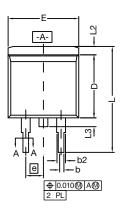
Normalized Thermal Transient Impedance, Junction-to-Case

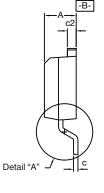
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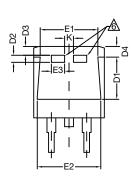


Vishay Siliconix

TO-263 (D²PAK): 3-LEAD









DETAIL A (ROTATED 90°)



| | | INCHES | | MILLIN | IETERS | |
|--|------------|-----------|-------|-----------|--------|--|
| DIM. | | MIN. | MAX. | MIN. | MAX. | |
| A | | 0.160 | 0.190 | 4.064 | 4.826 | |
| b | | 0.020 | 0.039 | 0.508 | 0.990 | |
| | b1 | 0.020 | 0.035 | 0.508 | 0.889 | |
| | b2 | 0.045 | 0.055 | 1.143 | 1.397 | |
| с* | Thin lead | 0.013 | 0.018 | 0.330 | 0.457 | |
| С | Thick lead | 0.023 | 0.028 | 0.584 | 0.711 | |
| c1 | Thin lead | 0.013 | 0.017 | 0.330 | 0.431 | |
| CI | Thick lead | 0.023 | 0.027 | 0.584 | 0.685 | |
| | c2 | 0.045 | 0.055 | 1.143 | 1.397 | |
| | D | 0.340 | 0.380 | 8.636 | 9.652 | |
| | D1 | 0.220 | 0.240 | 5.588 | 6.096 | |
| D2 | | 0.038 | 0.042 | 0.965 | 1.067 | |
| D3 | | 0.045 | 0.055 | 1.143 | 1.397 | |
| | D4 | 0.044 | 0.052 | 1.118 | 1.321 | |
| E | | 0.380 | 0.410 | 9.652 | 10.414 | |
| | E1 | 0.245 | - | 6.223 | - | |
| E2 | | 0.355 | 0.375 | 9.017 | 9.525 | |
| E3 | | 0.072 | 0.078 | 1.829 | 1.981 | |
| | е | 0.100 BSC | | 2.54 BSC | | |
| | К | 0.045 | 0.055 | 1.143 | 1.397 | |
| | L | 0.575 | 0.625 | 14.605 | 15.875 | |
| L1 | | 0.090 | 0.110 | 2.286 | 2.794 | |
| L2 | | 0.040 | 0.055 | 1.016 | 1.397 | |
| L3 | | 0.050 | 0.070 | 1.270 | 1.778 | |
| | L4 | 0.010 BSC | | 0.254 BSC | | |
| М | | - | 0.002 | - | 0.050 | |
| ECN: T13-0707-Rev. K, 30-Sep-13 DWG: 5843 | | | | | | |

Notes

- 1. Plane B includes maximum features of heat sink tab and plastic. 2. No more than 25 $\,\%\,$ of L1 can fall above seating plane by
- max. 8 mils.3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB.
 - Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

This feature is for thick lead.

Revison: 30-Sep-13



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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