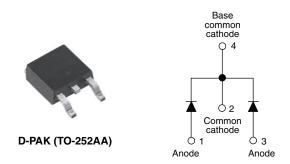
Vishay Semiconductors

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High Performance Schottky Rectifier, 2 x 3.5 A



PRODUCT SUMMARY							
Package	D-PAK (TO-252AA)						
I _{F(AV)}	2 x 3.5 A						
V _R	100 V						
V _F at I _F	See Electrical table						
I _{RM}	4.9 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	5 mJ						

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Center tap configuration
- · Small foot print, surface mountable
- High frequency operation
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-6CWQ10FN-M3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	7	А						
V _{RRM}		100	V						
I _{FSM}	t _p = 5 μs sine	440	А						
V _F	3 A _{pk} , T _J = 125 °C (per leg)	0.63	V						
TJ	Range	-40 to +150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-6CWQ10FN-M3	UNITS						
Maximum DC reverse voltage	V _R	100	V						
Maximum working peak reverse voltage	V _{RWM}	100							

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS					
Maximum average per leg		50 % duty cyclo at $T_{-} = 135 $ °C r	ty avala at T = 125 °C, reatangular wayafarm						
See fig. 5 per devic	F(AV)	50 % duty cycle at T_{C} = 135 °C, rectangular waveform		7	^				
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	440	A				
non-repetitive surge current per leg See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	70					
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 10 mH		5.0	mJ				
Repetitive avalanche current per leg		Current decaying linearly to zero i Frequency limited by T_J maximum	•	0.5	А				

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
		3 A	T _{.1} = 25 °C	0.81	V			
Maximum forward	V _{FM} ⁽¹⁾	6 A	1j=23 0	0.96				
voltage drop per leg See fig. 1	VFM ()	3 A	T ₁ = 125 °C	0.63				
		6 A	1j = 125 C	0.74				
Maximum reverse	I (1)	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	1	mA			
leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = nateu V _R	4.9				
Threshold voltage	V _{F(TO)}	T T maximum		0.48	V			
Forward slope resistance	r _t	IJ = IJ maximum	$T_J = T_J maximum$		mΩ			
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$, (test signal ran	92	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	5.0	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

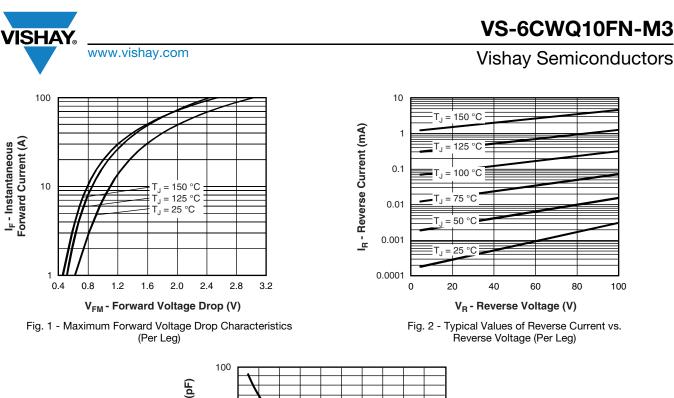
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T_{J} ⁽¹⁾ , T_{Stg}		-40 to +150	°C			
Maximum thermal resistance,	per leg	D	DC operation See fig. 4	4.70	°C/W			
junction to case	per device	R _{thJC}		2.35				
				0.3	g			
Approximate weight				0.01	oz.			
Marking device			Case style D-PAK (similar to TO-252AA)	6CWQ10FN				

Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$



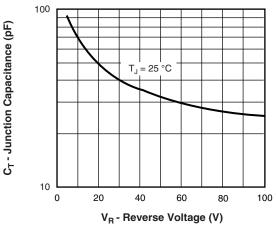


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

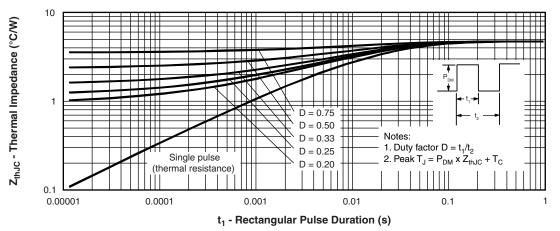
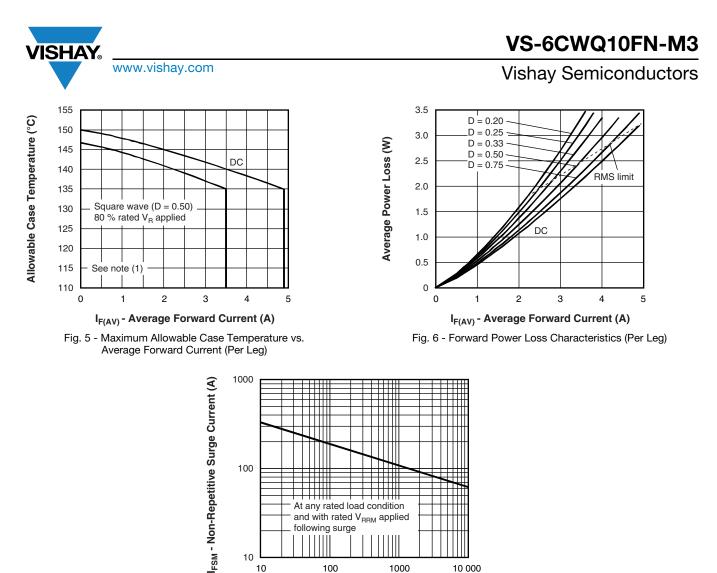


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

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<sup>(1)</sup> Formula used: T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};

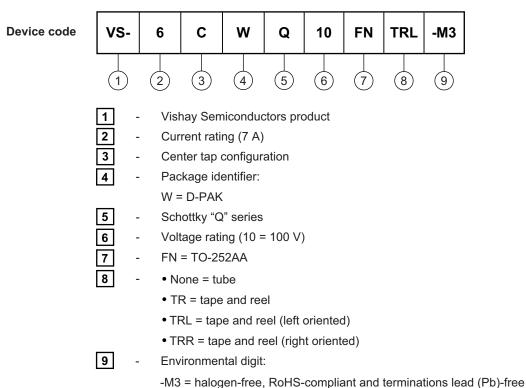
Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D) (see fig. 6);

Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R
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ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-6CWQ10FN-M3	75	3000	Antistatic plastic tube						
VS-6CWQ10FNTR-M3	2000	2000	13" diameter reel						
VS-6CWQ10FNTRL-M3	3000	3000	13" diameter reel						
VS-6CWQ10FNTRR-M3	3000	3000	13" diameter reel						

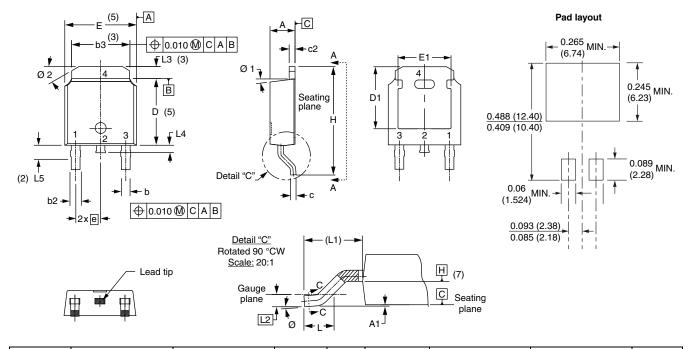
LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95627					
Part marking information	www.vishay.com/doc?95176					
Packaging information	www.vishay.com/doc?95033					





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA

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