

Vishay Siliconix

## N-Channel 30-V (D-S) MOSFET

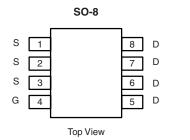
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)		
30	0.0085 at V <sub>GS</sub> = 10 V	13.5		
	0.0110 at V <sub>GS</sub> = 4.5 V	11		

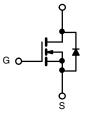
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested

D







Ordering Information: Si4420BDY-T1-E3 (Lead (Pb)-free) Si4420BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	30		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	13.5	9.5	
	T <sub>A</sub> = 70 °C		10.8	7.5	
Pulsed Drain Current		I <sub>DM</sub>	50		А
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.3	1.26	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	20		
Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	20		mJ
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	2.5	1.4	W
	T <sub>A</sub> = 70 °C		1.6	0.9	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lunation to Ambienta	t < 10 s	R <sub>thJA</sub>	40	50	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		70	90		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	23	28		

Notes:

a. Surface Mounted on FR4 board,  $t \leq 10 \mbox{ s.}$ 

# Si4420BDY

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.0		3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	
		$V_{DS} = 30$ V, $V_{GS} = 0$ V, $T_{J} = 55$ °C			5	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
Drain-Source On-State Resistance <sup>a</sup>	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 13.5 A		0.007	0.0085	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 11 A		0.009	0.0110	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 13.5 A		50		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{\rm S}$ = 2.3 A, $V_{\rm GS}$ = 0 V		0.75	1.1	V
Dynamic <sup>b</sup>						
Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 13.5 \text{ A}$		16	25	
Total Gate Charge	Q <sub>gt</sub>			31	50	-0
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 13.5 A		6.6		nC
Gate-Drain Charge	Q <sub>gd</sub>			4.0		
Gate Resistance	R <sub>g</sub>		0.5	1.0	1.5	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			15	25	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		11	18	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_\text{D}\cong \text{1}$ A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 6 $\Omega$		40	60	ns
Fall Time	t <sub>f</sub>			12	20	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.3 A, dl/dt = 100 A/μs		30	50	

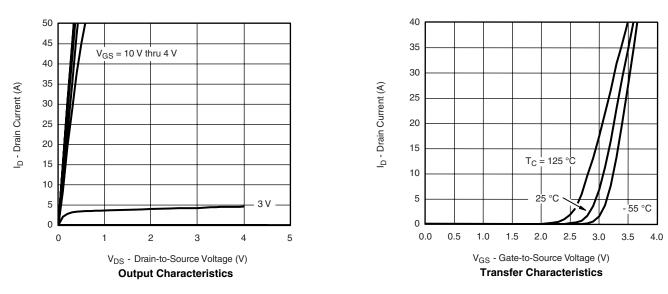
Notes:

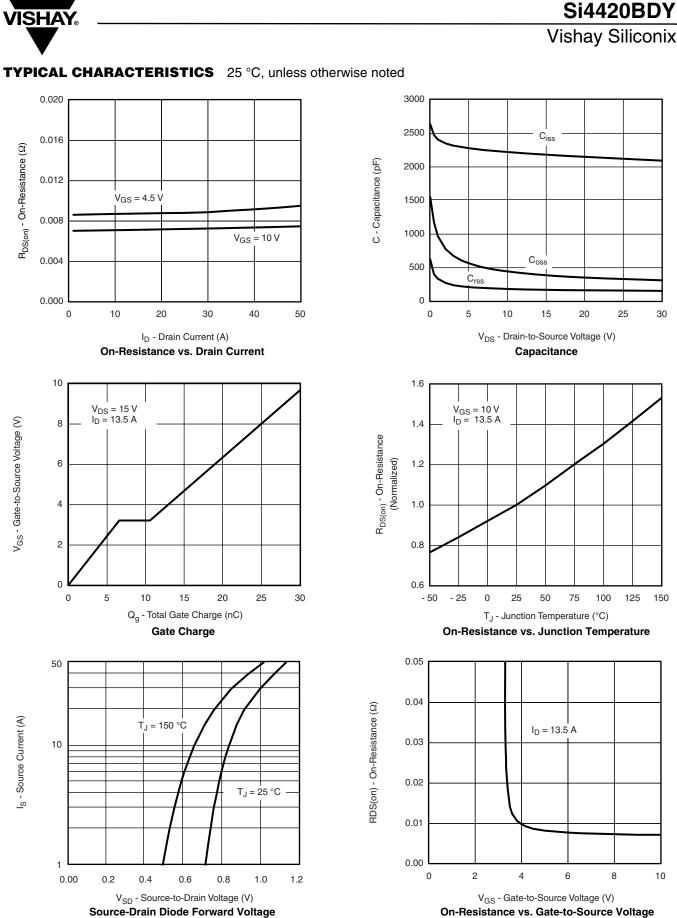
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





30

150

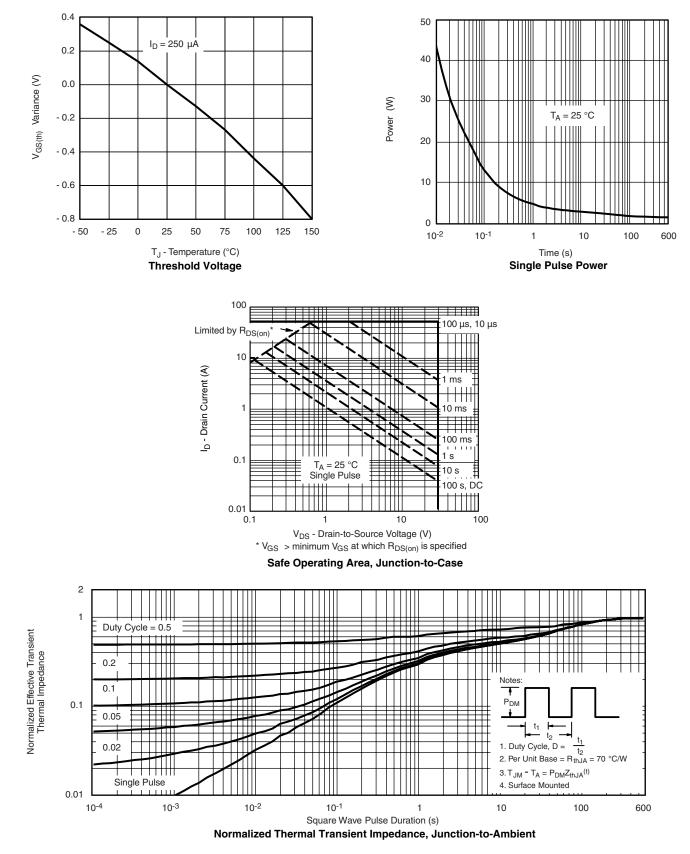
10

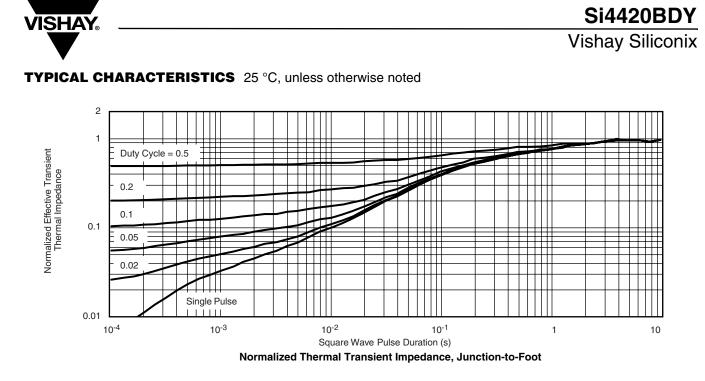
## Si4420BDY

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg273067">www.vishay.com/ppg273067</a>.



# Package Information

Vishay Siliconix

### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





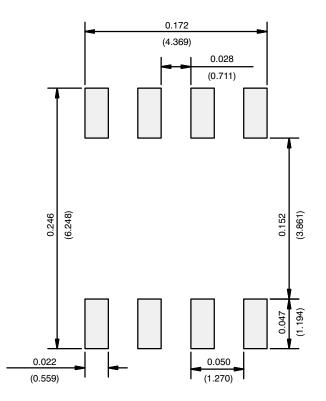
	MILLIM	IETERS	INCHES			
DIM	Min	Мах	Min	Max		
A	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498						

## **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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