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# VS-ETL1506SHM3, VS-ETL1506-1HM3

Vishay Semiconductors

# Ultrafast Rectifier, 15 A FRED Pt<sup>®</sup>





Base cathode 2 1 0 3 N/C Anode TO-262AA

VS-ETL1506SHM3

VS-ETL1506-1HM3

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Anode

**01** 

N/C

PRODUCT SUMMARY								
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA							
I <sub>F(AV)</sub>	15 A							
V <sub>R</sub>	600 V							
V <sub>F</sub> at I <sub>F</sub> (typ.)	0.85 V							
t <sub>rr</sub> (typ.)	60 ns							
T <sub>J</sub> max.	175 °C							
Diode variation	Single die							

#### FEATURES

- · State of the art low forward voltage drop
- Ultrafast recovery time
- 175 °C operating junction temperature
- Low leakage current
- $\bullet$  Designed and qualified according to JEDEC  $^{\textcircled{B}}\text{-}JESD$  47
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

State of the art, ultralow  $V_F$ , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

### APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS						
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V						
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 152 °C	15	٨						
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	200	A						
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C						

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	I <sub>R</sub> = 100 μA	600	-	-					
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	0.99	1.07	V					
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.85	0.91					
		$V_{R} = V_{R}$ rated	-	0.01	15					
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	100	μA 0				
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	12	-	pF				
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH				

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HALOGEN

FREE



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	ONDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100$	0 A/µs, V <sub>R</sub> = 30 V	-	60	110				
	+	$I_F = 15 \text{ A}, \text{ d}I_F/\text{d}t = 10$	-	185	270					
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	210	-	ns			
		T <sub>J</sub> = 125 °C		-	290	-				
Deek receiver / ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	$I_{\rm F} = 15  {\rm A}$	-	20	-	A			
Peak recovery current		T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/µs V <sub>R</sub> = 390 V	-	26	-				
	0	T <sub>J</sub> = 25 °C		-	2.2	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	4.0	-	- μC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C				
Thermal resistance, junction to case	R <sub>thJC</sub>		-	1.3	1.51	°C/W				
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70					
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-					
Waight			-	2.0	-	g				
Weight			-	0.07	-	oz.				
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)				
		Case style D <sup>2</sup> PAK modified		ETL1	506SH					
Marking device		Case style TO-262		ETL15	506-1H					

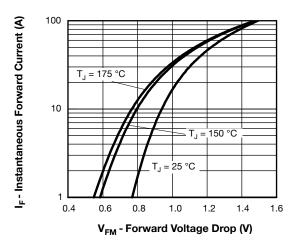
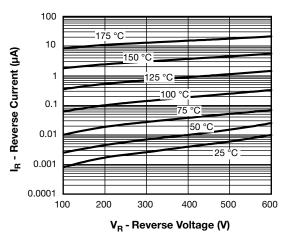
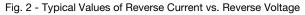


Fig. 1 - Typical Forward Voltage Drop Characteristics





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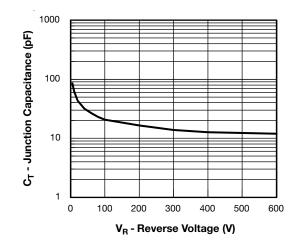


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

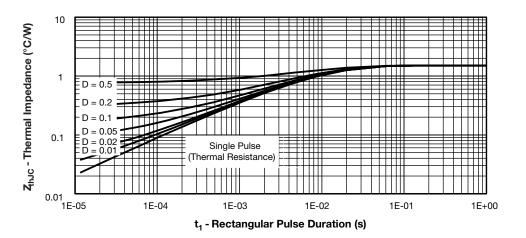
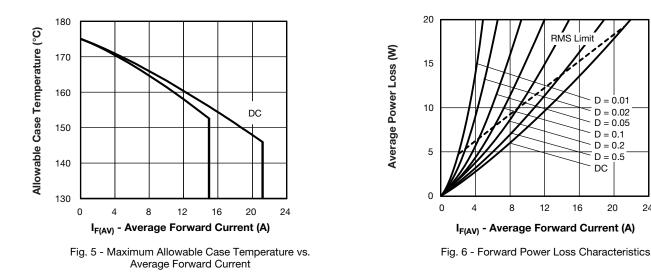


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics



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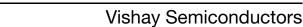
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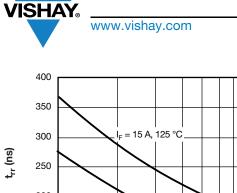
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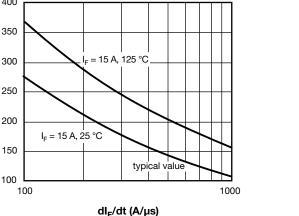


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

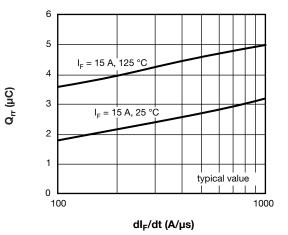


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

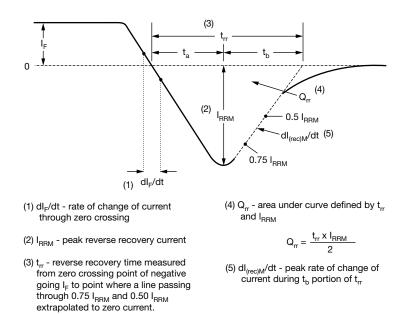
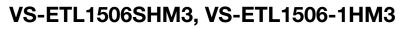


Fig. 9 - Reverse Recovery Waveform and Definitions





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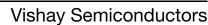
#### **ORDERING INFORMATION TABLE**

Device code	vs-	E	т	L	15	06	S	TRL	н	М3	
		2	3	4	5	6	7	8	9	10	
	1       -       Vishay Semiconductors product         2       -       Circuit configuration         E = single diode										
	3	- T = TO-220									
	4		L = ultrafast recovery time Current code (15 = 15 A)								
	5 · 6 ·			le (15 = le (06 =	,						
	7		= D <sup>2</sup> PA		,						
		• -1	= TO-20	62							
	8	- • None = tube (50 pieces)									
		• TF	<ul> <li>TRL = tape and reel (left oriented, for D<sup>2</sup>PAK package)</li> </ul>								
		• TF	<ul> <li>TRR = tape and reel (right oriented, for D<sup>2</sup>PAK package)</li> </ul>								
	9	- H=	AEC-Q	101 qua	lified						
	10	- M3	= halog	en-free,	RoHS-0	complia	nt, and	termina	tions le	ad (Pb)-	

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-ETL1506SHM3	50	1000	Antistatic plastic tube						
VS-ETL1506-1HM3	50	1000	Antistatic plastic tube						
VS-ETL1506STRRHM3	800	800	13" diameter reel						
VS-ETL1506STRLHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS								
Dimensions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046						
Dimensions	TO-262AA	www.vishay.com/doc?95419						
Port marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444						
Part marking information	TO-262AA	www.vishay.com/doc?95443						
Packaging information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95032						

## **Outline Dimensions**

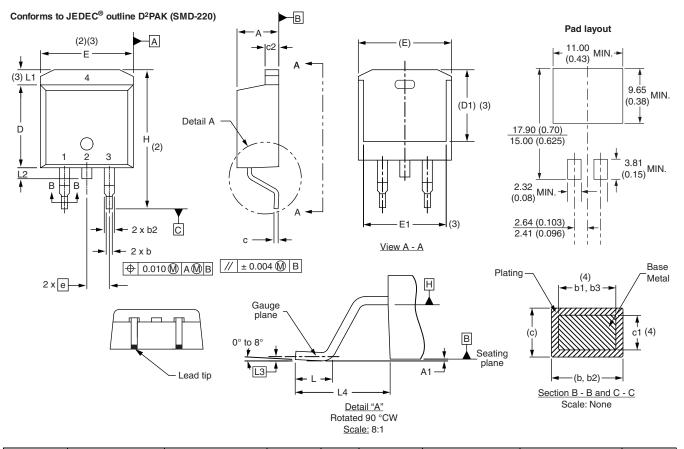


D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches

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SYMBOL	MILLIM	IETERS	INC	HES	NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

<sup>(5)</sup> Datum A and B to be determined at datum plane H

<sup>(6)</sup> Controlling dimension: inch

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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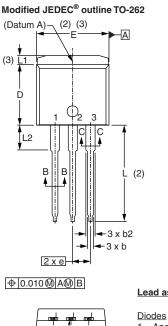
### **Outline Dimensions**



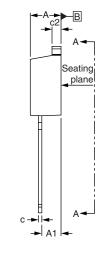
**Vishay Semiconductors** 

**TO-262** 

#### **DIMENSIONS** in millimeters and inches

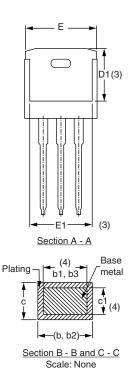


Lead tip -



Lead assignments

1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode



MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 \_ 1.65 0.065 3 \_ 3.36 0.132 0.146 L2 3.71

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

<sup>(4)</sup> Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) , D1 (minimum) and L2 where dimensions derived the actual package outline

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