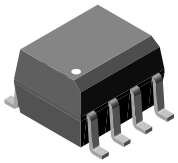
Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 package, 110 °C Rated



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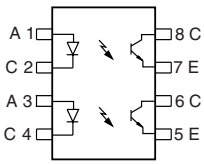


FEATURES

- Operating temperature from - 55 °C to + 110 °C
- Two Channel Coupler
- SOIC-8 Surface Mountable Package
- Isolation Test Voltage, 4000 V_{RMS}
- Compatible with Dual Wave, Vapor Phase and IR Reflow Soldering
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

Pb-free

e3

RoHS COMPLIANT

DESCRIPTION

The 110 °C rated ILD1205T/1206T/1207T are optically coupled pairs with a Gallium Arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD1205T/1206T/1207T come in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

A specified minimum and maximum CTR allows a narrow tolerance in the electrical design of the adjacent circuits. The high BV_{CEO} of 70 V gives a higher safety margin compared to the industry standard of 30 V.

AGENCY APPROVALS

- UL - File No. E52744 System Code Y
- CUL - File No. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-2(VDE0884) Available with Option 1

APPLICATIONS

- AC Adapters
- PLCs
- Switch Mode Power Supplies
- DC/DC Converters
- Microprocessor I/O Interfaces
- General impedance matching circuits

ORDER INFORMATION	
PART	**REMARKS**
ILD1205T	CTR 40 - 80 %, SOIC-8
ILD1206T	CTR 63 - 125 %, SOIC-8
ILD1207T	CTR 100 - 200 %, SOIC-8

Note:
For additional information on the available options refer to Option Information.

ABSOLUTE MAXIMUM RATINGS[1)]				
PARAMETER	**TEST CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
INPUT				
Continuous forward current per channel		I_F	30	mA
Peak reverse voltage		V_R	6.0	V
Peak pulsed current	1.0 µs, 300 pps	I_{FSM}	1.0	A
Power dissipation		P_{diss}	50	mW
Derate linearly from 25 °C			0.5	mW/°C
OUTPUT				
Collector-emitter voltage		V_{CE}	70	V
Power dissipation per channel		P_{diss}	125	mW
Derate linearly from 25 °C			1.25	mW/°C
COUPLER				
Isolation test voltage	t = 1.0 s	V_{ISO}	4000	V_{RMS}
Operating temperature		T_{amb}	- 55 to + 110	°C
Total package dissipation ambient (2 LEDs + 2 detectors, 2 channels)		P_{tot}	300	mW
Derate linearly from 25 °C			4.0	mW/°C
Storage temperature		T_{stg}	- 55 to + 150	°C

Note:
[1)] T_{amb} = 25 °C, unless otherwise specified.
Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

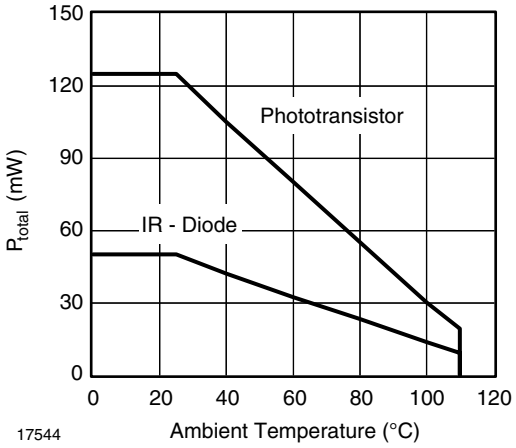
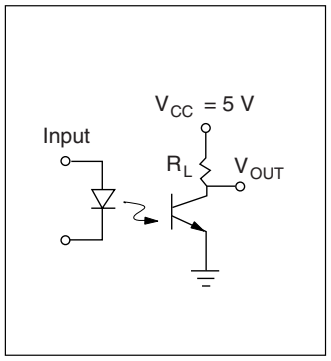


Figure 1. Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS[1)]							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN**	**TYP.**	**MAX**	**UNIT**
INPUT							
Forward voltage	I_F = 10 mA		V_F		1.2	1.55	V
Reverse current	V_R = 6.0 V		I_R		0.1	100	µA
Capacitance	V_R = 0		C_I		25		pF
OUTPUT							
Collector-emitter leakage current	V_{CE} = 10 V, I_F = 0		I_{CEO}		5.0	50	nA
Collector-emitter breakdown voltage	I_C = 100 µA		BV_{CEO}	70			V
Emitter-collector breakdown voltage	I_E = 10 µA		BV_{ECO}	7.0			V
Collector-emitter saturation voltage	I_F = 10 mA, I_C = 2.5 mA		V_{CEsat}			0.4	V
Collector-emitter capacitance	V_{CE} = 0		C_{CE}		10		pF
COUPLER							
DC Current Transfer Ratio	V_{CE} = 5.0 V, I_F = 10 mA	ILD1205T	CTR	40		80	%
		ILD1206T	CTR	63		125	%
		ILD1207T	CTR	100		200	%
Capacitance (input-output)			C_{IO}		0.5		pF

Note:
[1)] T_{amb} = 25 °C, unless otherwise specified.
Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN.**	**TYPE**	**MAX**	**UNIT**
Turn-on time	I_C = 2.0 mA, R_L = 100 Ω, V_{CC} = 5.0 V		t_{on}	5.0			µs
Turn-off time	I_C = 2.0 mA, R_L = 100 Ω, V_{CC} = 5.0 V		t_{off}	4.0			µs



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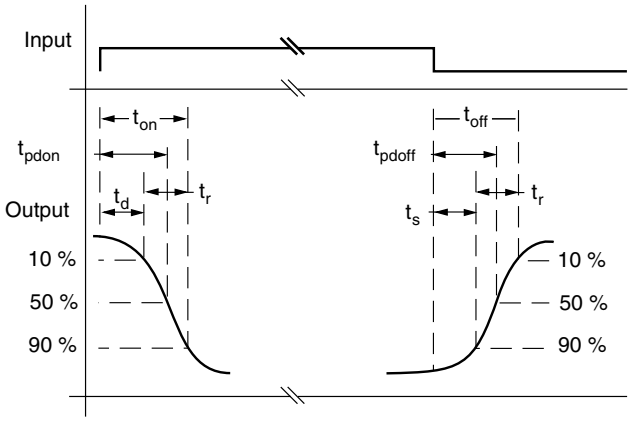


Figure 2. Switching Test Circuit

SAFETY AND INSULATION RATINGS[1)]							
PARAMETER	**TEST CONDITION**	**PART**	**SYMBOL**	**MIN.**	**TYPE**	**MAX**	**UNIT**
Climatic Classification (according to IEC 68 part 1)					55/110/21		
Polution Degree (DIN VDE 0109)					2.0		mm
Comparative tracking index per DIN IEC112/VDE 0303 part 1, group IIIa per DIN VDE 6110 175 399				175		399	
V_{IOTM}			V_{IOTM}	6000			V
V_{IORM}			V_{IORM}	560			V
Resistance, input to output			R_{IO}		100		Ω
P_{SI}						350	mW
I_{SI}						150	mA
T_{SI}						165	°C
Creeapage				4.0			mm
Clearance				4.0			mm

Note:
[1)] As per IEC60747-5-2, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

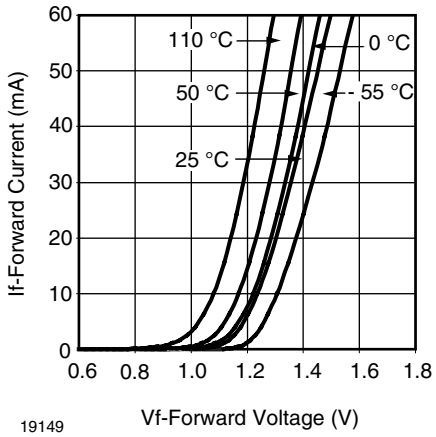


Figure 3. Forward Current vs. Forward Voltage

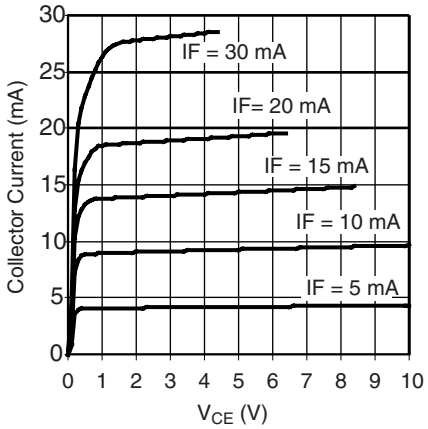


Figure 4. V_{CE} vs. I_C, (Non-Saturated)

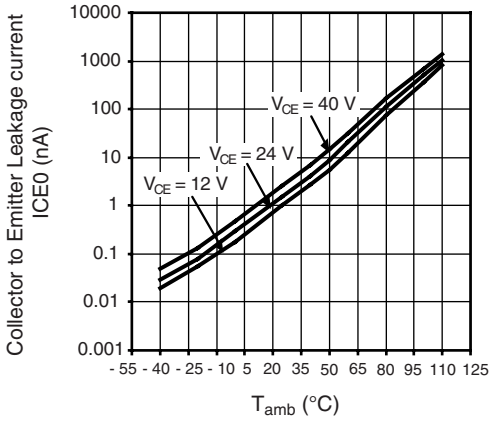
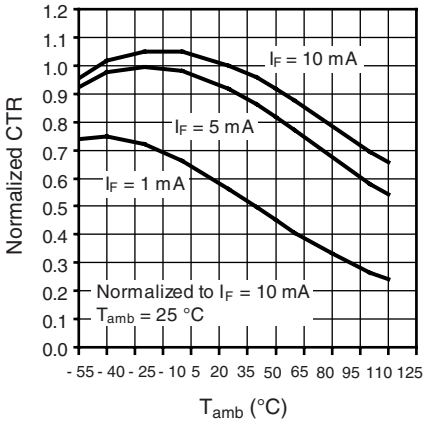


Figure 5. Collector to Emitter Leakage Current vs. Ambient Temperature



Normalized CTR vs. Ambient Temperature (Non-Saturated, V_{CE} = 5 V)

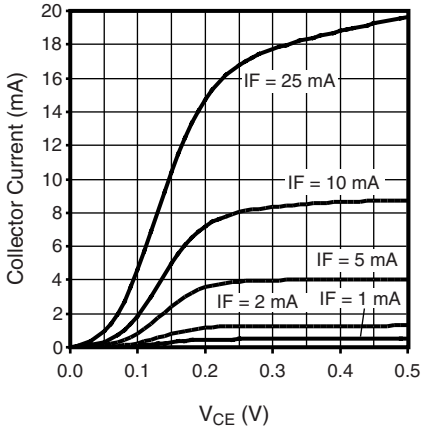


Figure 6. V_{CE} vs. I_C, (Saturated)

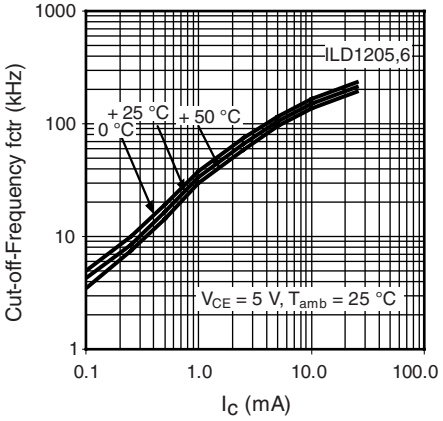


Figure 8. Cut-off-Frequency (- 3 dB) vs. Collector Current

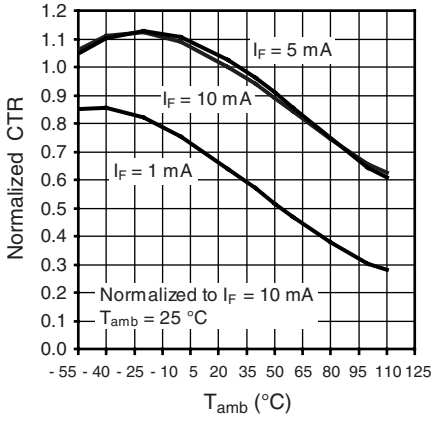


Figure 7. Normalized CTR vs. Ambient Temperature (Saturated, V_{CE} = 0.4 V)

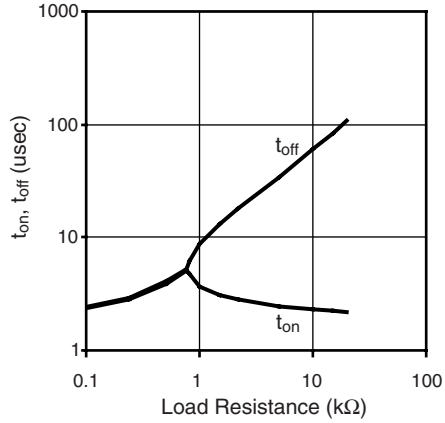
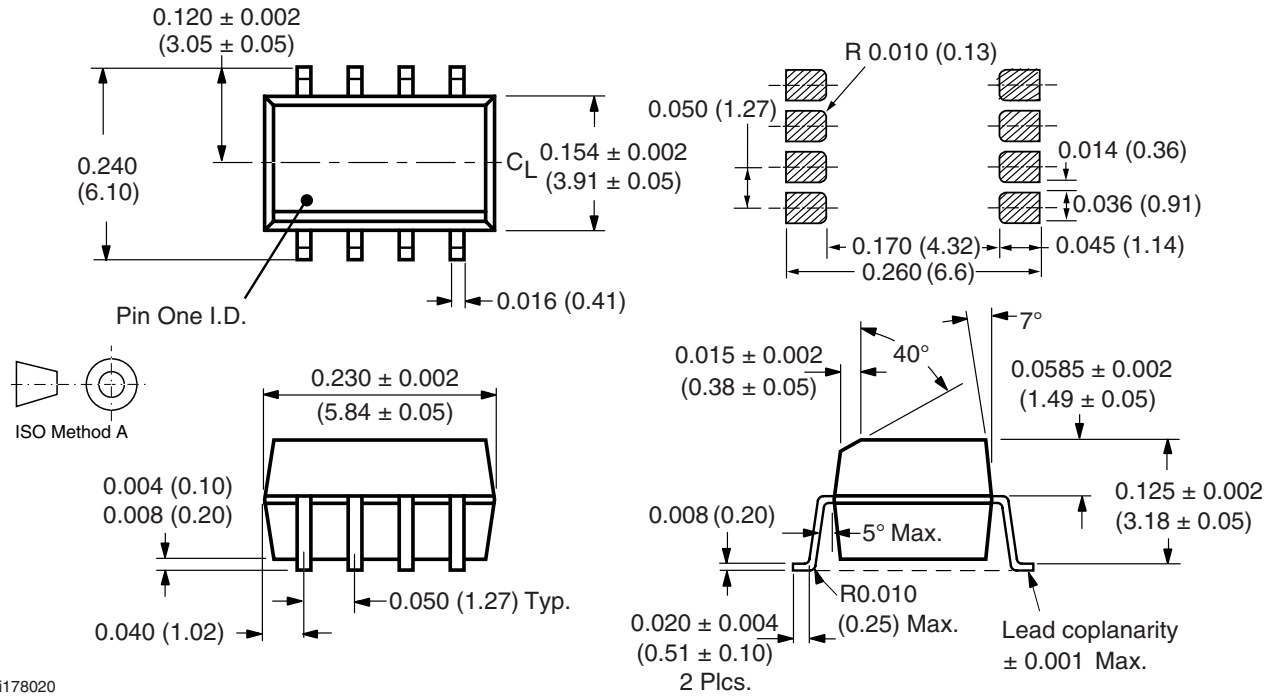


Figure 9. t_{on}, t_{off} vs. Load Resistance (100 Ω to 20000 Ω)

PACKAGE DIMENSIONS in inches (millimeters)



OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

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