

Hyperfast Rectifier, 4 A FRED Pt®



Package

Circuit configuration



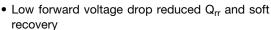
DPAK (TO-252AA)

Single

PRIMARY CHARACTERISTICS				
I _{F(AV)}	4 A			
V_{R}	200 V			
V _F at I _F	0.71 V			
t _{rr} (typ.)	23 ns			
T _J max.	175 °C			

FEATURES

- · Hyperfast recovery time
- 175 °C max. operating junction temperature
- Output rectification freewheeling





FREE

- Low leakage current
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V_{RRM}		200	V			
Average rectified forward current	I _{F(AV)}	T _C = 164 °C	4				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	80	Α			
Peak repetitive forward current	I _{FM}	T _C = 164 °C, f = 20 kHz, d = 50 %	8				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-			
Forward voltage	V _F	I _F = 4 A	-	0.87	0.95	95 V		
		I _F = 4 A, T _J = 150 °C	-	0.71	0.80			
B		$V_R = V_R$ rated	-	-	3			
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	2	20	<u>μ</u> Α		
Junction capacitance	C _T	V _R = 600 V	-	17	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 A, dI_F/dt =$	100 A/μs, V _R = 30 V	-	23	-	ns ns	
Payaraa raaayany tima		$I_F = 1.0 \text{ A}, dI_F/dt =$	50 A/μs, V _R = 30 V	-	24	-		
Reverse recovery time	t _{rr}	T _J = 25 °C	$I_F = 4 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	20	-		
		T _J = 125 °C		-	27	-		
Dook recovery current	I _{RRM}	T _J = 25 °C		-	2	-	Α	
Peak recovery current		T _J = 125 °C		-	3.4	-	^	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	20	-	nC	
		T _J = 125 °C		-	46	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C		
Thermal resistance, junction to case per leg	R _{thJC}		-	2.7	3.2	°C/W		
Approximate weight				0.3		g		
Approximate weight				0.01	•	oz.		
Marking device		Case style DPAK (TO-252AA)	4EWH02FNH					

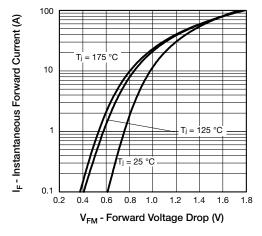


Fig. 1 - Typical Forward Voltage Drop Characteristics

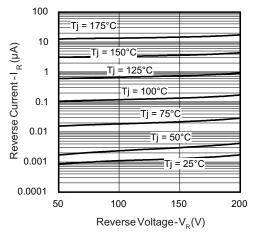


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

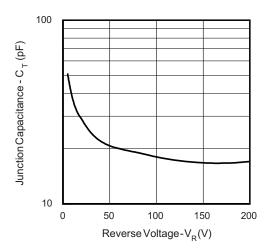


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

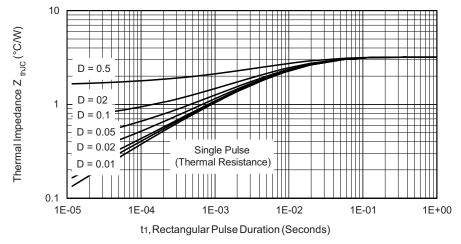


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

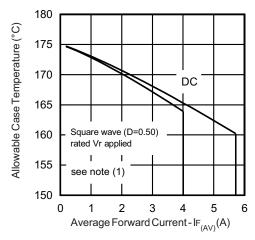


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

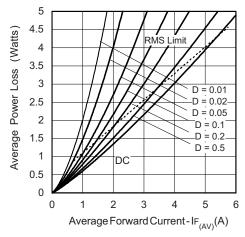


Fig. 6 - Forward Power Loss Characteristics

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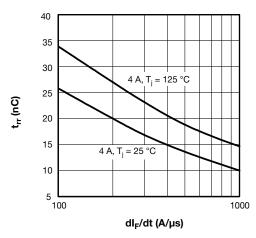


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

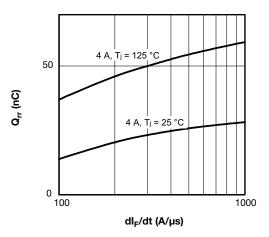
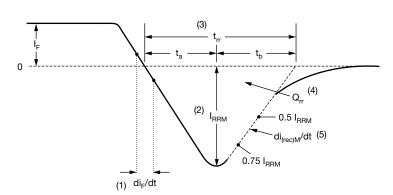


Fig. 8 - Typical Stored Charge vs. dl_E/dt

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{F}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

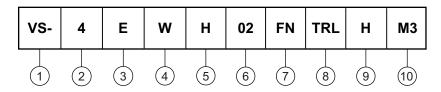
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (4 = 4 A)

Circuit configuration:

E = single diode

Package identifier:

W = DPAK

5 - H = hyperfast recovery

6 - Voltage rating (02 = 200 V)

7 - FN = TO-252AA

8 - • None = tube

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - H = AEC-Q101 qualified

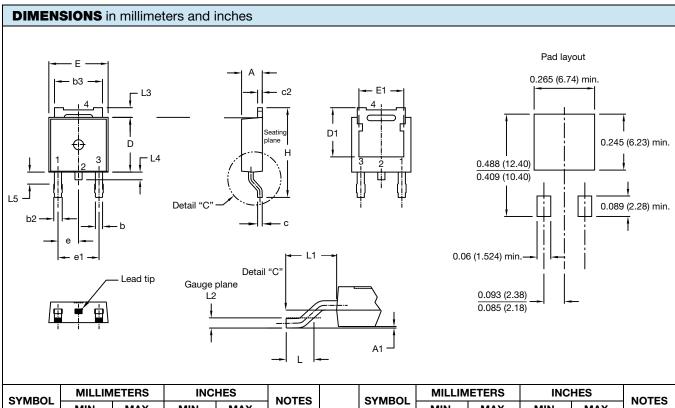
10 - Environmental digit:

M3 = halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-4EWH02FNHM3	75	3000	Antistatic plastic tube				
VS-4EWH02FNTRHM3	2000	2000	13" diameter reel				
VS-4EWH02FNTRLHM3	3000	3000	13" diameter reel				
VS-4EWH02FNTRRHM3	3000	3000	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				
SPICE model	www.vishay.com/doc?95381				

DPAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIN	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		0.108 REF.	
L2	0.51	0.51 BSC		0.020 BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
	·	·		·	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA



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