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Vishay Siliconix

RoHS

COMPLIANT

HALOGEN

FREE



#### **PRODUCT SUMMARY** V<sub>DS</sub> (V) -60 $R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = -10 V 0.120 $R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = -4.5 V 0.150 Qg typ. (nC) 8 I<sub>D</sub> (A) a -4.7 Configuration Single

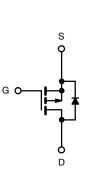
#### **FEATURES**

P-Channel 60 V (D-S) MOSFET

- TrenchFET<sup>®</sup> power MOSFET
- 100 % UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

· Primary side switch



P-Channel MOSFET

ORDERING INFORMATION				
Package	SO-8			
Lead (Pb)-free	Si9407BDY-T1-E3			
Lead (Pb)-free and halogen-free	Si9407BDY-T1-GE3			

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	-60	V	
Gate-source voltage		V <sub>GS</sub>	± 20	v	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		-4.7		
	T <sub>C</sub> = 70 °C		-3.8		
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-3.2 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		-2.6 <sup>b, c</sup>		
Pulsed drain current (10 µs width)		I <sub>DM</sub>	-20	- A	
Continuous source-drain diode current	T <sub>C</sub> = 25 °C		-4.2		
	T <sub>A</sub> = 25 °C	I <sub>S</sub>	-2 <sup>b, c</sup>		
Avalanche current		I <sub>AS</sub>	-15		
Single-pulse avalanche energy	L = 0.1 mH	E <sub>AS</sub>	11	mJ	
Maximum power dissipation	T <sub>C</sub> = 25 °C		5		
	T <sub>C</sub> = 70 °C		3.2	10/	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.4 <sup>b, c</sup>	— W	
	T <sub>A</sub> = 70 °C		1.5 <sup>b, c</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

#### THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient <sup>b, d</sup>		R <sub>thJA</sub>	42	53	°C ///	
Maximum junction-to-foot (drain)	Steady state	R <sub>thJF</sub>	19	25	°C/W	

#### Notes

a. Based on  $T_C = 25 \ ^{\circ}C$ 

b. Surface mounted on 1" x 1" FR4 board

c. t = 10 s

d. Maximum under steady state conditions is 85 °C/W

S09-0704-Rev. B, 27-Apr-09

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# Si9407BDY

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				•			
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	-60	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	1 250 4	-	-50	-	m)//°C	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	4	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	-1	-	-3	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA	
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	-1	μΑ	
Zero gate voltage drain current	IDSS	$V_{DS}$ = -60 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C	-	-	-10		
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	-20	-	-	А	
	_	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -3.2 \text{ A}$	-	0.100	0.120	Ω	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = -4.5 V, I <sub>D</sub> = -2.9 A	-	0.126	0.150		
Forward transconductance a	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -3.2 \text{ A}$	-	8.5	-	S	
Dynamic <sup>b</sup>							
Input capacitance	Ciss		-	600	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS}$ = -30 V, $V_{GS}$ = 0 V, f = 1 MHz	-	70	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	50	-		
Total gate charge Q <sub>g</sub>		$V_{DS}$ = -30 V, $V_{GS}$ = -10 V, $I_D$ = -3.2 A	-	14.5	22		
	Qg	g	-	8	12		
Gate-source charge	Q <sub>gs</sub>	$V_{DS}$ = -30 V, $V_{GS}$ = -4.5 V, $I_{D}$ = -3.9 A	-	2.2	-	nC	
Gate-drain charge	Q <sub>gd</sub>		-	3.7	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	-	14	-	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	30	45		
Rise time	tr	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{I}} = 11.5 \Omega$	-	70	105		
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong -2.6 \text{ A}, \text{ V}_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \ \Omega$	-	40	60	ns	
Fall time	t <sub>f</sub>		-	30	45		
Turn-on delay time	t <sub>d(on)</sub>		-	10	15		
Rise time	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, \text{ R}_{\text{I}} = 11.5 \Omega$	-	13	20		
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong$ -2.6 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$	-	35	55	ns	
Fall time	t <sub>f</sub>		-	30	45		
Drain-Source Body Diode Characteris	tics						
Continuous source-drain diode current	ا <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	-4.2	^	
Pulse diode forward current	I <sub>SM</sub>		-	-	-20	A	
Body diode voltage	V <sub>SD</sub>	$I_{\rm S}$ = -2 A, $V_{\rm GS}$ = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	30	50	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = -2 A, di/dt = -100 A/μs,	-	35	60	nC	
Reverse recovery fall time	t <sub>a</sub>	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$	-	16	-		
Reverse recovery rise time	t <sub>b</sub>		-	14	-	ns	

#### Notes

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %

b. Guaranteed by design, not subject to production testing

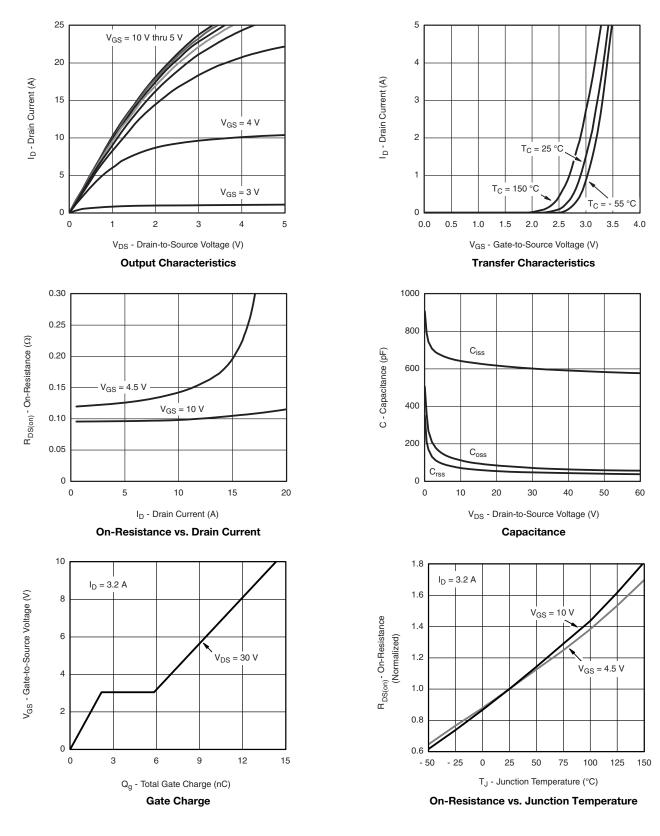
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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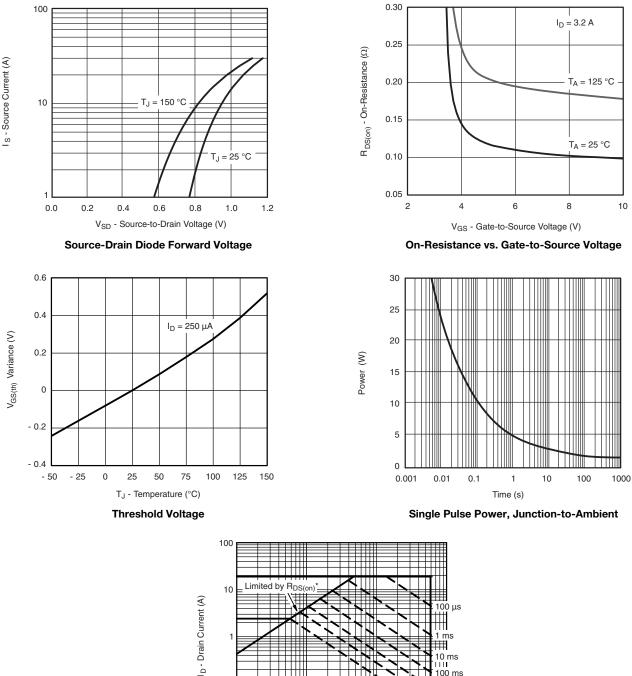
Document Number: 69902

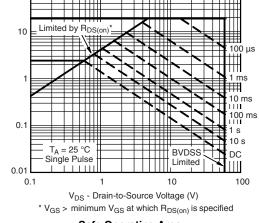
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### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





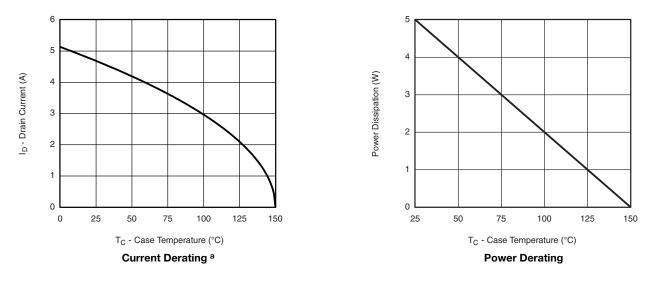
Safe Operating Area

Document Number: 69902



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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



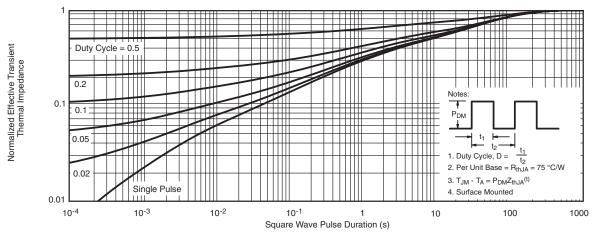
#### Note

a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

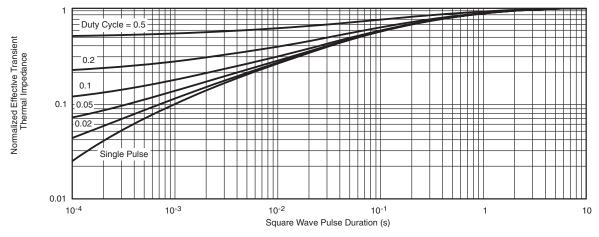


**Vishay Siliconix** 

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69902.



## Package Information

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### SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012





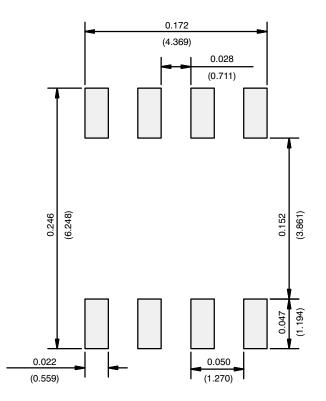
	MILLIM	IETERS	INC	HES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					

## **Application Note 826**

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**RECOMMENDED MINIMUM PADS FOR SO-8** 



Recommended Minimum Pads Dimensions in Inches/(mm)

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