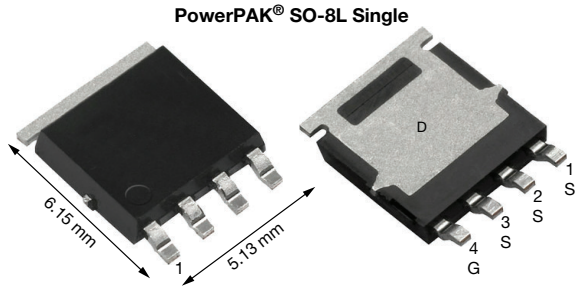
N-Channel 80 V (D-S) MOSFET

PowerPAK® SO-8L Single



Top View Bottom View

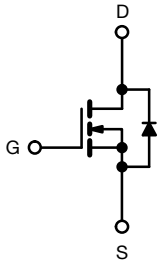
FEATURES

- TrenchFET® power MOSFET
- 100 % R_g and UIS tested
- Capable of operating with 5 V gate drive
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT HALOGEN FREE

APPLICATIONS

- DC/DC primary side switch
- Synchronous rectification
- High current switching



N-Channel MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	80
$R_{DS(on)}$ max. (Ω) at V_{GS} = 10 V	0.0062
$R_{DS(on)}$ max. (Ω) at V_{GS} = 7.5 V	0.0065
$R_{DS(on)}$ max. (Ω) at V_{GS} = 4.5 V	0.0095
Q_g typ. (nC)	24
I_D (A) [a, g]	60
Configuration	Single

ORDERING INFORMATION	
Package	PowerPAK SO-8L
Lead (Pb)-free and halogen-free	SiJ482DP-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	80	V
Gate-source voltage		V_{GS}	± 20	
Continuous drain current (T_J = 150 °C)	T_C = 25 °C	I_D	60 [g]	A
	T_C = 70 °C		60 [g]	
	T_A = 25 °C		21.1 [b, c]	
	T_A = 70 °C		16.9 [b, c]	
Pulsed drain current (t = 300 µs)		I_{DM}	100	
Continuous source-drain diode current	T_C = 25 °C	I_S	60 [g]	
	T_A = 25 °C		4.5 [b, c]	
Single pulse avalanche current	L = 0.1 mH	I_{AS}	30	
Single pulse avalanche energy		E_{AS}	45	mJ
Maximum power dissipation	T_C = 25 °C	P_D	69.4	W
	T_C = 70 °C		44.4	
	T_A = 25 °C		5 [b, c]	
	T_A = 70 °C		3.2 [b, c]	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) [d, e]			260	

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient [b, f]	t ≤ 10 s	R_{thJA}	20	25	°C/W
Maximum junction-to-case (drain)	Steady state	R_{thJC}	1.3	1.8	

Notes

a. Based on T_C = 25 °C
b. Surface mounted on 1" x 1" FR4 board
c. t = 10 s
d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SO-8L is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection
e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components
f. Maximum under steady state conditions is 65 °C/W
g. Package limited

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	80	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA	-	36	-	mV/°C
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$		-	-5.7	-	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1.5	-	2.7	V
Gate-source leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	V_{DS} = 80 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 55 °C	-	-	10	
On-state drain current [a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 10 V	30	-	-	A
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 20 A	-	0.0051	0.0062	Ω
		V_{GS} = 7.5 V, I_D = 15 A	-	0.0054	0.0065	
		V_{GS} = 4.5 V, I_D = 10 A	-	0.0068	0.0095	
Forward transconductance [a]	g_{fs}	V_{DS} = 10 V, I_D = 20 A	-	68	-	S
Dynamic [b]						
Input capacitance	C_{iss}	V_{DS} = 40 V, V_{GS} = 0 V, f = 1 MHz	-	2425	-	pF
Output capacitance	C_{oss}		-	1180	-	
Reverse transfer capacitance	C_{rss}		-	100	-	
Total gate charge	Q_g	V_{DS} = 40 V, V_{GS} = 10 V, I_D = 10 A	-	47	71	nC
		V_{DS} = 40 V, V_{GS} = 7.5 V, I_D = 10 A	-	36.5	55	
		V_{DS} = 40 V, V_{GS} = 4.5 V, I_D = 10 A	-	24	36	
Gate-source charge	Q_{gs}		-	6.6	-	
Gate-drain charge	Q_{gd}		-	10.2	-	
Output charge	Q_{oss}	V_{DS} = 40 V, V_{GS} = 0 V	-	69	105	
Gate resistance	R_g	f = 1 MHz	0.4	1.1	2.2	Ω
Turn-on delay time	$t_{d(on)}$	V_{DD} = 40 V, R_L = 4 Ω I_D ≅ 10 A, V_{GEN} = 10 V, R_g = 1 Ω	-	14	28	ns
Rise time	t_r		-	11	22	
Turn-off delay time	$t_{d(off)}$		-	36	72	
Fall time	t_f		-	9	18	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 40 V, R_L = 4 Ω I_D ≅ 10 A, V_{GEN} = 7.5 V, R_g = 1 Ω	-	16	32	
Rise time	t_r		-	13	26	
Turn-off delay time	$t_{d(off)}$		-	35	70	
Fall time	t_f			11	22	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	T_C = 25 °C	-	-	60	A
Pulse diode forward current [a]	I_{SM}		-	-	100	
Body diode voltage	V_{SD}	I_S = 4 A	-	0.73	1.1	V
Body diode reverse recovery time	t_{rr}	I_F = 10 A, di/dt = 100 A/µs, T_J = 25 °C	-	46	90	ns
Body diode reverse recovery charge	Q_{rr}		-	44	86	nC
Reverse recovery fall time	t_a		-	21	-	ns
Reverse recovery rise time	t_b		-	25	-	

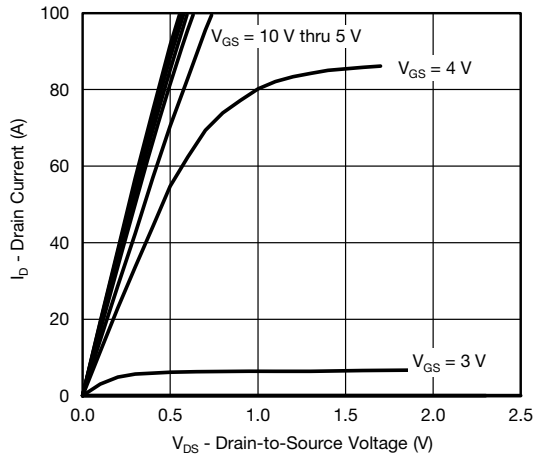
Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %

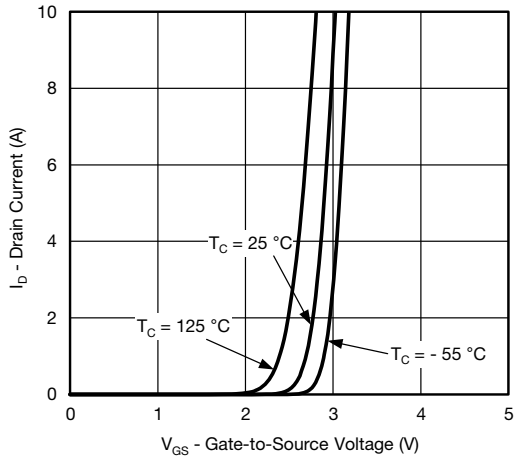
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

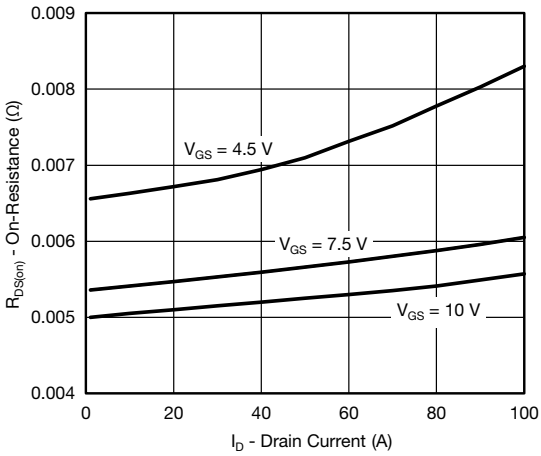
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



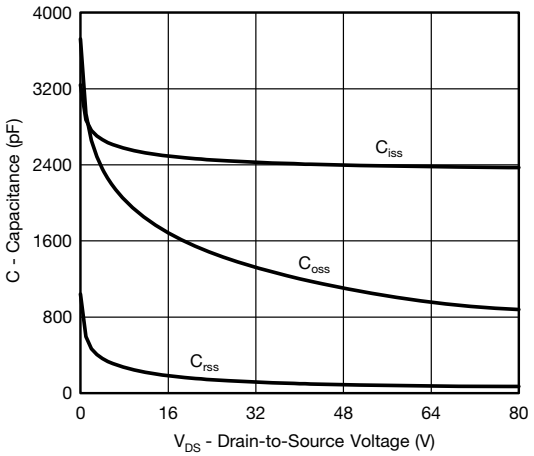
Output Characteristics



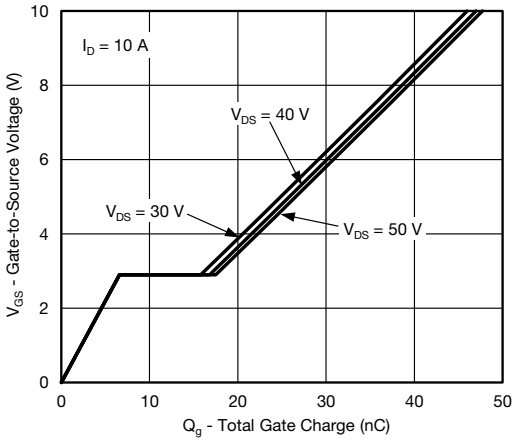
Transfer Characteristics



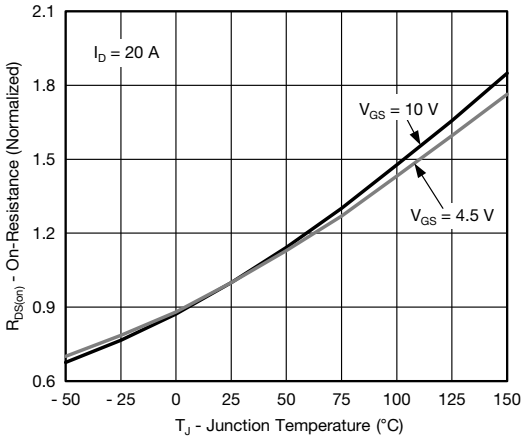
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

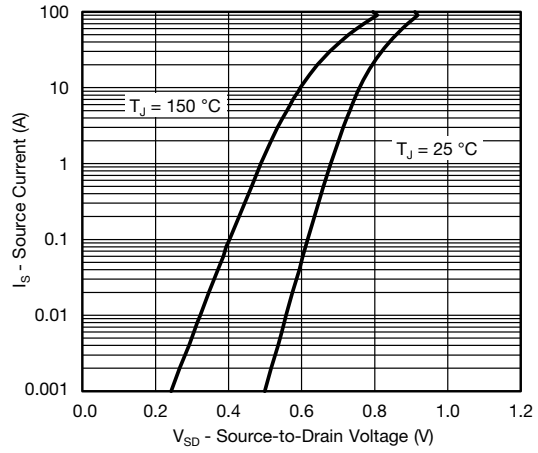


Gate Charge

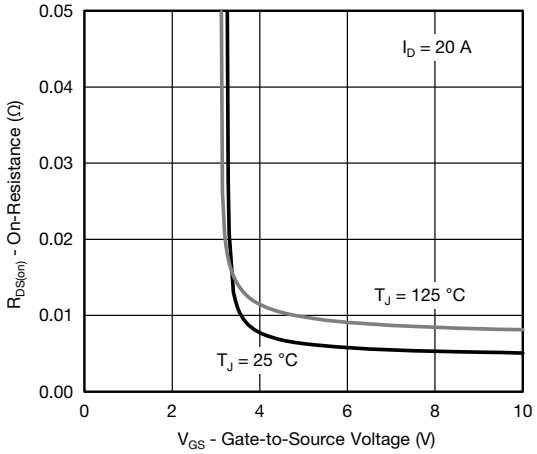


On-Resistance vs. Junction Temperature

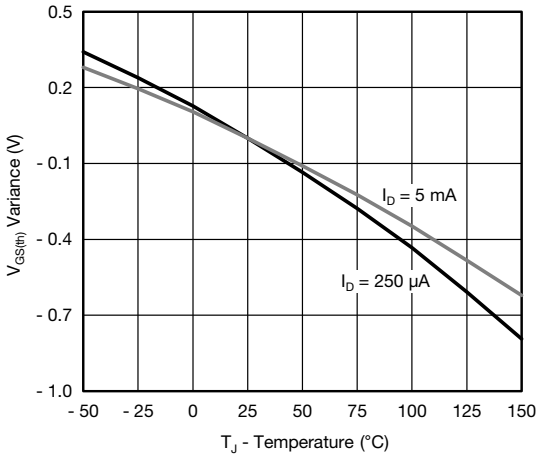
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



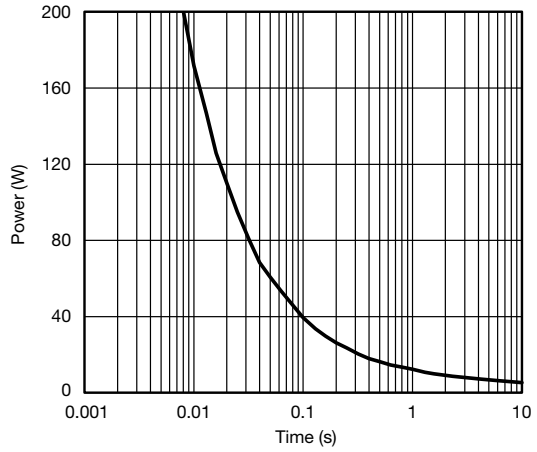
Source-Drain Diode Forward Voltage



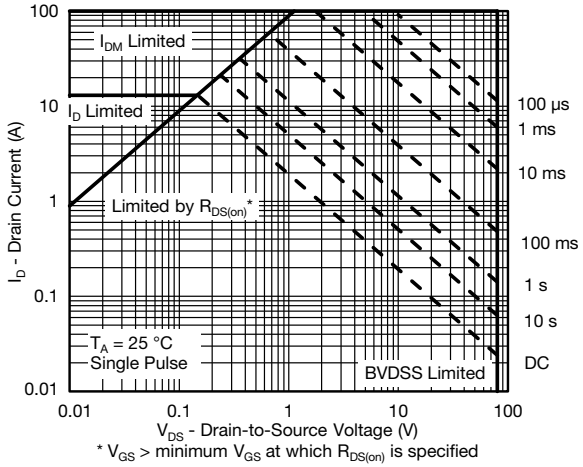
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

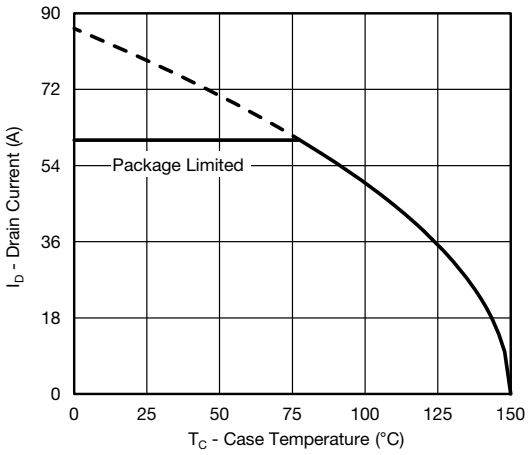


Single Pulse Power, Junction-to-Ambient

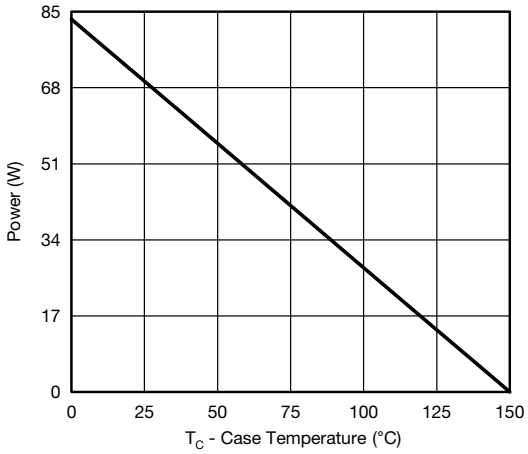


Safe Operating Area, Junction-to-Ambient

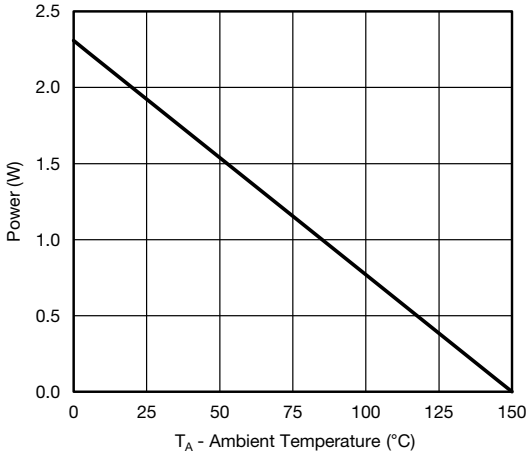
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating [a]



Power, Junction-to-Case

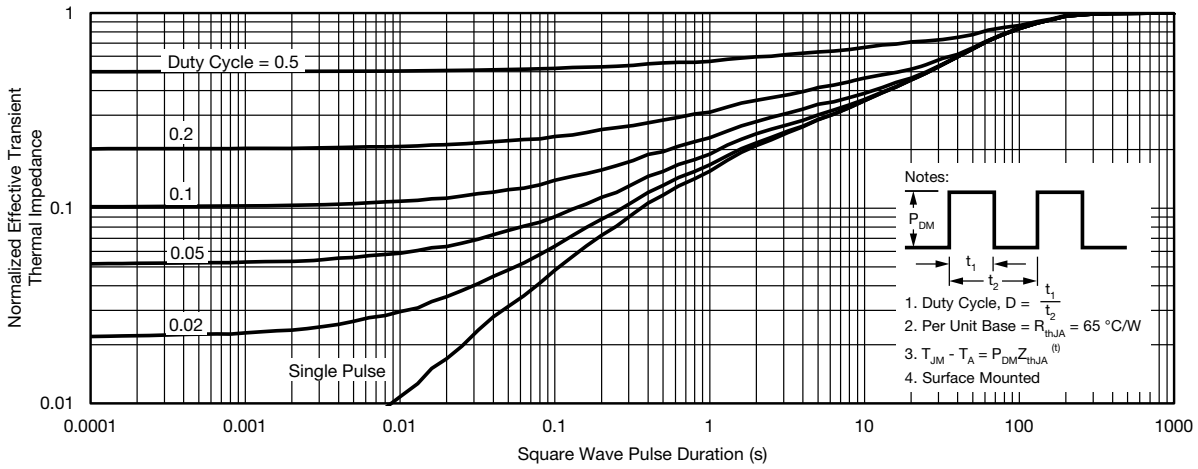


Power, Junction-to-Ambient

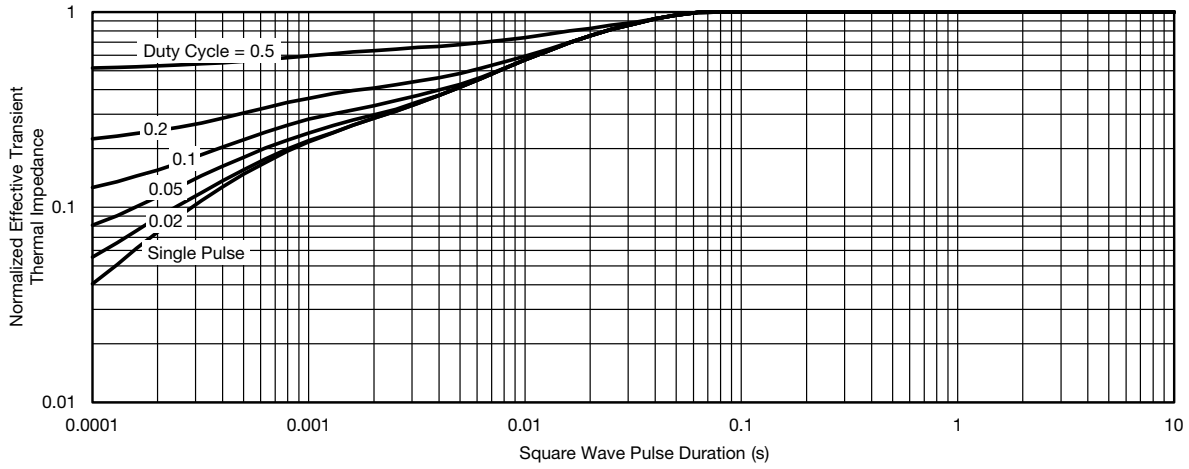
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



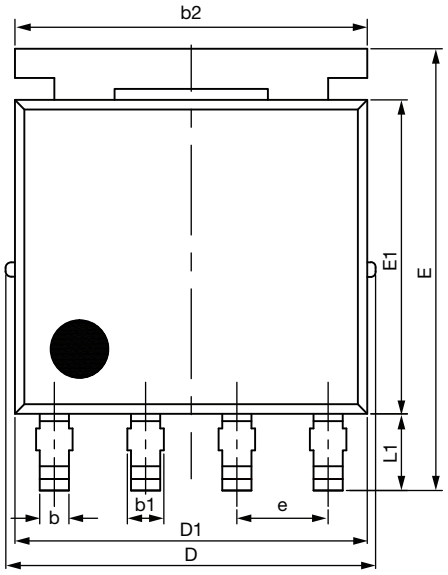
Normalized Thermal Transient Impedance, Junction-to-Ambient



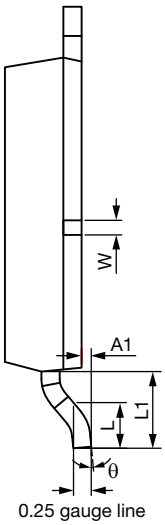
Normalized Thermal Transient Impedance, Junction-to-Case

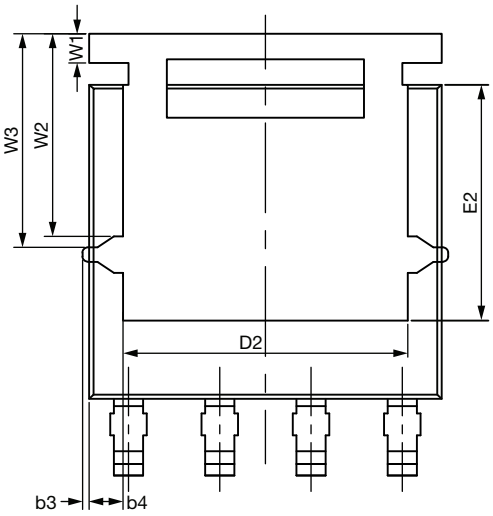
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63728.

PowerPAK® SO-8L Case Outline 1

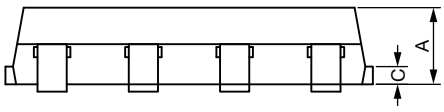


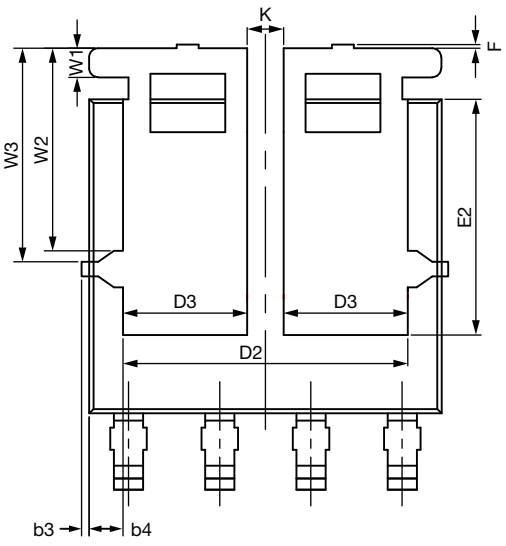
Topside view





Backside view (single)





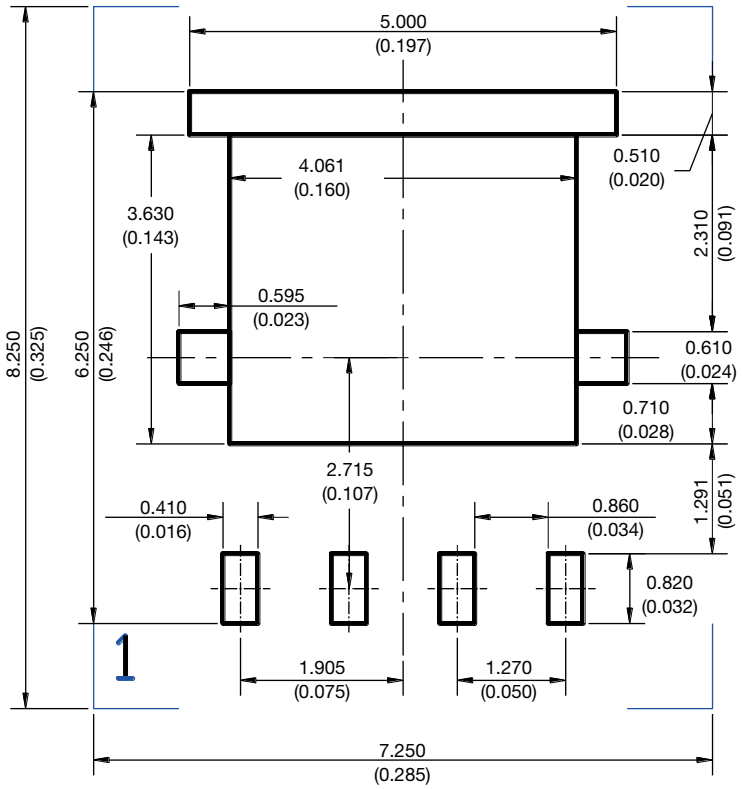
Backside view (dual)

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	1.00	1.07	1.14	0.039	0.042	0.045
A1	0.00	-	0.127	0.00	-	0.005
b	0.33	0.41	0.48	0.013	0.016	0.019
b1	0.44	0.51	0.58	0.017	0.020	0.023
b2	4.80	4.90	5.00	0.189	0.193	0.197
b3	0.094			0.004		
b4	0.47			0.019		
c	0.20	0.25	0.30	0.008	0.010	0.012
D	5.00	5.13	5.25	0.197	0.202	0.207
D1	4.80	4.90	5.00	0.189	0.193	0.197
D2	3.86	3.96	4.06	0.152	0.156	0.160
D3	1.63	1.73	1.83	0.064	0.068	0.072
e	1.27 BSC			0.050 BSC		
E	6.05	6.15	6.25	0.238	0.242	0.246
E1	4.27	4.37	4.47	0.168	0.172	0.176
E2	3.18	3.28	3.38	0.125	0.129	0.133
F	-	-	0.15	-	-	0.006
L	0.62	0.72	0.82	0.024	0.028	0.032
L1	0.92	1.07	1.22	0.036	0.042	0.048
K	0.51			0.020		
W	0.23			0.009		
W1	0.41			0.016		
W2	2.82			0.111		
W3	2.96			0.117		
θ	0°	-	10°	0°	-	10°
ECN: S19-0643-Rev. E, 05-Aug-2019 DWG: 5976						

Note

- Millimeters will gover

RECOMMENDED MINIMUM PAD FOR PowerPAK® SO-8L SINGLE



Recommended Minimum Pads
Dimensions in mm (inches)

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay’s knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer’s responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer’s technical experts. Product specifications do not expand or otherwise modify Vishay’s terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

单击下面可查看定价，库存，交付和生命周期等信息

>>Vishay(威世)

>>点击查看相关商品