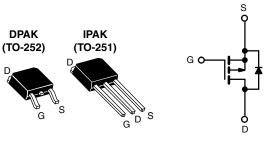
Vishay Siliconix

# **Power MOSFET**



P-Channel MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	-250			
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = -10 V 3.0			
Q <sub>g</sub> (Max.) (nC)	14			
Q <sub>gs</sub> (nC)	3.1			
Q <sub>gd</sub> (nC)	6.8			
Configuration	Single			

## **FEATURES**

- Advanced process technology
- Fully avalanche rated
- Surface-mount (IRFR9214, SiHFR9214)
- Straight lead (IRFU9214, SiHFU9214)
- P-channel
- · Fast switching
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



## **DESCRIPTION**

Third generation power MOSFETs from Vishay utilize advanced processing techniques to achieve low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The DPAK is designed for surface mounting using vapor phase, infrared, or wave soldering techniques. The straight lead version (IRFU, SiHFU series) is for through-hole mounting applications. Power dissipation levels up to 1.5 W are possible in typical surface-mount applications.

ORDERING INFORMATION						
Package	DPAK (TO-252)	DPAK (TO-252)	DPAK (TO-252)	IPAK (TO-251)		
Lead (Pb)-free and halogen-free	SiHFR9214-GE3	SiHFR9214TRL-GE3	SiHFR9214TR-GE3	SiHFU9214-GE3		
Lead (Pb)-free	IRFR9214PbF	IRFR9214TRLPbF <sup>a</sup>	IRFR9214TRPbF a	IRFU9214PbF		

## Note

a. See device orientation

ABSOLUTE MAXIMUM RATINGS ( $T_C$	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			$V_{DS}$	-250	V	
Gate-source voltage			$V_{GS}$	± 20	7 v	
Continuous drain current	V at 10 V	T <sub>C</sub> = 25 °C T <sub>C</sub> = 100 °C		-2.7		
Continuous drain current	V <sub>GS</sub> at -10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	-1.7	Α	
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	-11		
Linear derating factor				0.40	W/°C	
Single pulse avalanche energy b			E <sub>AS</sub>	100	mJ	
Repetitive avalanche current a			I <sub>AR</sub>	-2.7	А	
Repetitive avalanche energy a			E <sub>AR</sub>	5.0	mJ	
Maximum power dissipation $T_C = 25  ^{\circ}C$			$P_{D}$	50	W	
Peak diode recovery dV/dt <sup>c</sup>			dV/dt	-5.0	V/ns	
Operating junction and storage temperature range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) d	oldering recommendations (peak temperature) d For 10 s		_	260	°C	

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Starting T<sub>J</sub> = 25 °C, L = 27 mH, R<sub>g</sub> = 25  $\Omega$ , I<sub>AS</sub> = 2.7 A (see fig. 12)
- c.  $I_{SD} \le -2.7 \text{ A}$ ,  $dI/dt \le 600 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le V_{DS}$ ,  $T_{J} \le 150 \text{ °C}$
- d. 1.6 mm from case

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THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R <sub>thJA</sub>	-	-	110	
Maximum junction-to-ambient (PCB mount) <sup>a</sup>	R <sub>thJA</sub>	-	-	50	°C/W
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	-	2.5	

#### Note

a. When mounted on 1" square PCB (FR-4 or G-10 material)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		^			I.	I.	ı
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$		- 250	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = - 1 mA	-	- 0.25	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 2.0	-	- 4.0	V
Gate-source leakage	I <sub>GSS</sub>	,	$V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>		- 250 V, V <sub>GS</sub> = 0 V V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	- 100 - 500	μA
Drain-source on-state resistance	R <sub>DS(on)</sub>		I <sub>D</sub> = - 1.7 A <sup>b</sup>	-	-	3.0	Ω
Forward transconductance	9 <sub>fs</sub>		- 50 V, I <sub>D</sub> = - 1.7 A	0.9	-	-	S
Dynamic		^			I.	I.	ı
Input capacitance	C <sub>iss</sub>		$V_{GS} = 0 V$	-	220	-	
Output capacitance	C <sub>oss</sub>	1	$V_{DS} = -25 \text{ V},$	1	75	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1.	0 MHz, see fig. 5	-	11	-	
Total gate charge	Qq			-	-	14	
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = - 10 V	$V_{GS} = -10 \text{ V}$ $I_D = -1.7 \text{ A}, V_{DS} = -200 \text{ V},$		-	3.1	nC
Gate-drain charge	Q <sub>gd</sub>	see fig. 6 and 13 <sup>b</sup>		-	-	6.8	
Turn-on delay time	t <sub>d(on)</sub>			-	11	-	
Rise time	t <sub>r</sub>	$V_{DD} = -125 \text{ V}, I_D = -1.7 \text{ A}, R_g = 21 \Omega, R_D = 70 \Omega, \text{ see fig. } 10^b$		-	14	-	
Turn-off delay time	t <sub>d(off)</sub>			-	20	-	ns
Fall time	t <sub>f</sub>	1	1 1		17	-	
Internal drain inductance	L <sub>D</sub>	Between I 6 mm (0.25	) from	-	4.5	-	nH
Internal source inductance	L <sub>S</sub>	package and die conta	~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	-	7.5	-	11111
<b>Drain-Source Body Diode Characteristic</b>	es						
Continuous source-drain diode current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		=	-	- 2.7	^
Pulsed diode forward current a	I <sub>SM</sub>			-	-	- 11	A
Body diode voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C,	$I_S = -2.7 \text{ A}, V_{GS} = 0 \text{ V}^b$	-	-	- 5.8	V
Body diode reverse recovery time	t <sub>rr</sub>	T 05 00 1	17 A Al/At 100 A / - h	-	150	220	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	$-$ T <sub>J</sub> = 25 °C, I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/ $\mu$ s <sup>b</sup>		-	870	1300	nC
Forward turn-on time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> and L <sub>D</sub> )				Ln)	

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %

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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

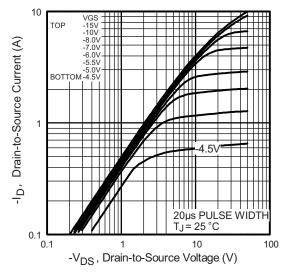


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

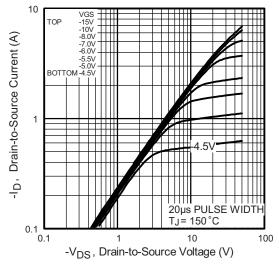


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 150 °C

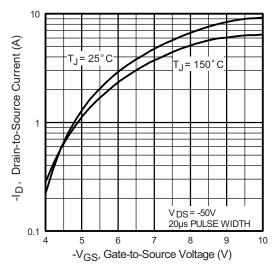


Fig. 2 - Typical Transfer Characteristics

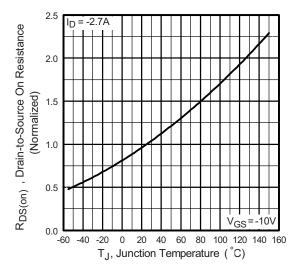


Fig. 3 - Normalized On-Resistance vs. Temperature

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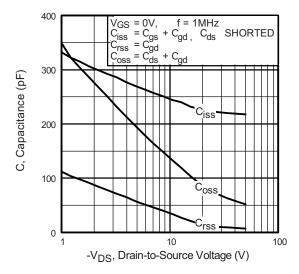


Fig. 4 - Typical Capacitance vs. Drain-to-Source Voltage

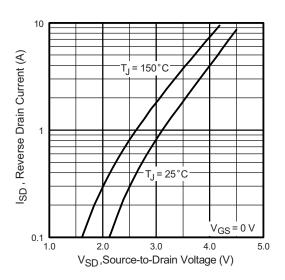


Fig. 6 - Typical Source-Drain Diode Forward Voltage

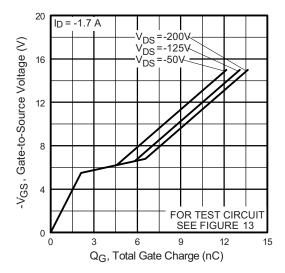


Fig. 5 - Typical Gate Charge vs. Gate-to-Source Voltage

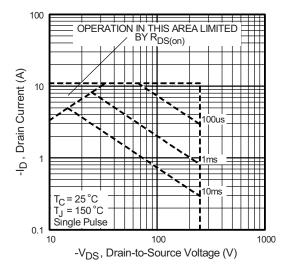


Fig. 7 - Maximum Safe Operating Area

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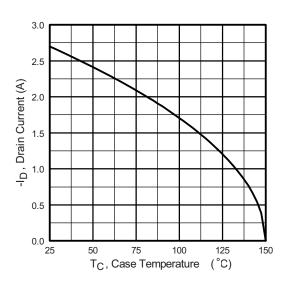


Fig. 8 - Maximum Drain Current vs. Case Temperature

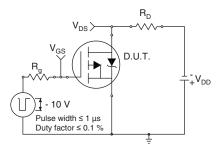


Fig. 10a - Switching Time Test Circuit

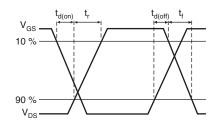


Fig. 10b - Switching Time Waveforms

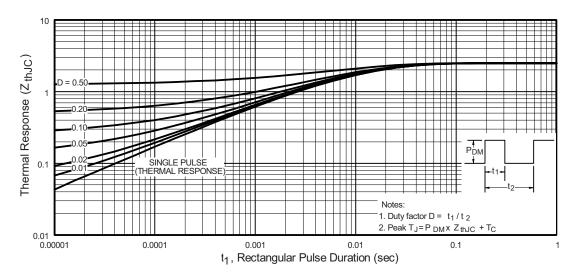
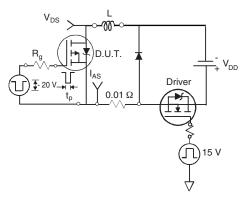


Fig. 9 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

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TAS — — t<sub>p</sub> V<sub>DS</sub>

Fig. 12a - Unclamped Inductive Test Circuit

Fig. 12b - Unclamped Inductive Waveforms

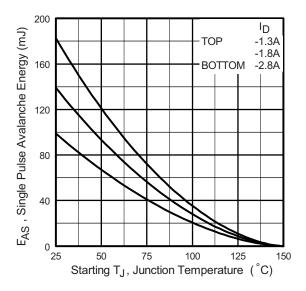


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

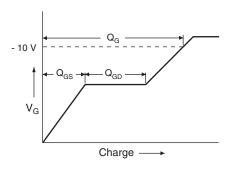


Fig. 13a - Basic Gate Charge Waveform

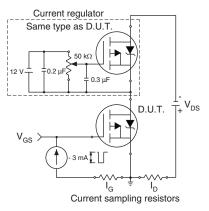
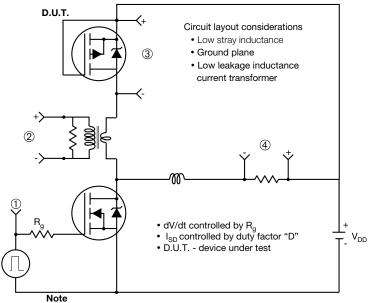


Fig. 13b - Gate Charge Test Circuit

S21-0373-Rev. E, 19-Apr-2021

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#### Peak Diode Recovery dV/dt Test Circuit



• Compliment N-Channel of D.U.T. for driver

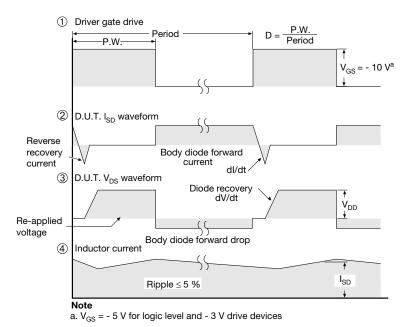


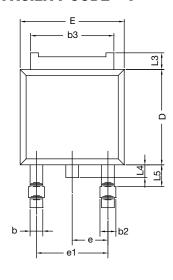
Fig. 10 - For P-Channel

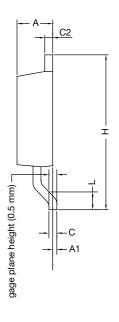
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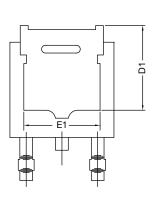


# **TO-252AA Case Outline**

## **VERSION 1: FACILITY CODE = Y**







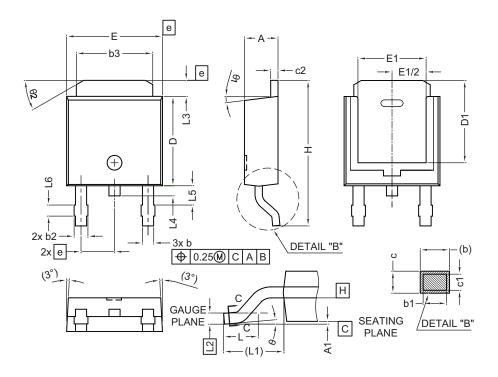
	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
Е	6.35	6.73	
E1	4.32	=	
Н	9.40	10.41	
е	2.28	BSC	
e1	4.56 BSC		
L	1.40	1.78	
L3	0.89	1.27	
L4	-	1.02	
L5	1.01	1.52	

#### Note

• Dimension L3 is for reference only



## **VERSION 2: FACILITY CODE = N**



	MILLIMETERS			
DIM.	MIN.	MAX.		
Α	2.18	2.39		
A1	-	0.13		
b	0.65	0.89		
b1	0.64	0.79		
b2	0.76	1.13		
b3	4.95	5.46		
С	0.46	0.61		
c1	0.41	0.56		
c2	0.46	0.60		
D	5.97	6.22		
D1	5.21	-		
E	6.35	6.73		
E1	4.32	-		
е	2.29 BSC			
Н	9.94	10.34		

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74	ref.	
L2	0.51	BSC	
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

## Notes

- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

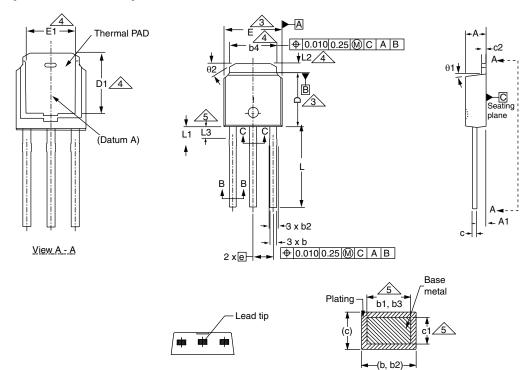
ECN: E19-0649-Rev. Q, 16-Dec-2019

DWG: 5347

Revision: 16-Dec-2019



## **TO-251AA (HIGH VOLTAGE)**



	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
Α	2.18	2.39	0.086	0.094
A1	0.89	1.14	0.035	0.045
b	0.64	0.89	0.025	0.035
b1	0.65	0.79	0.026	0.031
b2	0.76	1.14	0.030	0.045
b3	0.76	1.04	0.030	0.041
b4	4.95	5.46	0.195	0.215
С	0.46	0.61	0.018	0.024
c1	0.41	0.56	0.016	0.022
c2	0.46	0.86	0.018	0.034
D	5.97	6.22	0.235	0.245

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
D1	5.21	-	0.205	-
Е	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
е	2.29	BSC	2.29 BSC	
L	8.89	9.65	0.350	0.380
L1	1.91	2.29	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.14	1.52	0.045	0.060
θ1	0'	15'	0'	15'
θ2	25'	35'	25'	35'

Section B - B and C - C

ECN: S-82111-Rev. A, 15-Sep-08

DWG: 5968

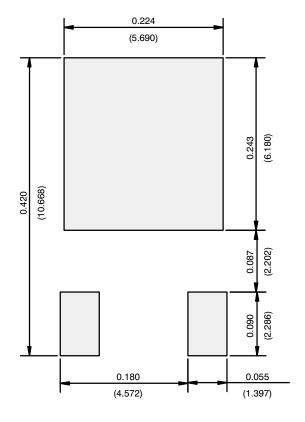
#### Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimension are shown in inches and millimeters.
- 3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.13 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body.
- 4. Thermal pad contour optional with dimensions b4, L2, E1 and D1.
- 5. Lead dimension uncontrolled in L3.
- 6. Dimension b1, b3 and c1 apply to base metal only.
- 7. Outline conforms to JEDEC outline TO-251AA.

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Revision: 15-Sep-08 1



## **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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