

0.45- Ω CMOS, 1.65-V to 3.6-V, Dual DPDT Analog Switch

FEATURES

BENEFITS

- Low Voltage Operation (1.65 V to 3.6 V)
- Low On-Resistance r_{ON}: 0.45 Ω @ 2.7 V
- Fast Switching: t_{ON} = 28 ns
- t_{OFF} = 17 ns

• QFN-16 (3x3) Package

Reduced Power Consumption

- Heduced Fower Const
 High Accuracy
- Reduce Decod One
- Reduce Board Space
- TTL/1.8-V Logic Compatible
- High Bandwidth

APPLICATIONS

- Cellular Phones
- Speaker Headset Switching
- Audio and Video Signal Routing
- PCMCIA Cards
- Battery Operated Systems

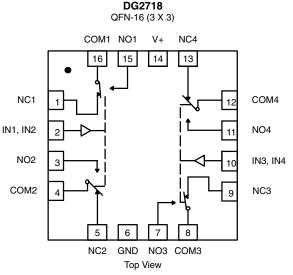
DESCRIPTION

The DG2718 is a dual double-pole/double-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed, low on-resistance and small physical size, the DG2718 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2718 is built on Vishay Siliconix's low voltage process. An epitaxial layer prevents latchup. Break-before-make is guaranteed.

The switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



TRUTH TABLE					
Logic	NC1, 2, 3 and 4	NO1, 2, 3 and 4			
0	ON	OFF			
1	OFF	ON			

ORDERING INFORMATION*					
Temp Range	Package	Part Number			
-40 to 85°C	16-Pin QFN (3 x 3 mm) Variation 2	DG2718DN-T1-E4			
* Lead-Free Version Available					

Underside exposed pad has no device electrical connection. It is recommended that no electrical connection is made to it.

NOTE:

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ABSOLUTE MAXIMUM RATINGS

Reference to GND

V+).3 to +4.0 V
IN, COM, NC, NO ^a	(V+ + 0.3 V)
Current (Any terminal except NO, NC or COM) 30 mA	30 mA
Continuous Current (NO, NC, or COM) ± 300 mA	\pm 300 mA
Peak Current ±500 mA	$\pm 500 \text{ mA}$
(Pulsed at 1 ms, 10% duty cycle)	
Storage Temperature (D Suffix)65 to 150°C	65 to 150°C
Package Solder Reflow Conditions ^d	
16-Pin QFN (3 x 3 mm) 250°C	250°C
Power Dissipation (Packages) ^b	

Notes:

a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by

- internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC Board.
- c. Derate 17.3 mW/ $^{\circ}$ C above 70 $^{\circ}$ C
- d. Manual soldering with iron is not recommended for leadless components. The QFN is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper lip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameter	Symbol	Test Conditions Otherwise Unless Specified V+ = 1.8 V, V _{IN} = 0.4 or 1.1 V ^e	Temp ^a	Limits -40 to 85°C			
				Min ^b	Typ ^c	Max ^b	Unit
Analog Switch							
Analog Signal Range ^d	V _{NO} , V _{NC} , V _{COM}		Full	0		V+	V
On-Resistance ^d	r _{ON}	V+ = 1.8 V, V _{COM} = 0.2 V/0.9 V, I _{NO} , I _{NC} = 100 mA	Room Full		0.7	2.0 2.8	Ω
Digital Control							
Input High Voltage	V _{INH}		Full	1.1			
Input Low Voltage	V _{INL}		Full			0.4	v
Input Capacitance	C _{in}		Full		6		pF
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 or V+	Full	-1		1	μA
Dynamic Characteristic	s						
Turn-On Time	t _{ON}	$V_{NO} \text{ or } V_{NC}$ = 1.5 V, R_L = 50 Ω, C_L = 35 pF	Room Full		62	94 92	ns
Turn-Off Time	tOFF		Room Full		24	52 55	
Break-Before-Make Time	t _d		Full	16			
Charge Injectiond	Q _{INJ}	C_L = 1 nF, V_{GEN} = 0 V, R_{GEN} = 0 Ω	Room		65		рС
Off-Isolation ^d	OIRR		Room		-74		dB
Crosstalk ^d	X _{TALK}	$R_L = 50 \Omega$, $C_L = 5 pF$, f = 100 kHz	Room		-74		
N _O , N _C Off Capacitance ^d	C _{NO(off)}	V _{IN} = 0 or V+, f = 1 MHz	Room		108		pF
	C _{NC(off)}		Room		108		
Channel-On Capacitanced	C _{NO(on)}		Room		225		
	C _{NC(on)}		Room		225		
Power Supply							
Power Supply Current	1+	V _{IN} = 0 or V+	Full	1	1	1.0	μA



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SPECIFICATIONS (V+ = 3 V)							
Parameter		Test Conditions Otherwise Unless Specified V+ = 3 V, \pm 10%, V _{IN} = 0.5 or 1.4 V ^e	Temp ^a	Limits -40 to 85°C			
	Symbol			Min ^b	Турс	Max ^b	Unit
Analog Switch					•		
Analog Signal Range ^d	V _{NO} , V _{NC} , V _{COM}		Full	0		V+	V
On-Resistance ^d	r _{ON}	V+ = 2.7 V, V _{COM} = 0.2 V/1.5 V, I _{NO} , I _{NC} = 100 mA	Room Full		0.45	0.6 0.7	Ω
r _{ON} Flatness ^d	r _{ON} Flatness	V+=2.7 V	Room		0.1	0.15	
r _{ON} Match ^d	Δr_{ON}	$V_{COM} = 0$ to V+, I_{NO} , $I_{NC} = 100$ mA	Room		0.05		l
Switch Off Leakage Current	I _{NO(off)} , I _{NC(off)}	V+ = 3.3 V, V _{NO} , V _{NC} = 0.3 V/3 V	Room Full	-1 -10		1 10	
Switch On Leakage Ourient	I _{COM(off)}	V _{COM} = 3 V/0.3 V	Room Full	-1 -10		1 10	nA
Channel-On Leakage Current	I _{COM(on)}	V+ = 3.3 V, V _{NO} , V _{NC} = V _{COM} = 0.3 V/3 V	Room Full	-1 -10		1 10	
Digital Control							
Input High Voltage	V _{INH}		Full	1.4			
Input Low Voltage	V _{INL}		Full			0.5	V
Input Capacitance	C _{in}		Full		6		pF
Input Current	I _{INL} or I _{INH}	V _{IN} = 0 or V+	Full	-1		1	μΑ
Dynamic Characteristics	S						
Turn-On Time	t _{ON}		Room Full		28	57 60	ns
Turn-Off Time	t _{OFF}	$V_{NO}~\text{or}~V_{NC}$ = 1.5 V, R_L = 50 $\Omega,~C_L$ = 35 pF	Room Full		17	45 47	
Break-Before-Make Time	t _d		Full	1			ĺ
Charge Injectiond	Q _{INJ}	C_L = 1 nF, V_{GEN} = 0 V, R_{GEN} = 0 Ω	Room		232		рС
Off-Isolation ^d	OIRR		Room		-75		dB
Crosstalk ^d	X _{TALK}	$R_L = 50 \Omega$, $C_L = 5 pF$, $f = 100 kHz$	Room		-75		
N N Off Consolitoneed	C _{NO(off)}		Room		102		- pF
N _O , N _C Off Capacitance ^d	C _{NC(off)}	- V _{IN} = 0 or V+, f = 1 MHz	Room		102		
Channel-On Capacitance ^d	C _{NO(on)}		Room		234		
	C _{NC(on)}		Room		234		
Power Supply							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	V _{IN} = 0 or V+	Full			1.0	μΑ
			•				

Notes:

a.

Room = 25°C, Full = as determined by the operating suffix. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet. Typical values are for design aid only, not guaranteed nor subject to production testing. Guarantee by design, nor subjected to production test. b.

c. d.

e. f. $V_{IN} = \text{input voltage to perform proper function.} \\ \text{Guaranteed by 5-V leakage testing, not production tested.} \\$

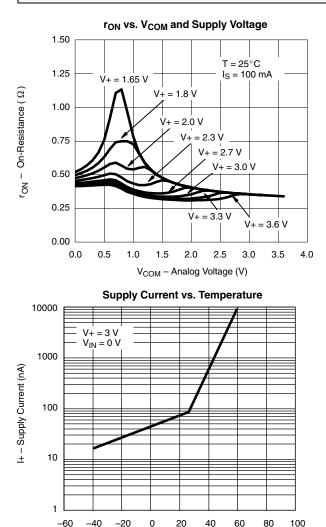
DG2718

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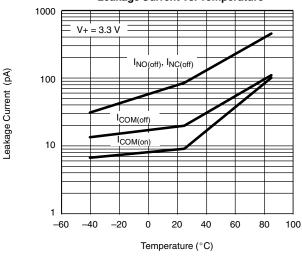


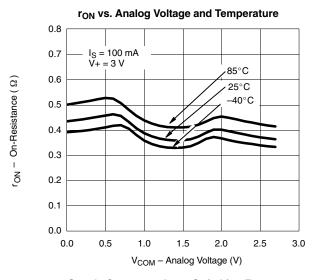
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

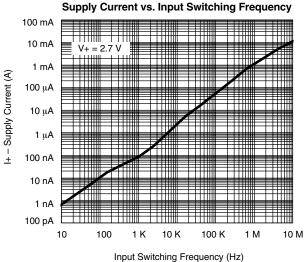


Temperature (°C)

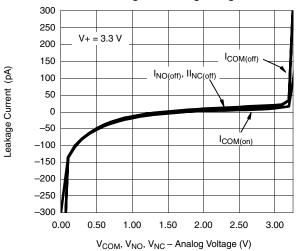
Leakage Current vs. Temperature







Leakage vs. Analog Voltage

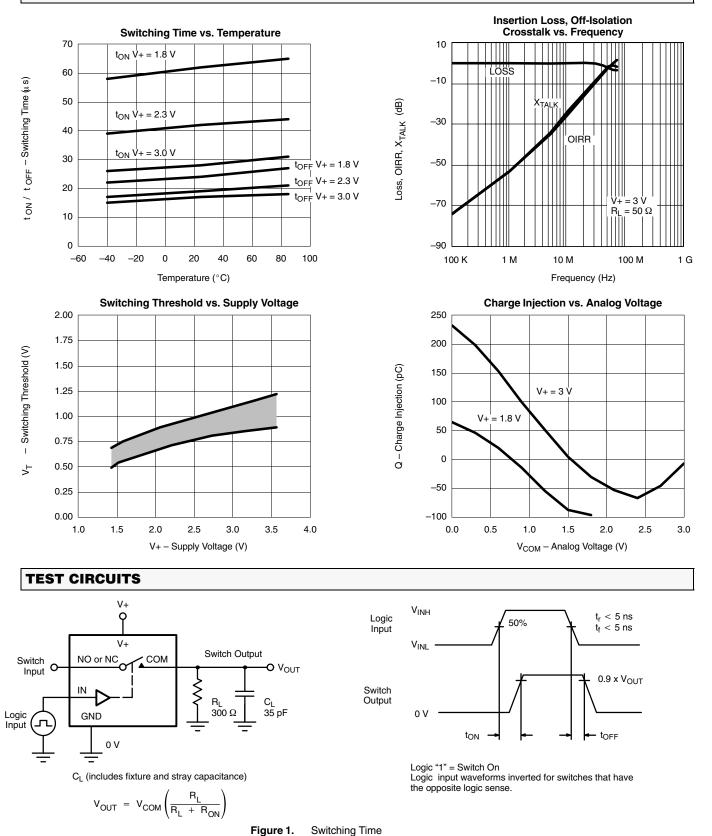




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DG2718 Vishay Siliconix

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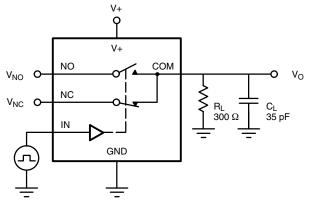
DG2718

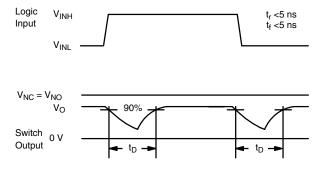
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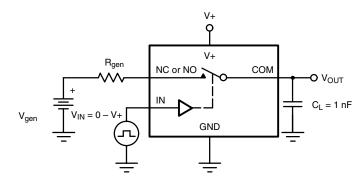
TEST CIRCUITS

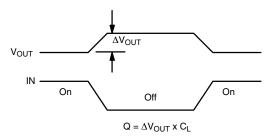




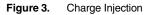
C_L (includes fixture and stray capacitance)

Figure 2. Break-Before-Make Interval





IN depends on switch configuration: input polarity determined by sense of switch.



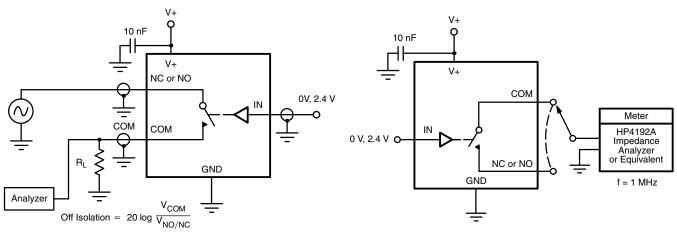


Figure 5. Channel Off/On Capacitance



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