

Vishay Siliconix

Dual P-Channel 1.8-V (G-S) MOSFET

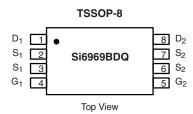
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
	0.030 at V _{GS} = - 4.5 V	- 4.6			
- 12	0.040 at V _{GS} = - 2.5 V	- 3.8			
	0.055 at V _{GS} = - 1.8 V	- 3.0			

FEATURES

- Halogen-free Option Available
- TrenchFET[®] Power MOSFETs

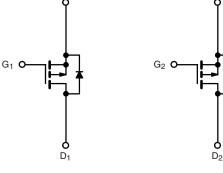
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Si6969BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)

Ordering Information: Si6969BDQ-T1



P-Channel MOSFET

P-Channel MOSFET

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ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 12		V
Gate-Source Voltage		V _{GS}	± 8		
	T _A = 25 °C	I _D	- 4.6	- 4.0	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 3.8	- 3.2	
Pulsed Drain Current (10 µs Pulse Width)		I _{DM}	- 30		A
Continuous Source Current (Diode Conduction) ^a		ا _S	- 1.0	- 0.7	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.14	0.83	w
	T _A = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum langting to Angling 12	t ≤ 10 s	R _{thJA}	88	110	
Maximum Junction-to-Ambient ^a	Steady State		120	150	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	65	80	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Test Conditions Min.		Max.	Unit		
Static			•					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.45		- 0.8	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}$			- 1			
		V_{DS} = - 9.6 V, V_{GS} = 0 V, T_{J} = 70 °C			- 25	μA		
On-State Drain Current ^a	I _{D(on)}	V_{DS} - 8 V, V_{GS} = - 4.5 V	- 30			А		
Drain-Source On-State Resistance ^a		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.6 \text{ A}$		0.024	0.030	Ω		
	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -3.8 \text{ A}$		0.031	0.040			
		$V_{GS} = -1.8 \text{ V}, \text{ I}_{D} = -3.0 \text{ A}$		0.044	0.055			
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -8 V, I_{D} = -4.6 A$		18		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.25 A, V _{GS} = 0 V		- 0.68	- 1.1	V		
Dynamic ^b								
Total Gate Charge	Qg			16.5	25	nC		
Gate-Source Charge	Q _{gs}	V_{DS} = - 6 V, V_{GS} = - 4.5 V, I_D = - 4.6 A		2				
Gate-Drain Charge	Q _{gd}			4.7		1		
Turn-On Delay Time	t _{d(on)}			20	40			
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		35	60	ns		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1.0 A, V_GEN = - 4.5 V, R_G = 6 Ω		110	180			
Fall Time	t _f			90	150			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.25 A, dl/dt = 100 A/μs		100	200			

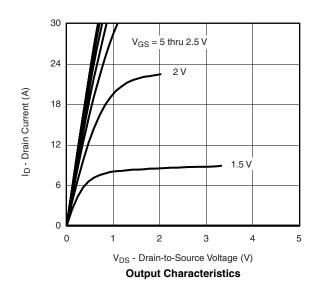
Notes:

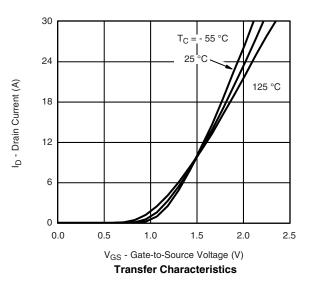
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

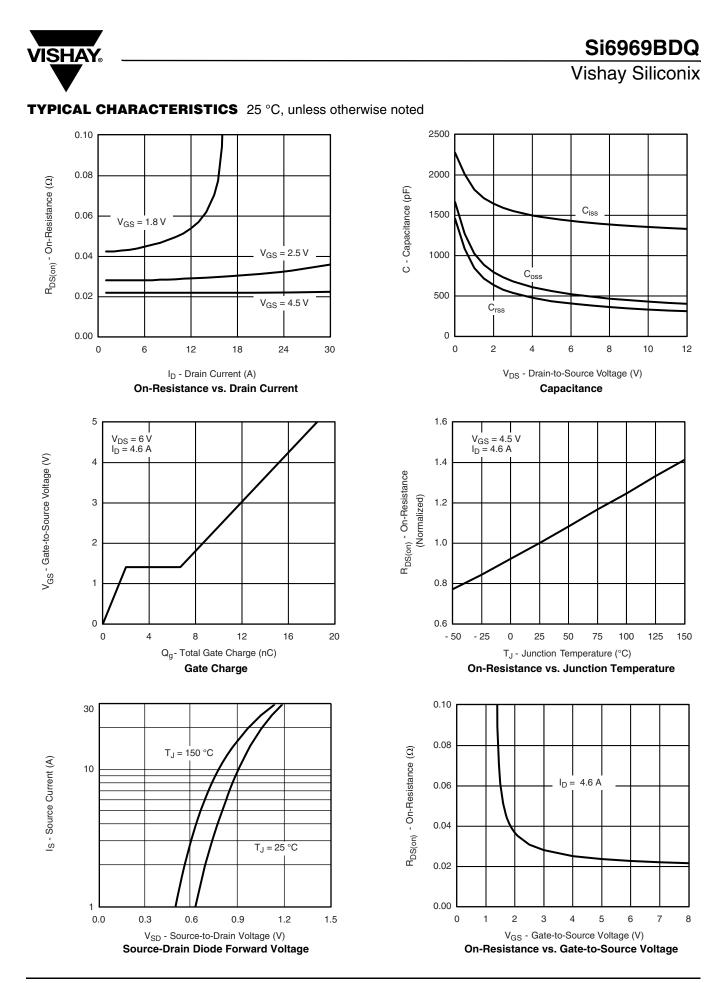
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



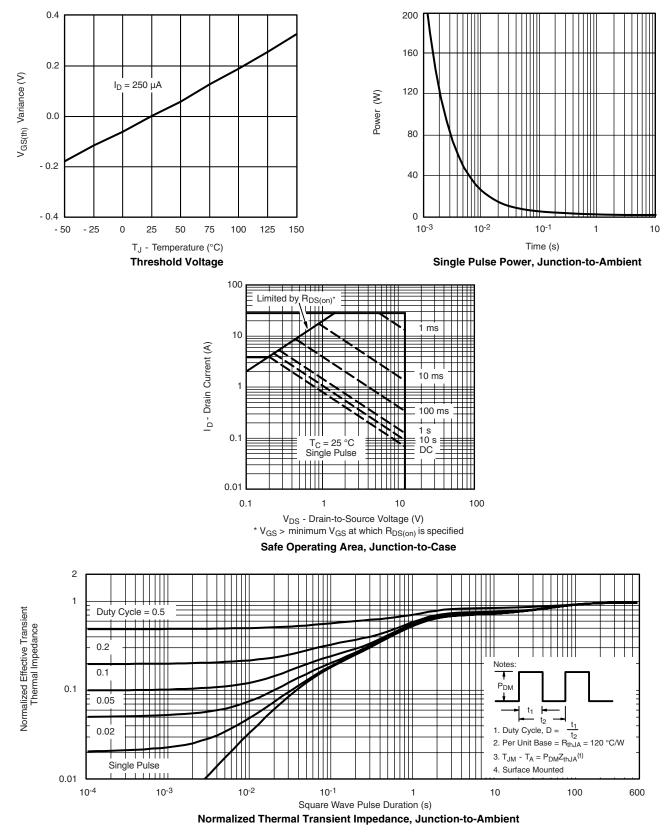




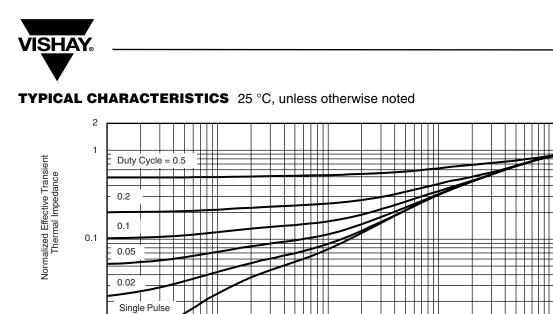
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot 1

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