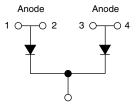
VS-UFB310CB40

Vishay Semiconductors





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Base common cathode

PRIMARY CHARACTERISTICS							
V _R	400 V						
$I_{F(AV)}$ at T_C = 119 °C per module ⁽¹⁾	310 A						
t _{rr}	39 ns						
at T _C	135 °C						
Туре	Modules - diode, FRED Pt®						
Package	SOT-227						

Note

⁽¹⁾ All 4 anode terminals connected

FEATURES

- Not insulated package
- Ultrafast reverse recovery
- Ultrasoft reverse recovery current shape
- Optimized for power conversion: welding and industrial SMPS applications
- Plug-in compatible with other SOT-227 packages
- · Easy to assemble
- · Direct mounting to heatsink
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-UFB310CB40 not insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The planar structure of the diodes, and the platinum doping life time control, provide a ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V _R		400	V			
Continuous forward current per diode	١ _F	T _C = 135 °C	155	٨			
Single pulse forward current per diode	I _{FSM} ⁽¹⁾	T _C = 25 °C	1300	A			
Maximum power dissipation per module	PD	T _C = 135 °C	421	W			
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C			

Note

⁽¹⁾ 10 ms sine or 6 ms rectangular pulse

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COMPLIANT



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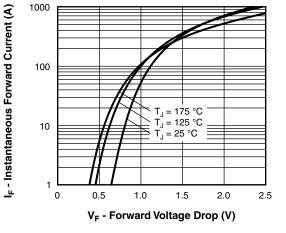
ELECTRICAL SPECIFICATIONS PER DIODE (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	BOL TEST CONDITIONS		TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	400	-	-		
		I _F = 100 A	-	1.11	1.34		
	V _{FM}	I _F = 100 A, T _J = 125 °C	-	0.99	1.1	v	
Forward voltage, per leg		I _F = 100 A, T _J = 175 °C	-	0.97	-		
		I _F = 200 A	-	1.3	1.6		
		I _F = 200 A, T _J = 125 °C	-	1.22	1.4		
		I _F = 200 A, T _J = 175 °C	-	1.25	-		
		$V_{R} = V_{R}$ rated	-	1.3	50		
Reverse leakage current, per leg	I _{RM}	$V_R = V_R$ rated, $T_J = 125 \text{ °C}$	-	100	-	μA	
		$V_R = V_R$ rated, $T_J = 175 \text{ °C}$	-	1	4	mA	
Junction capacitance, per leg	CT	V _R = 400 V	-	100	-	pF	

DYNAMIC RECOVERY CHARACTERISTICS PER DIODE (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 400 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	39	-	
Reverse recovery time, per leg t _{rr}		T _J = 25 °C		-	89	-	ns
		T _J = 125 °C		-	184	-	
		T _J = 25 °C	I _F = 50 A	-	9	-	А
Peak recovery current, per leg	I _{RRM}	T _J = 125 °C	dI _F /dt = 200 A/µs V _R = 200 V	-	20	-	A
	Q _{rr}	T _J = 25 °C		-	400	-	nC
Reverse recovery charge, per leg		T _J = 125 °C		-	1840	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C	
Junction to case, single leg conducting	Р		-	-	0.19		
Junction to case, both leg conducting	R _{thJC}		-	-	0.095	°C/W	
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.07	-		
Weight			-	30	-	g	
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)	
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)	
Case style			SOT-227 not insulated				

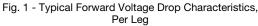
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SHAY



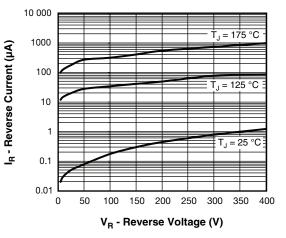
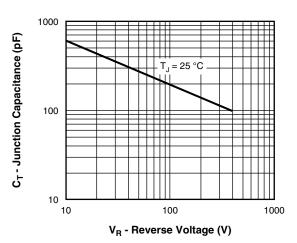
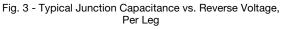


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage, Per Leg





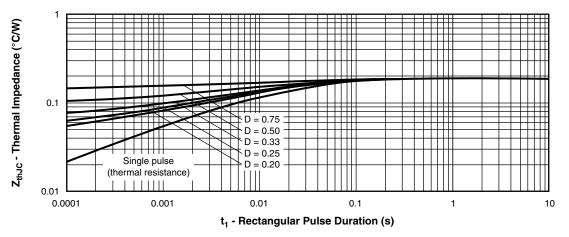


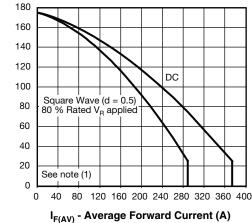
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics, Per Leg

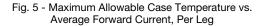
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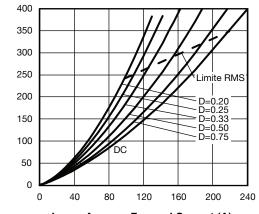
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I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics, Per Leg

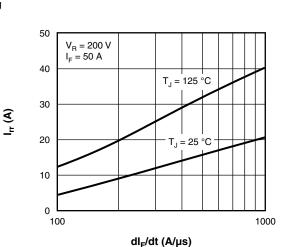


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt, Per Leg

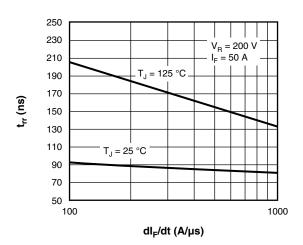


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt, Per Leg

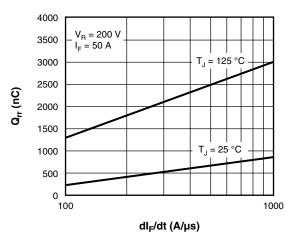


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt, Per Leg

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Allowable Case Temperature (°C)

Average Power Loss (W)

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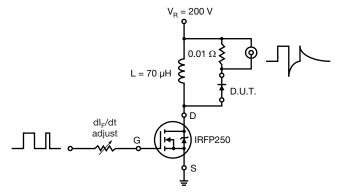


Fig. 10 - Reverse Recovery Parameter Test Circuit

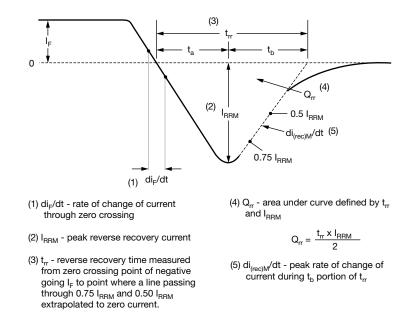


Fig. 11 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code	VS-	UF	В	310	С	В	40
		2	3	4	5	6	(7)
	1 - Vishay Semiconductors product						
	2 -	- Ultrafast rectifier					
	3 -	Ultrafast Pt diffused					
	4 -	- Current rating (310 = 310 A)					
	5 -	- Circuit configuration (two diodes common cathode)					
	6 -	Package indicator (SOT-227 standard not insulated					
	7 -	Voltage rating (40 = 400 V)					

Quantity per tube is 10 pcs, M4 screw and washer included

CIRCUIT CONFIGURATION							
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING					
Two diodes common cathode	С	Lead Assignment					

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95423					
Packaging Information	www.vishay.com/doc?95425					

Vishay Semiconductors



SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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